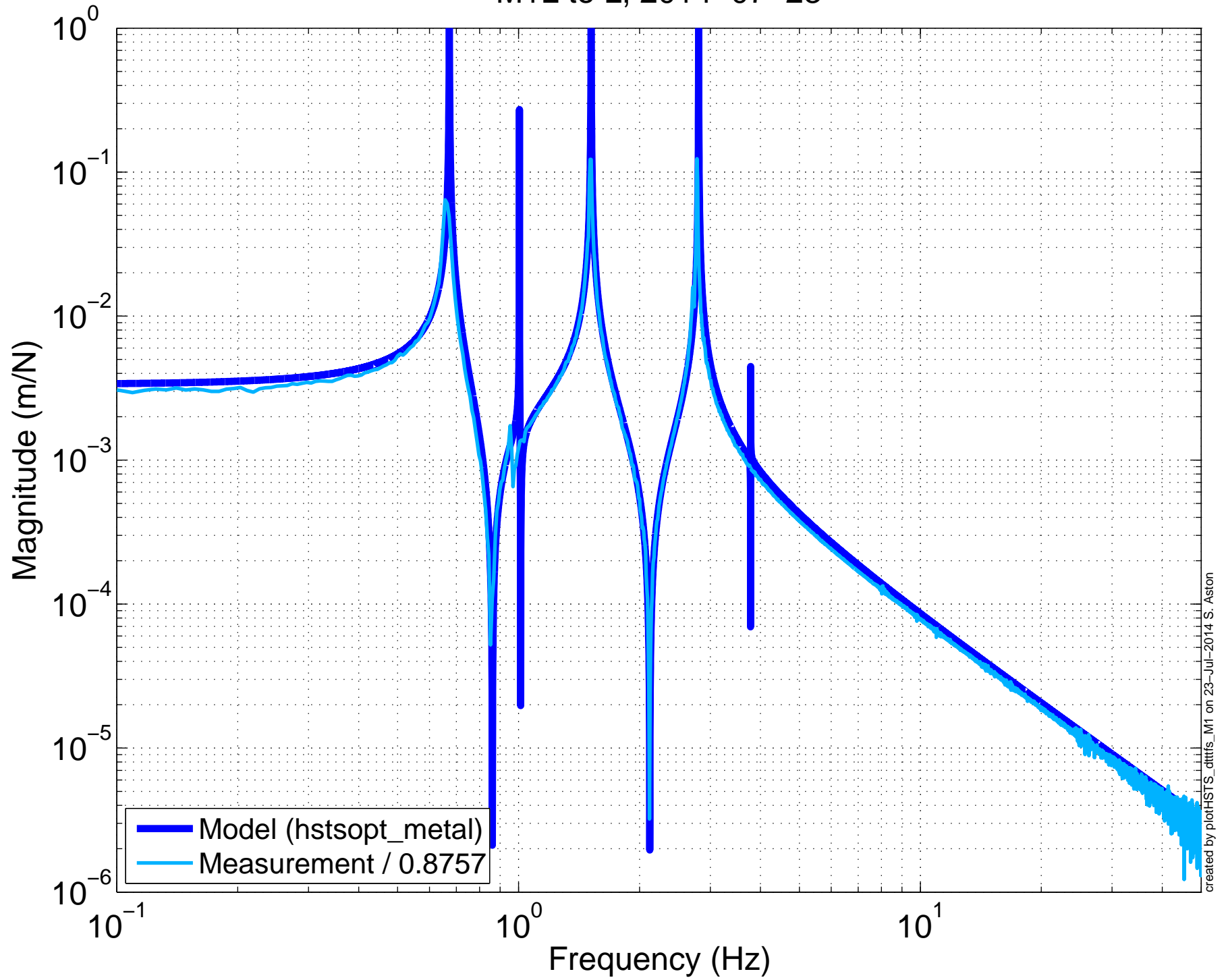


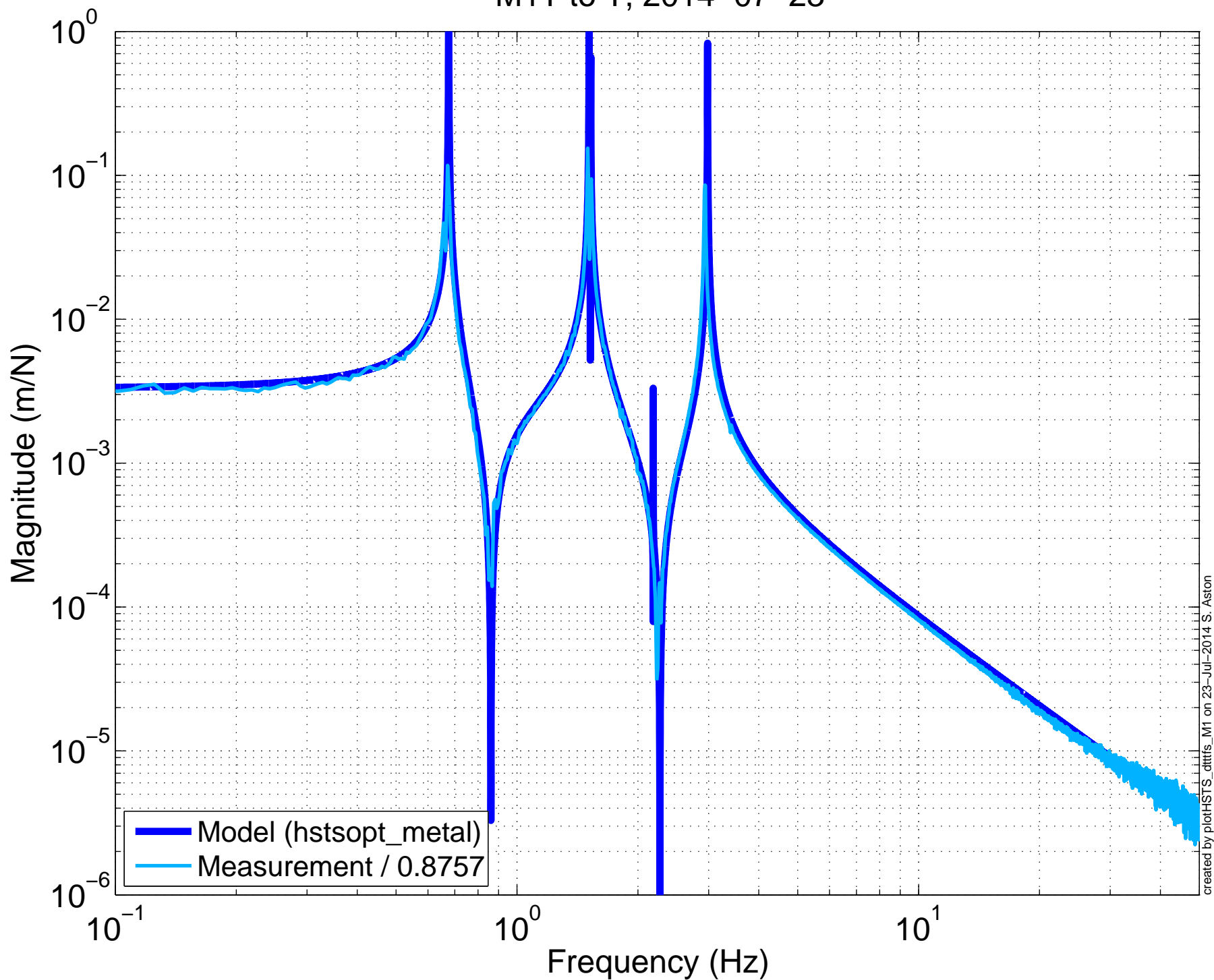
H1SUSSR2 BUILD

M1L to L, 2014-07-23



H1SUSSR2 BUILD

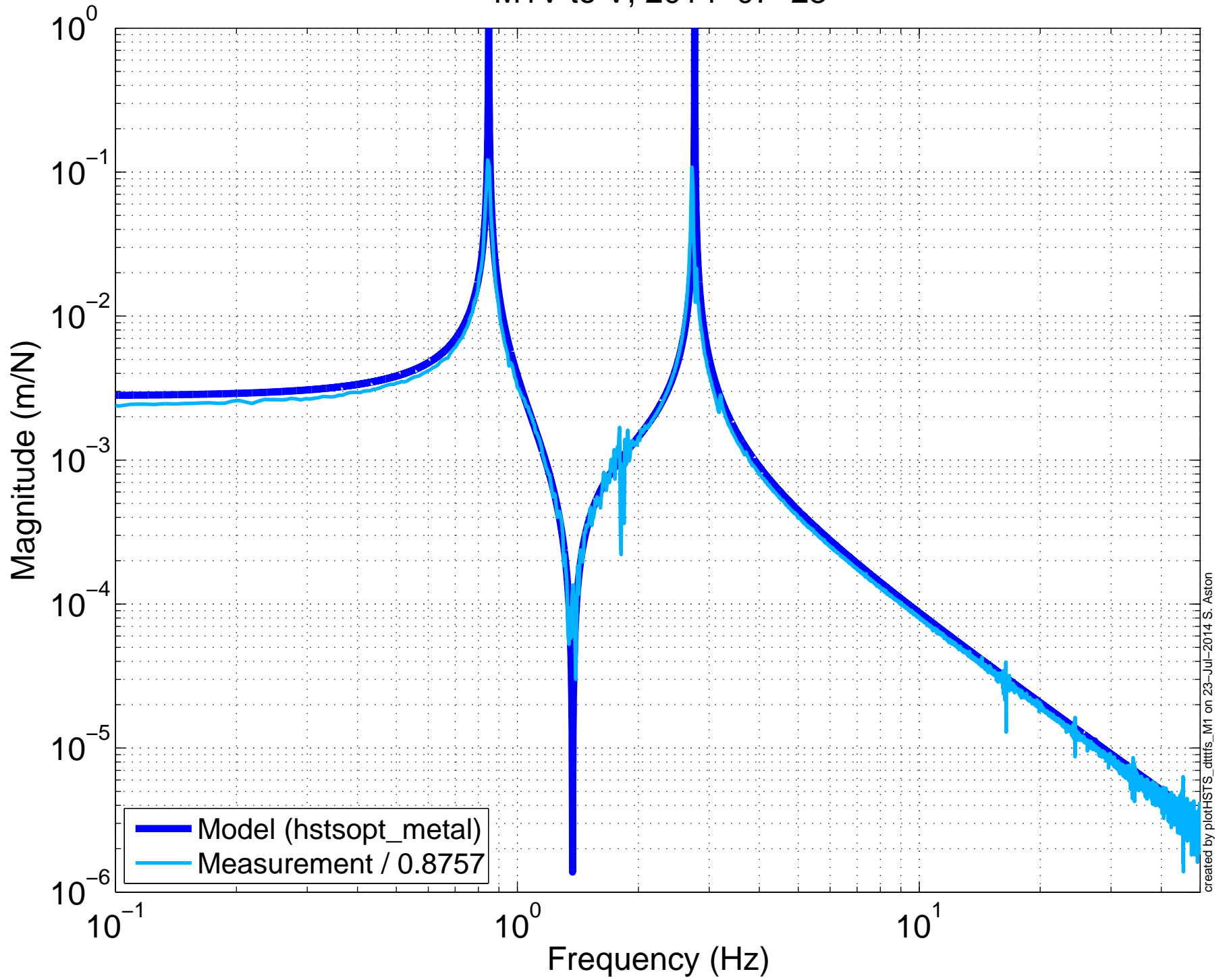
M1T to T, 2014-07-23



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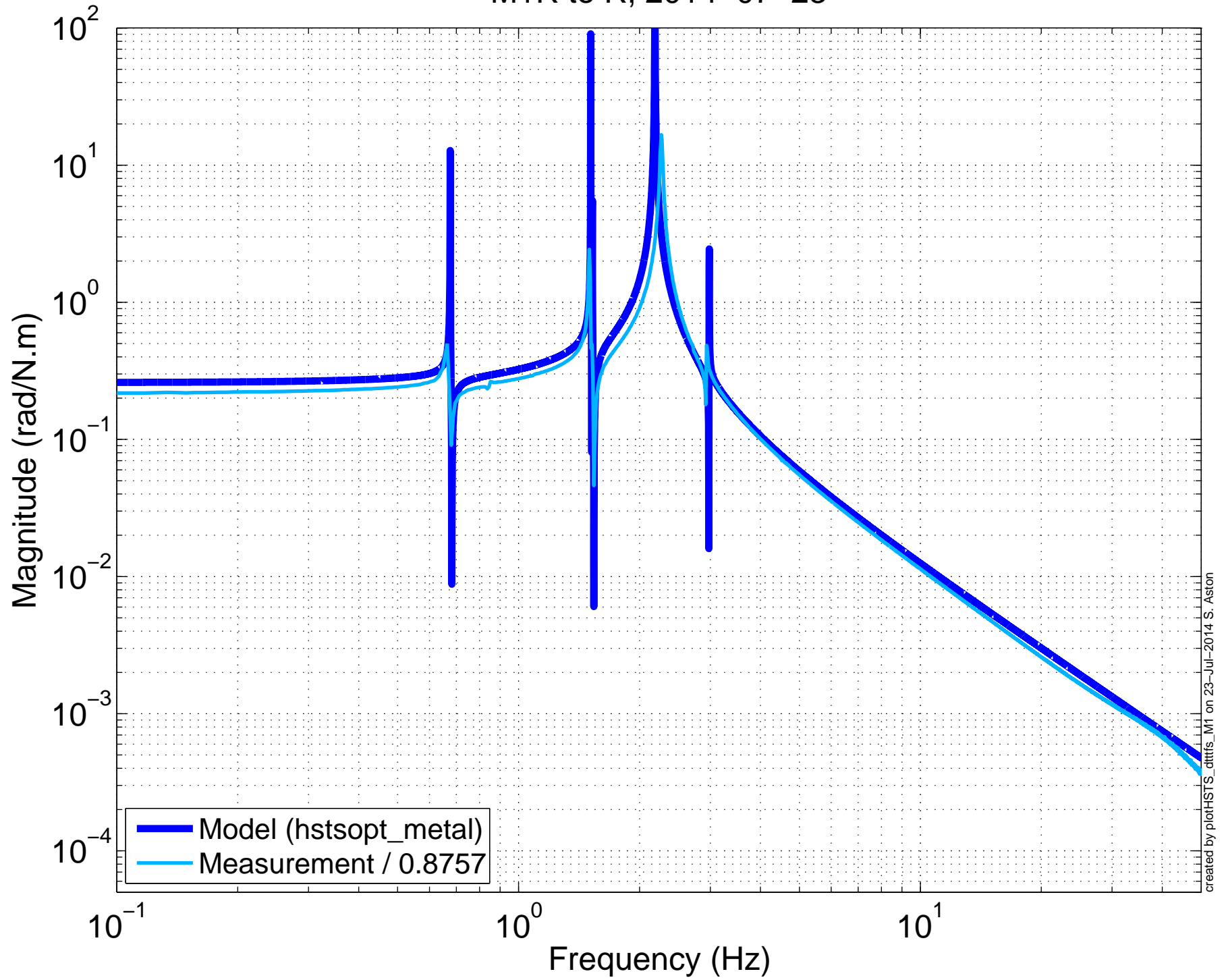
H1SUSSR2 BUILD

M1V to V, 2014-07-23



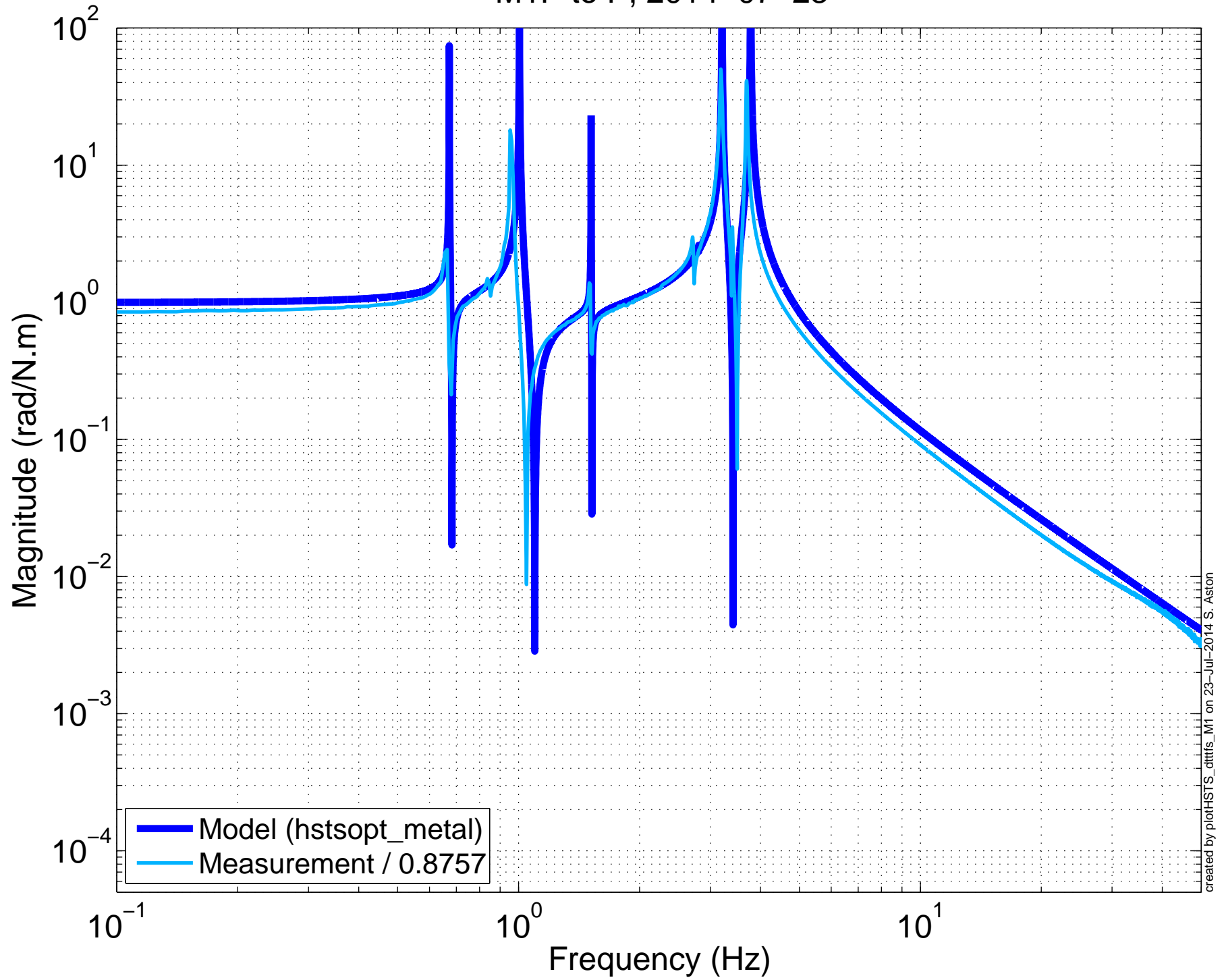
H1SUSSR2 BUILD

M1R to R, 2014-07-23



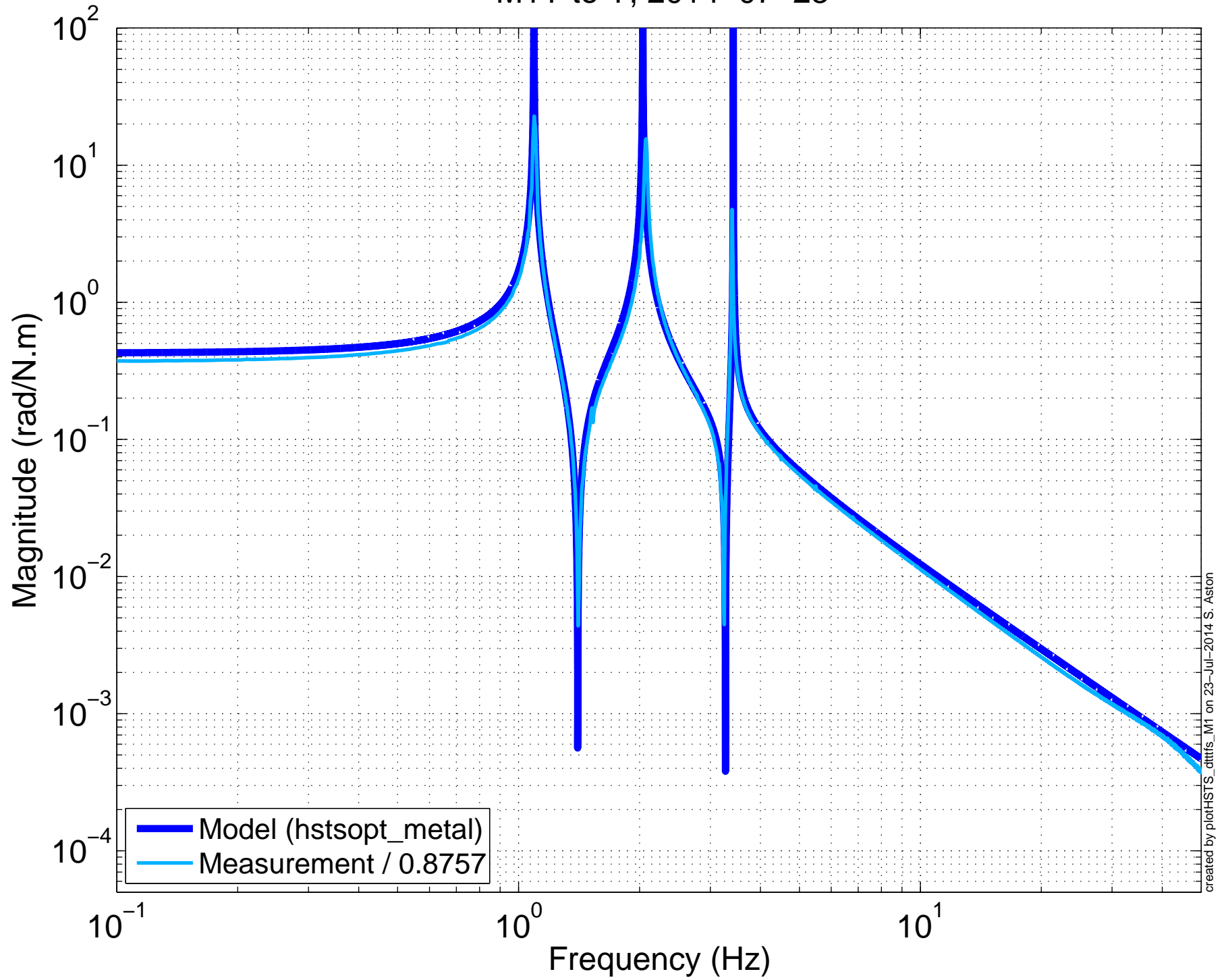
H1SUSSR2 BUILD

M1P to P, 2014-07-23



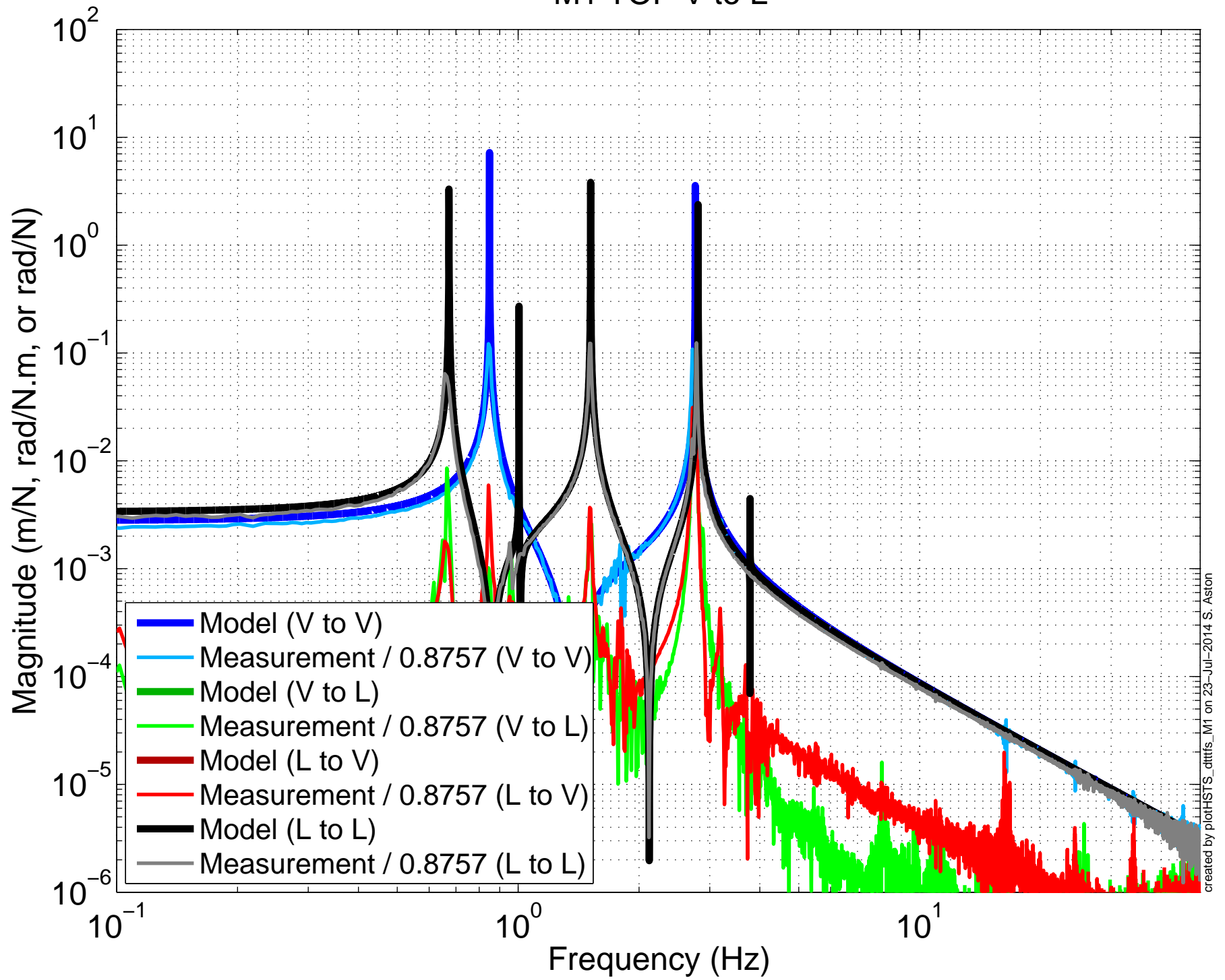
H1SUSSR2 BUILD

M1Y to Y, 2014-07-23



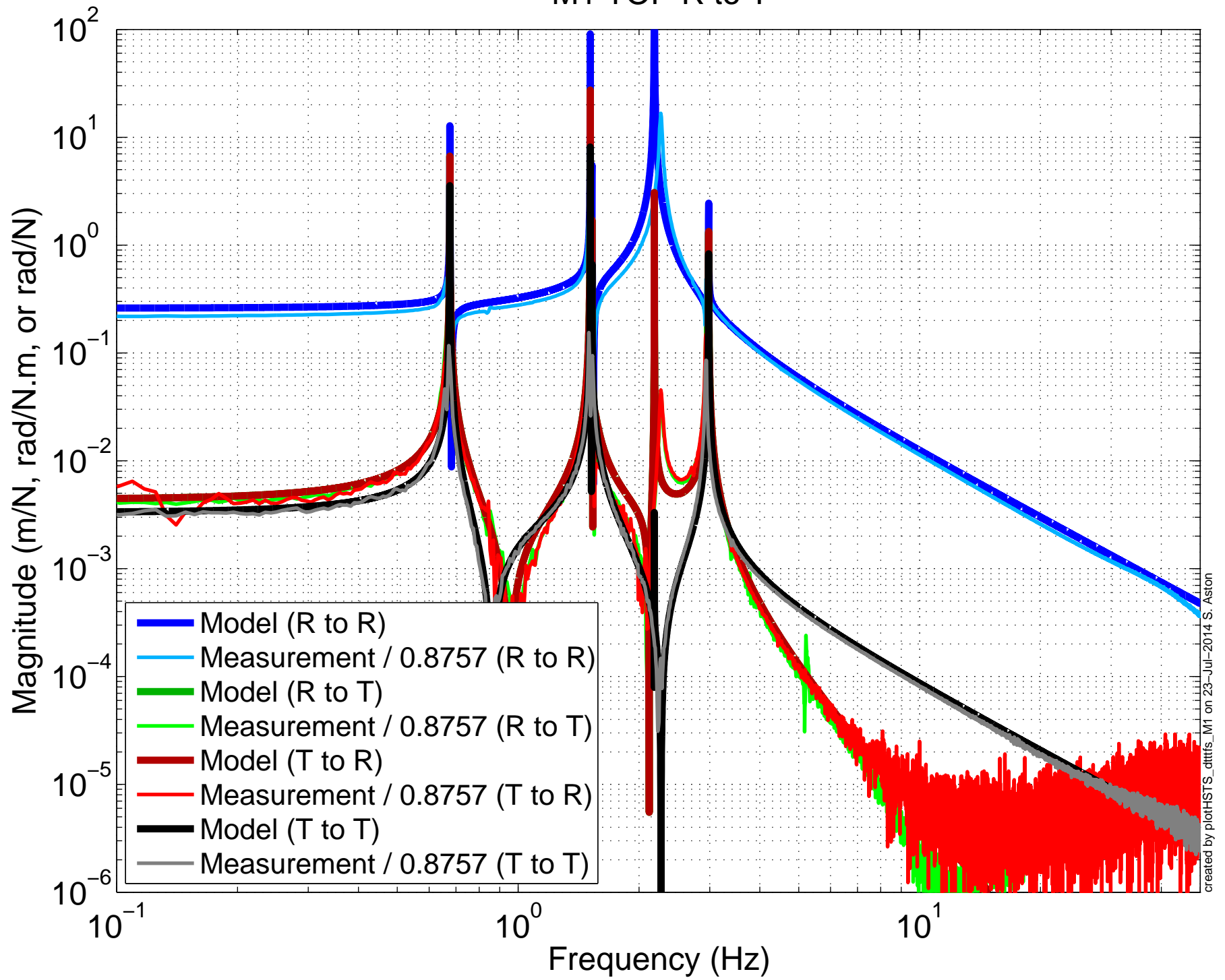
H1SUSSR2 BUILD

M1 TOP V to L



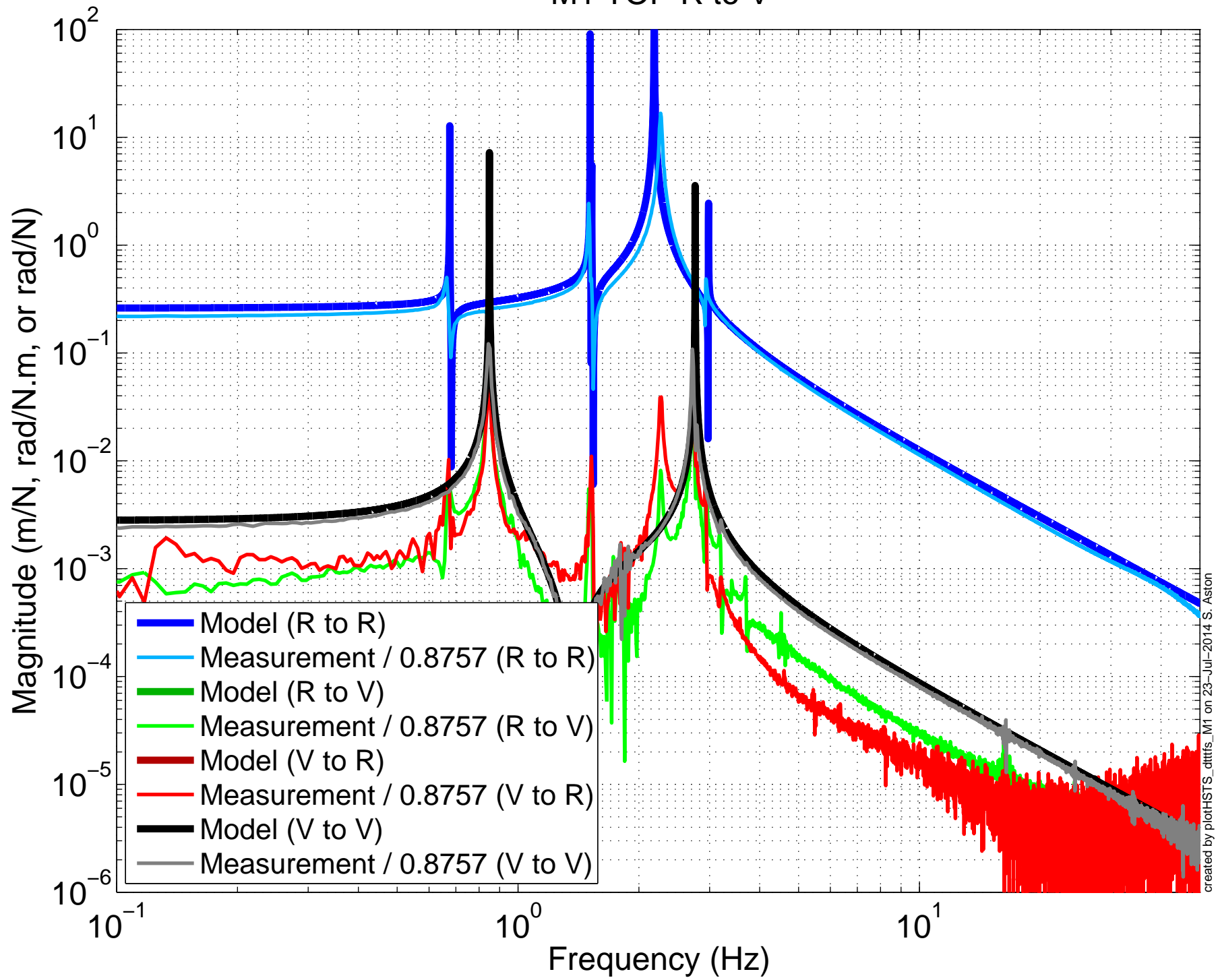
H1SUSSR2 BUILD

M1 TOP R to T



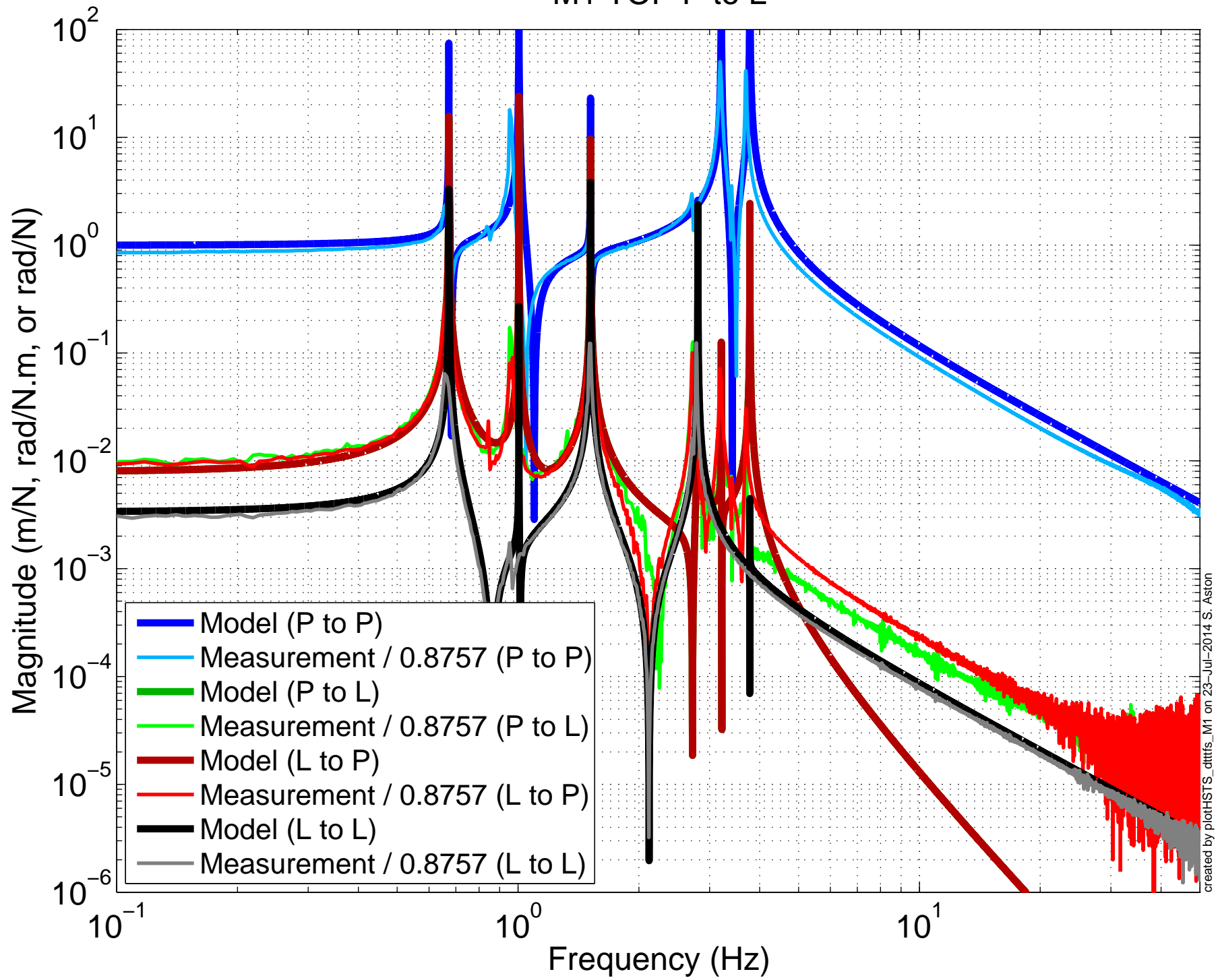
H1SUSSR2 BUILD

M1 TOP R to V



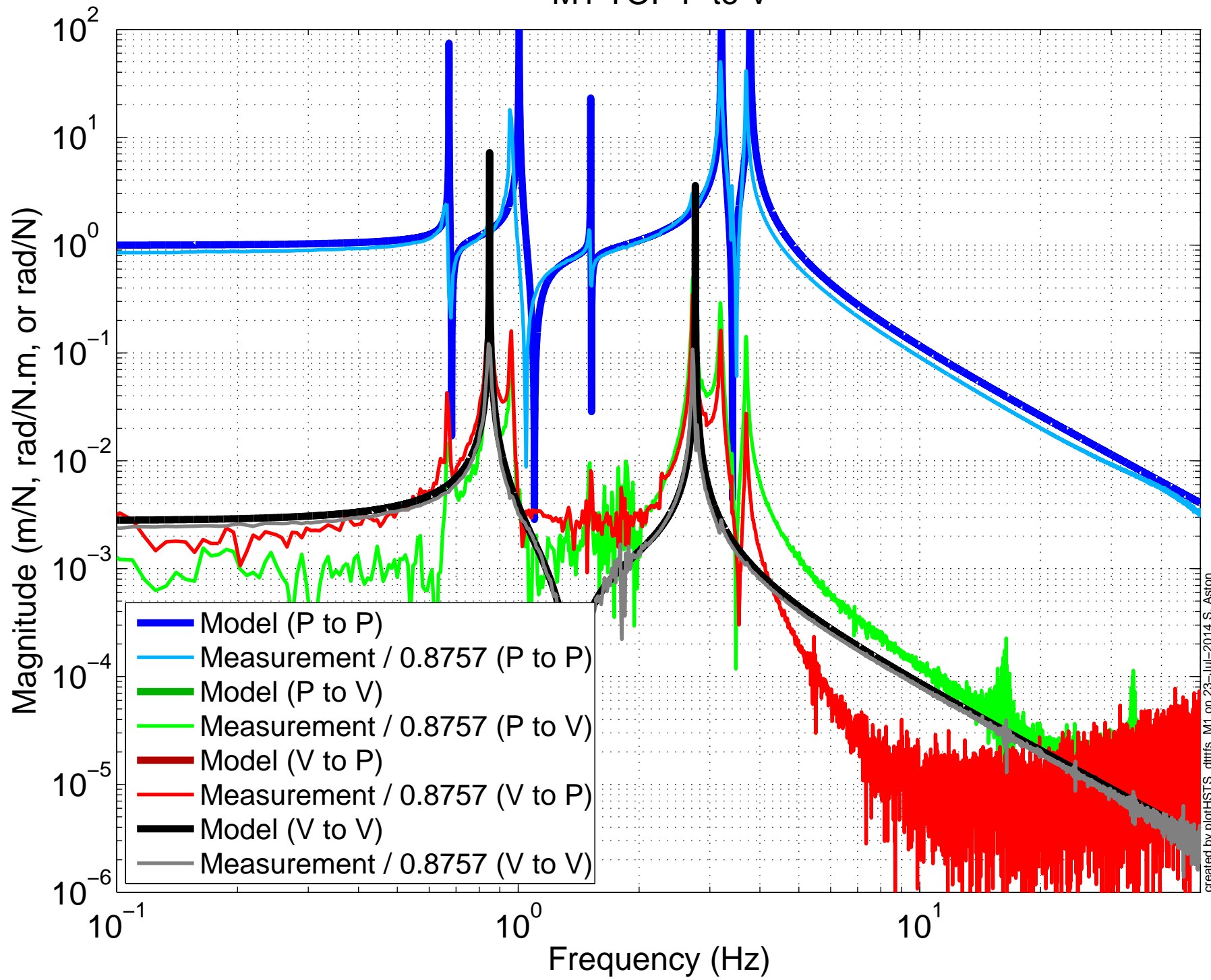
H1SUSSR2 BUILD

M1 TOP P to L



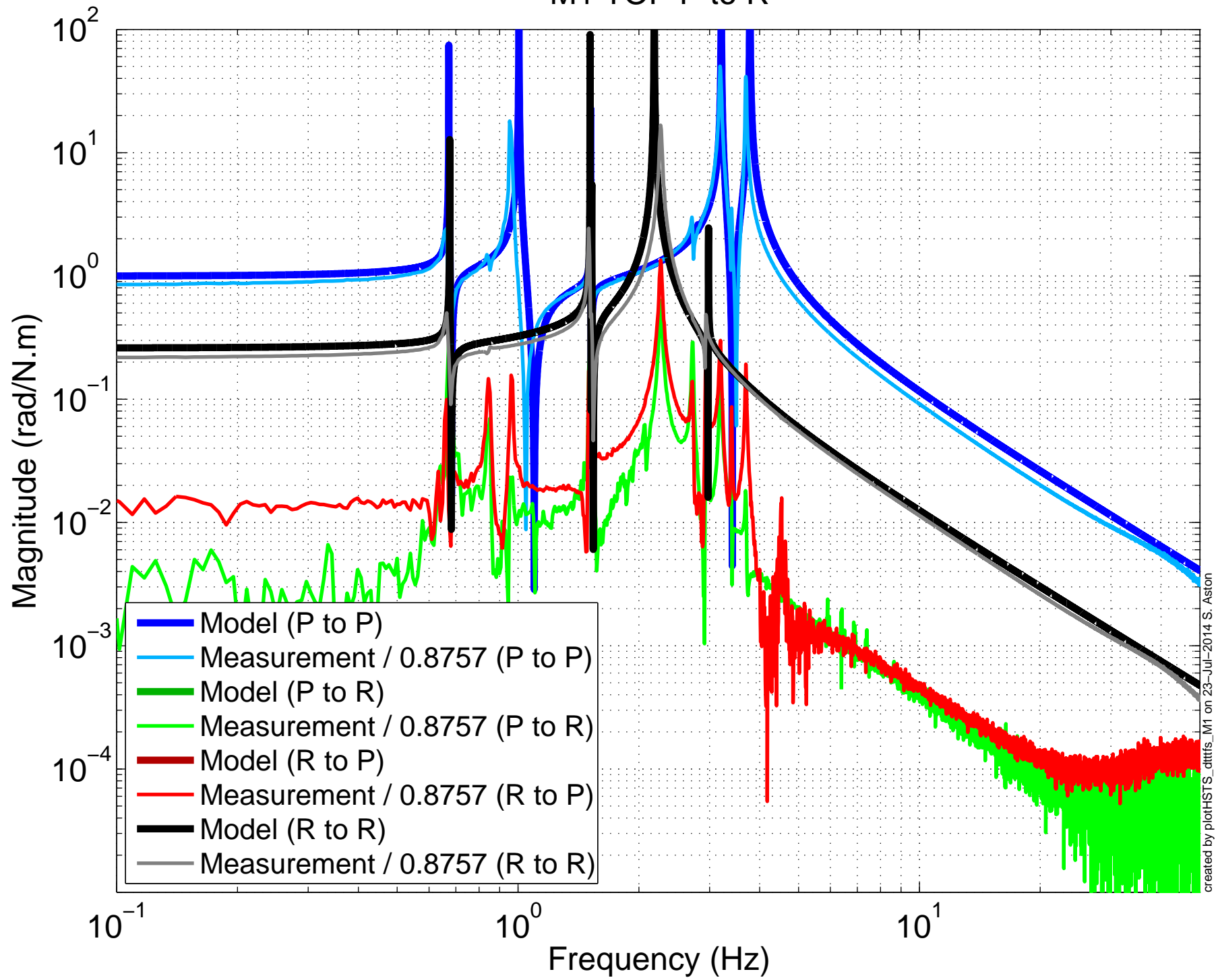
H1SUSSR2 BUILD

M1 TOP P to V

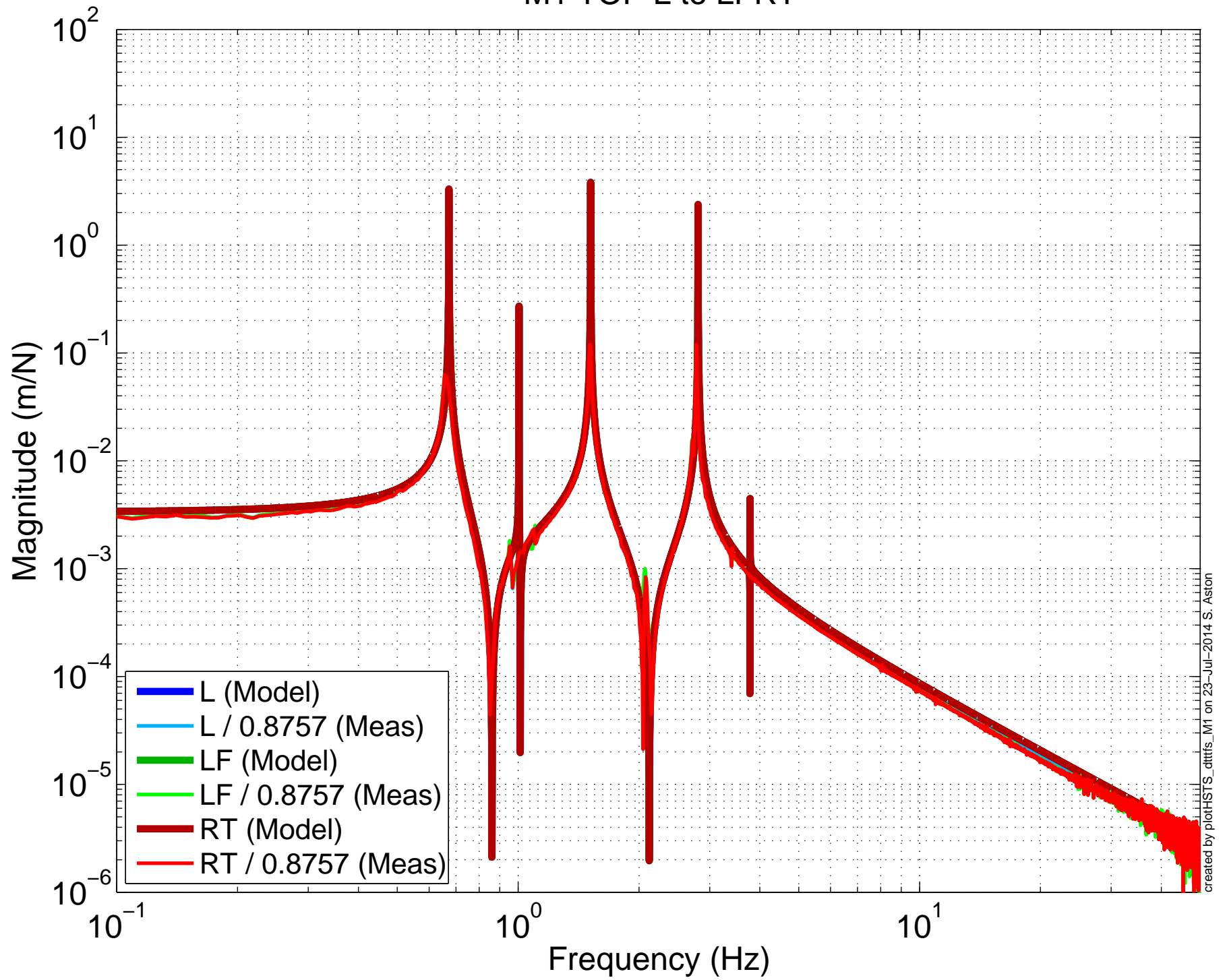


H1SUSSR2 BUILD

M1 TOP P to R



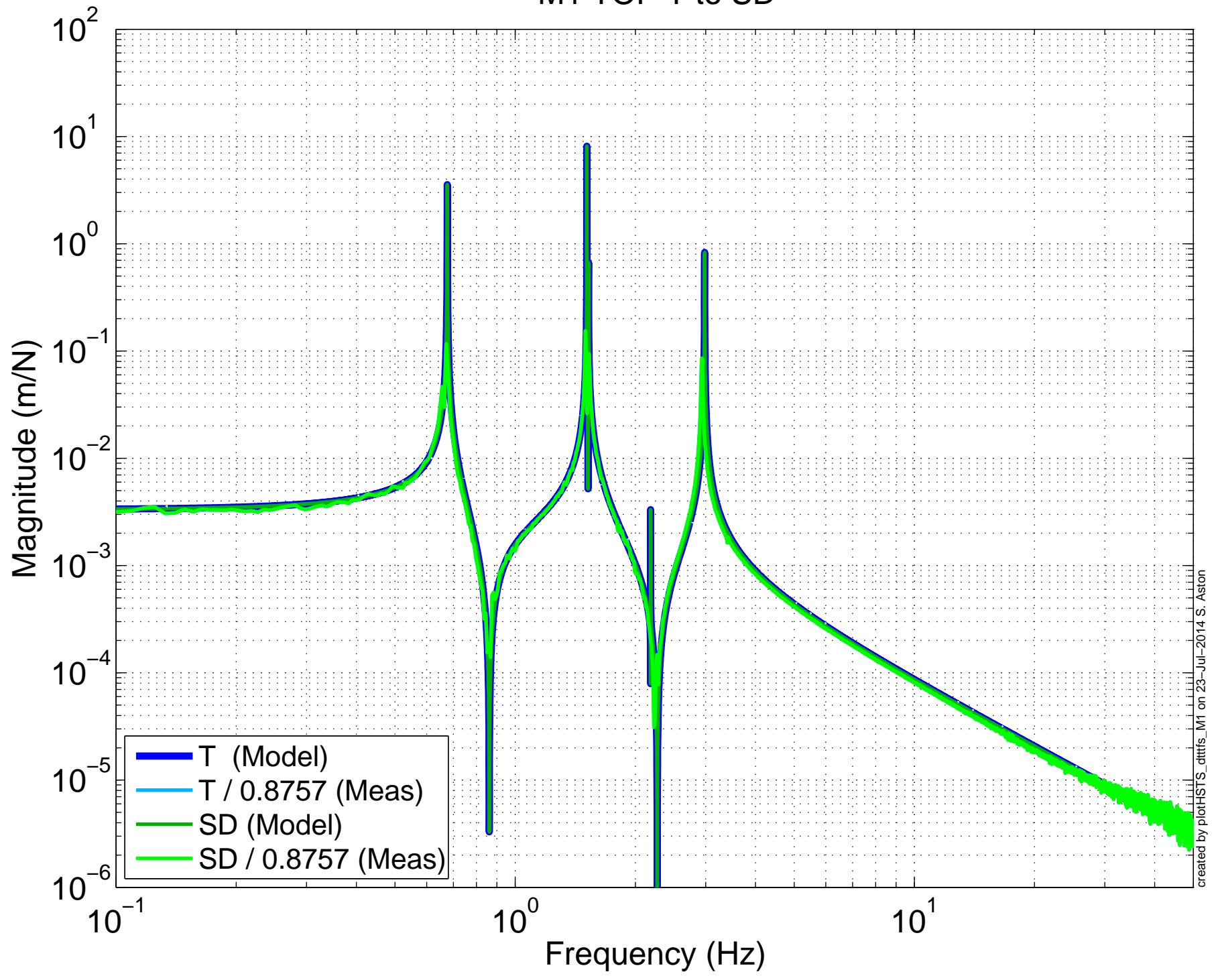
H1SUSSR2 BUILD M1 TOP L to LFRT



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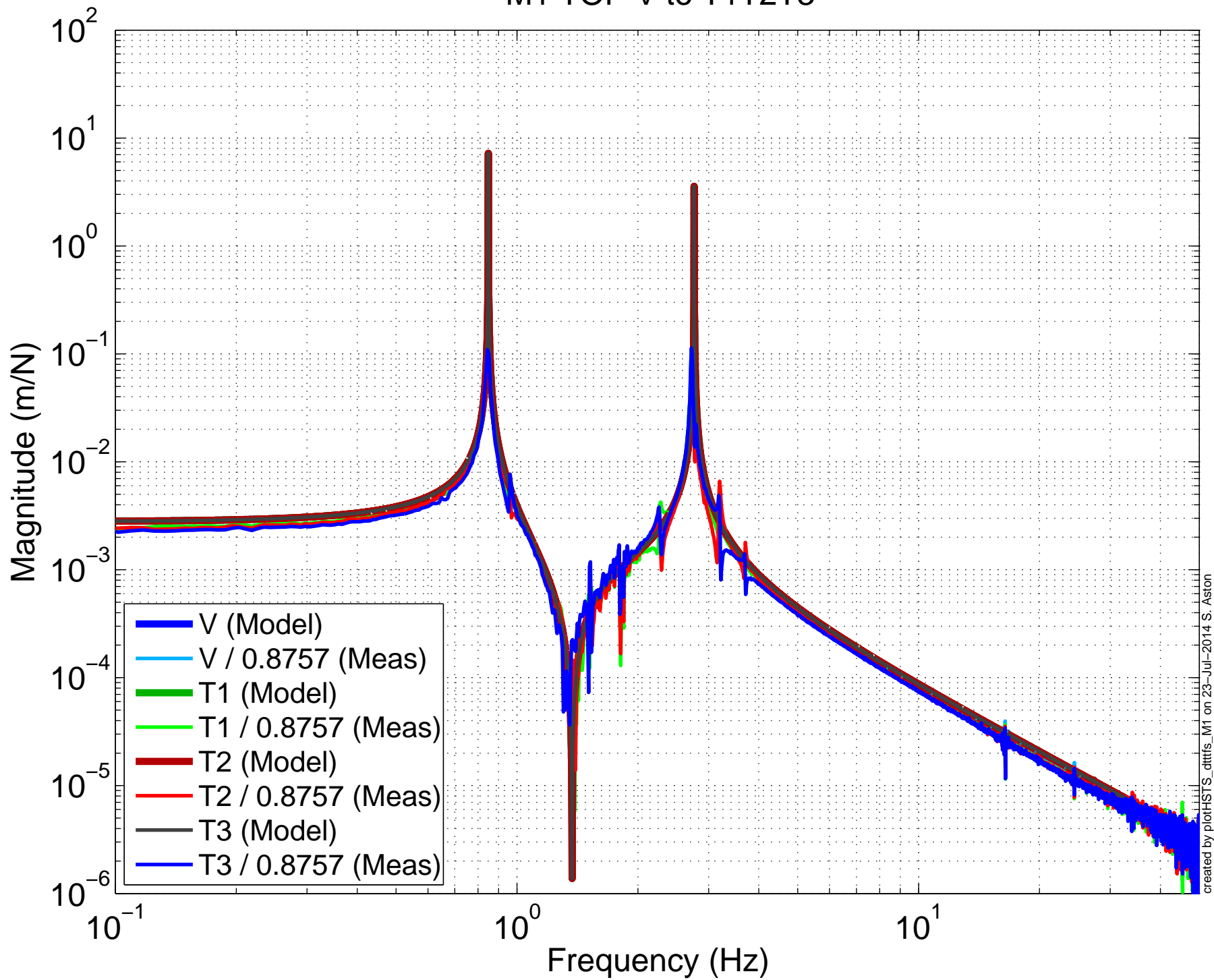
H1SUSSR2 BUILD

M1 TOP T to SD



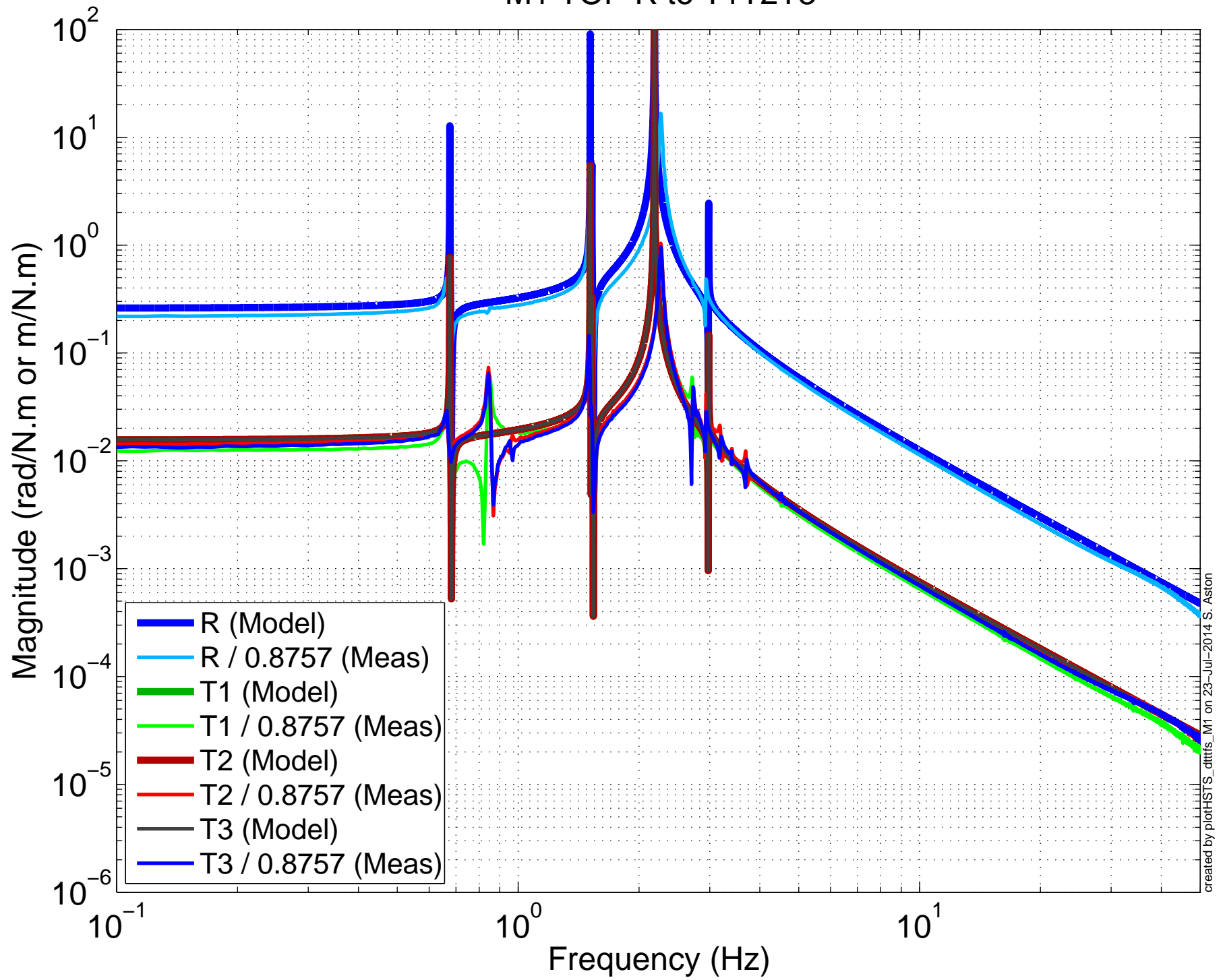
H1SUSSR2 BUILD

M1 TOP V to T1T2T3



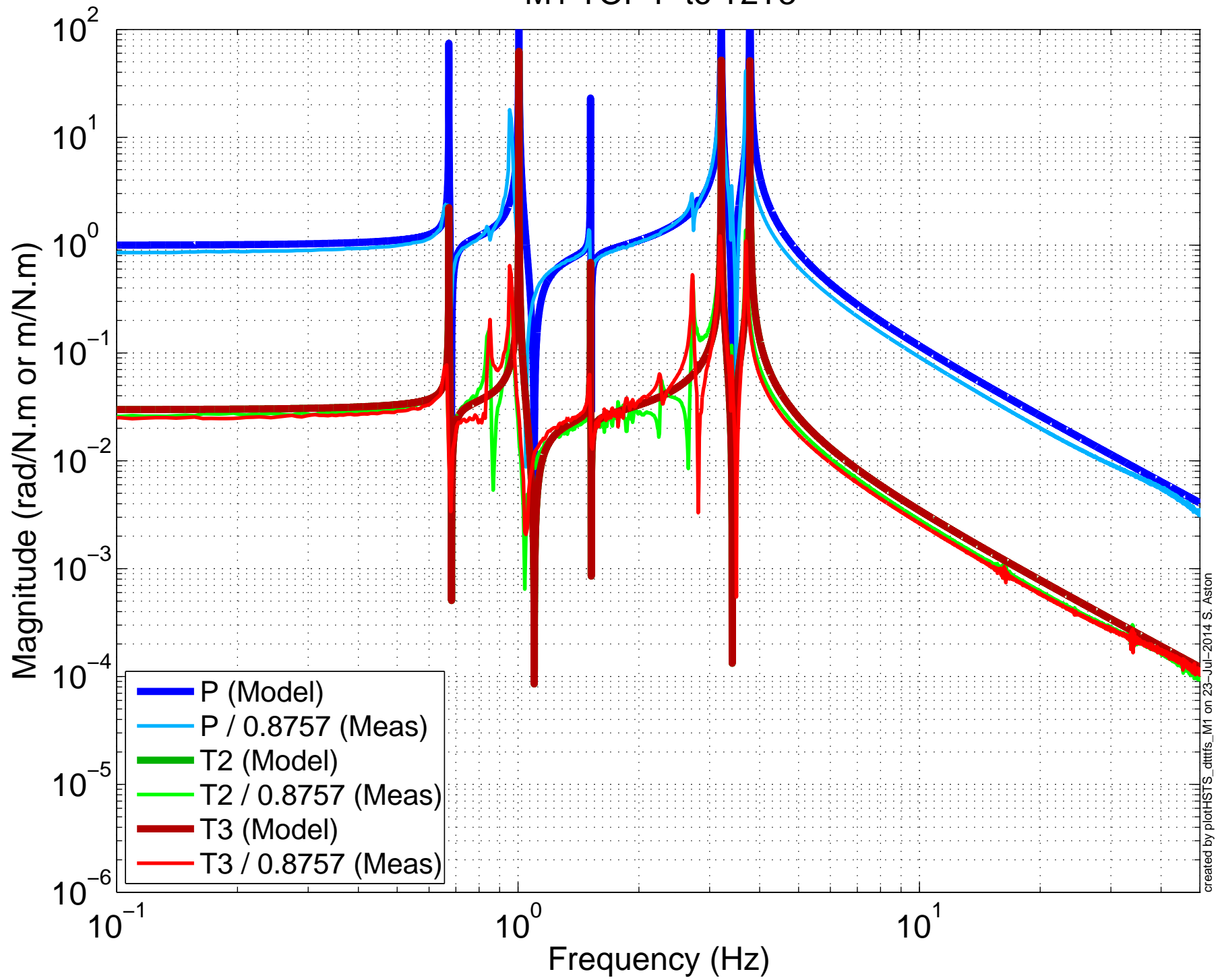
H1SUSSR2 BUILD

M1 TOP R to T1T2T3



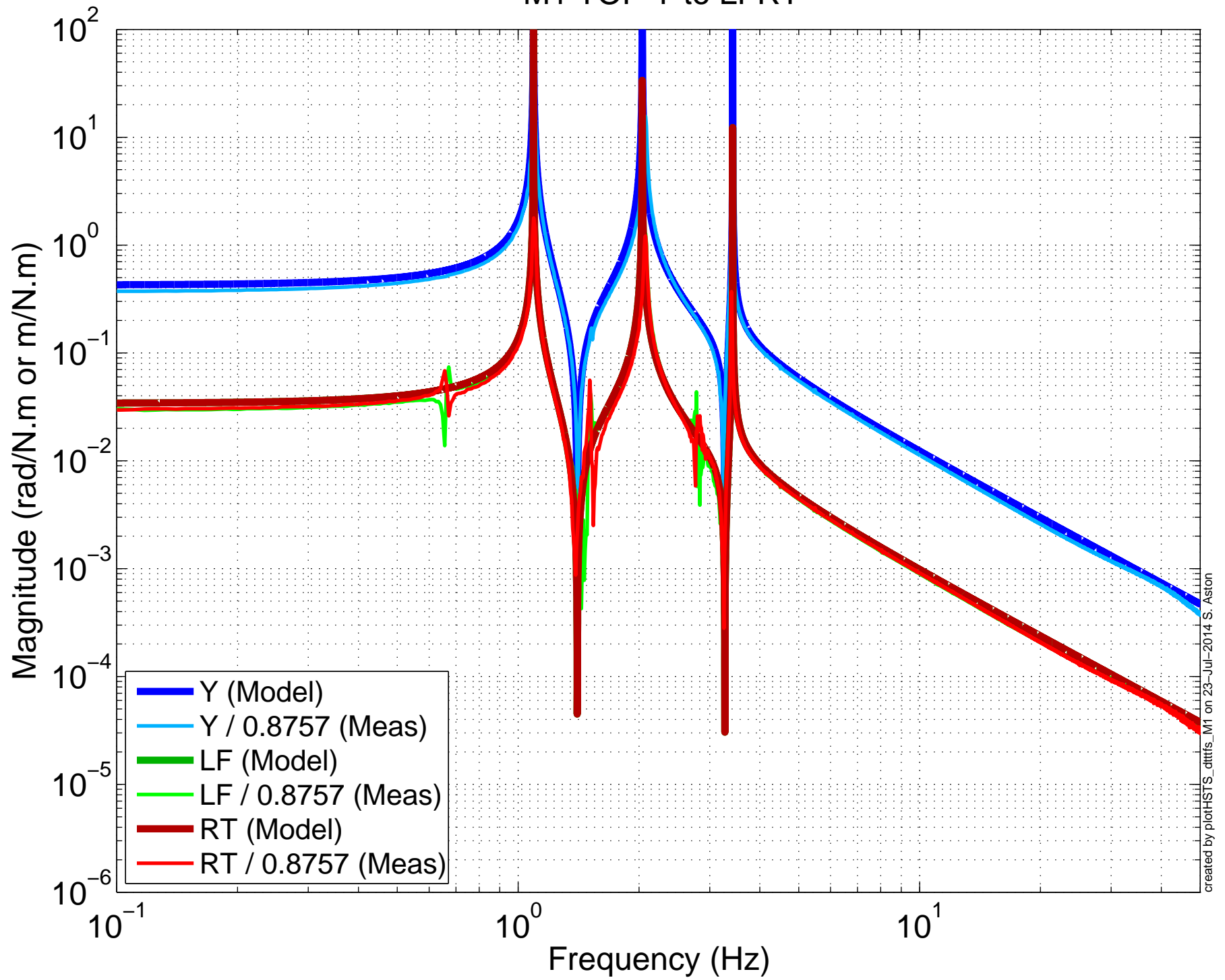
H1SUSSR2 BUILD

M1 TOP P to T2T3



H1SUSSR2 BUILD

M1 TOP Y to LFRT



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