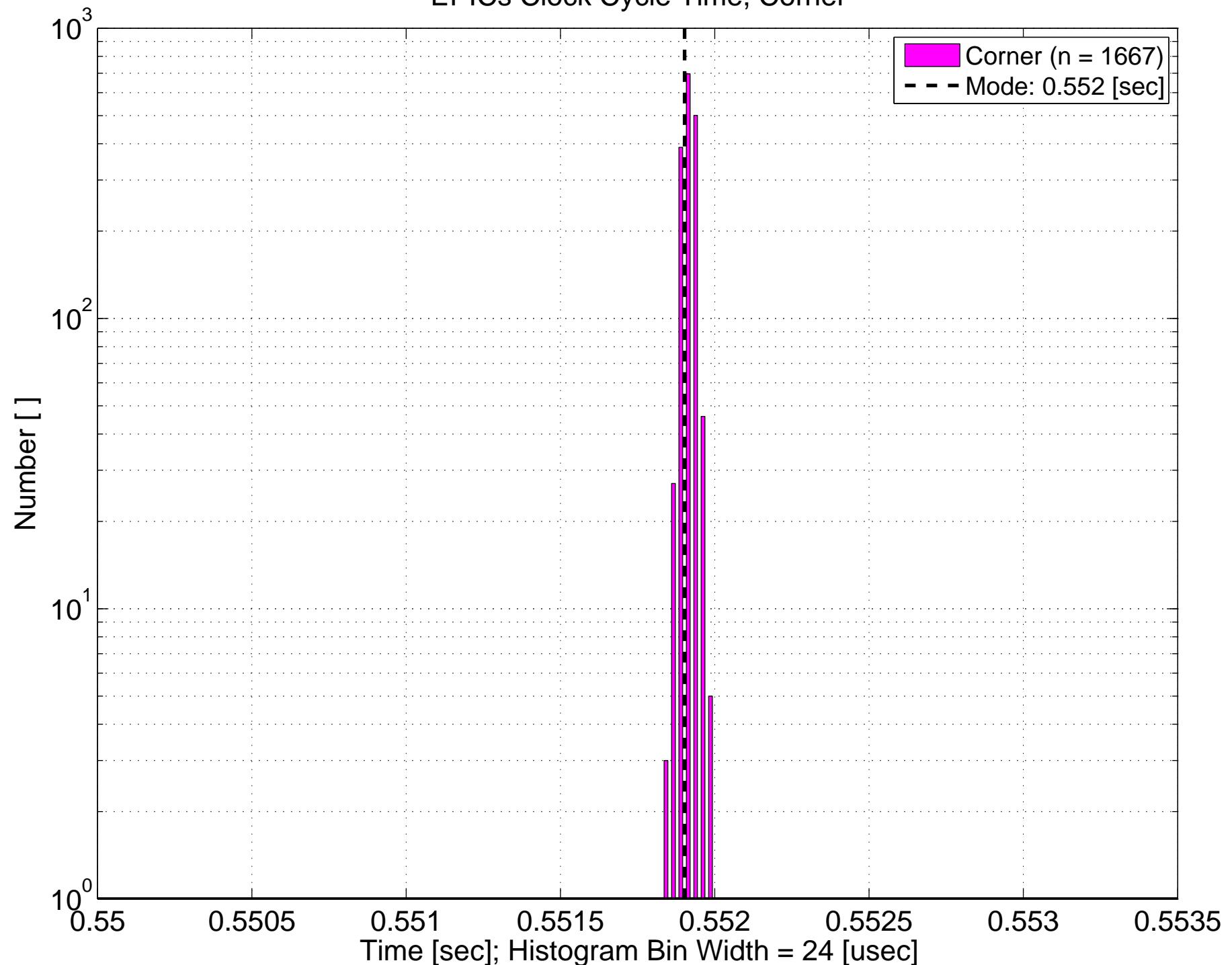
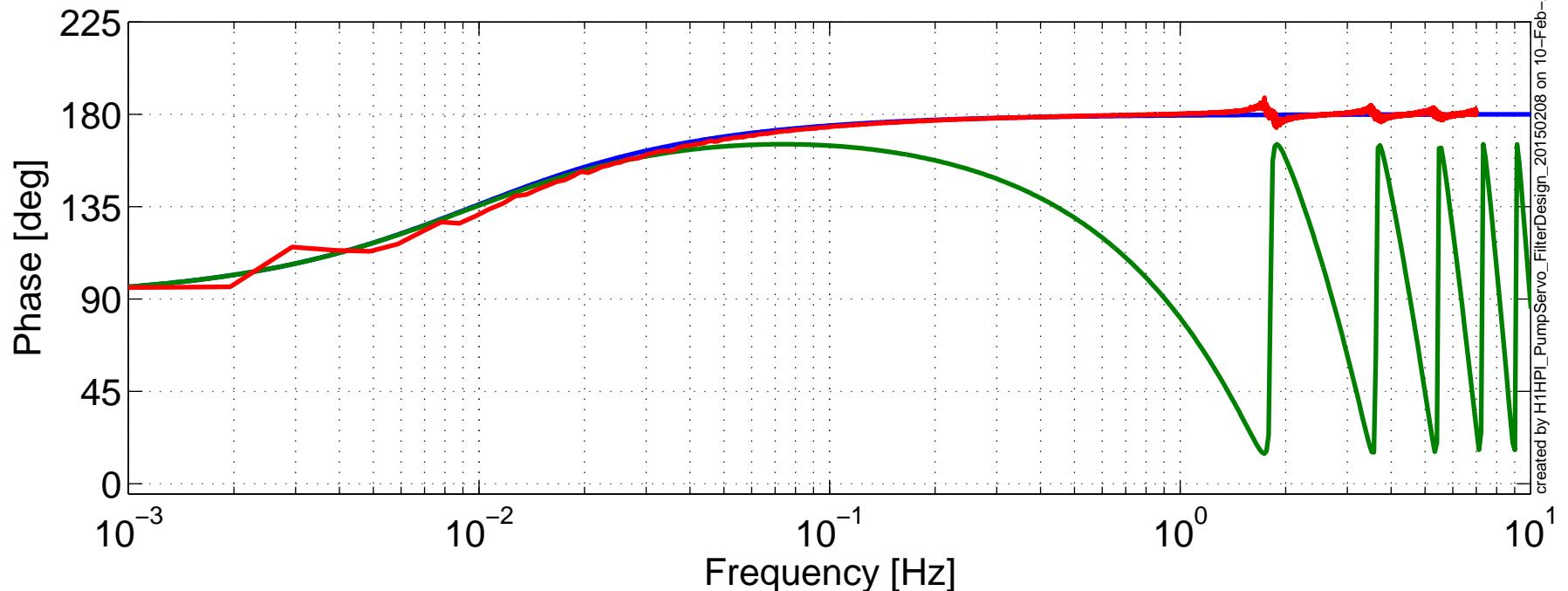
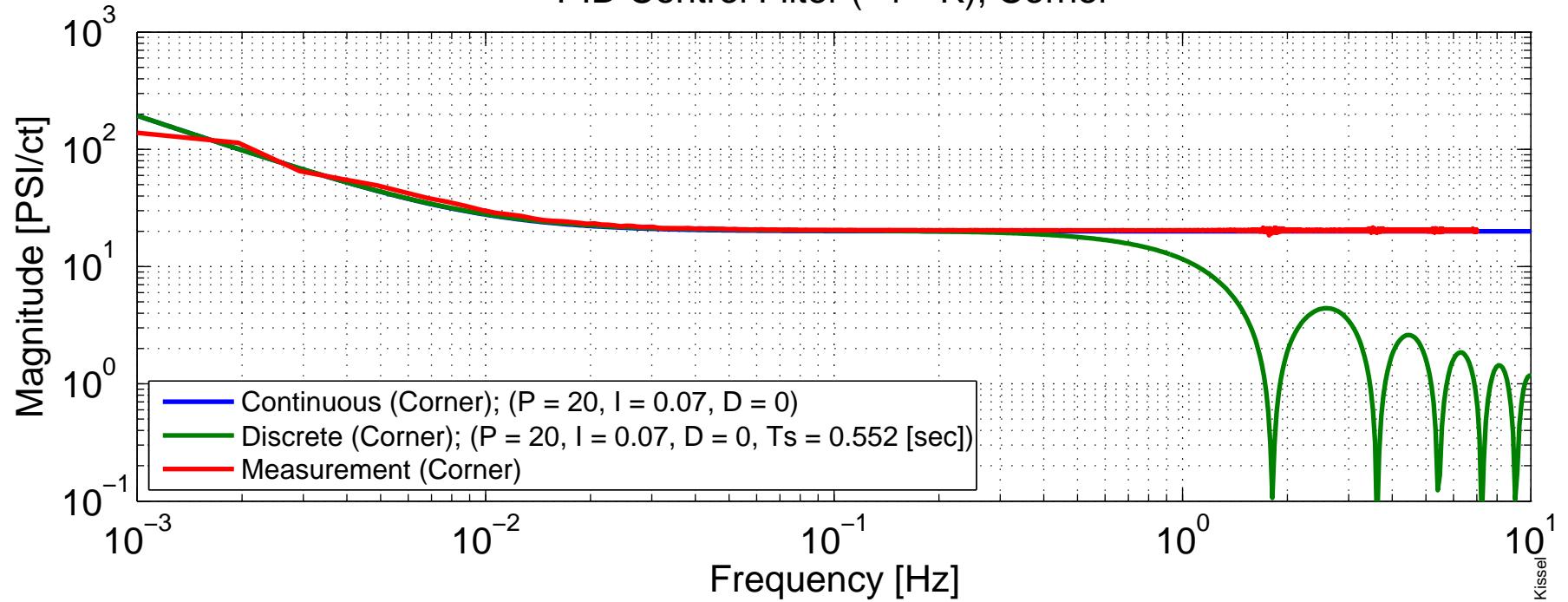


2015-02-10 H1 HPI Pump Servo Design
EPICs Clock Cycle Time, Corner

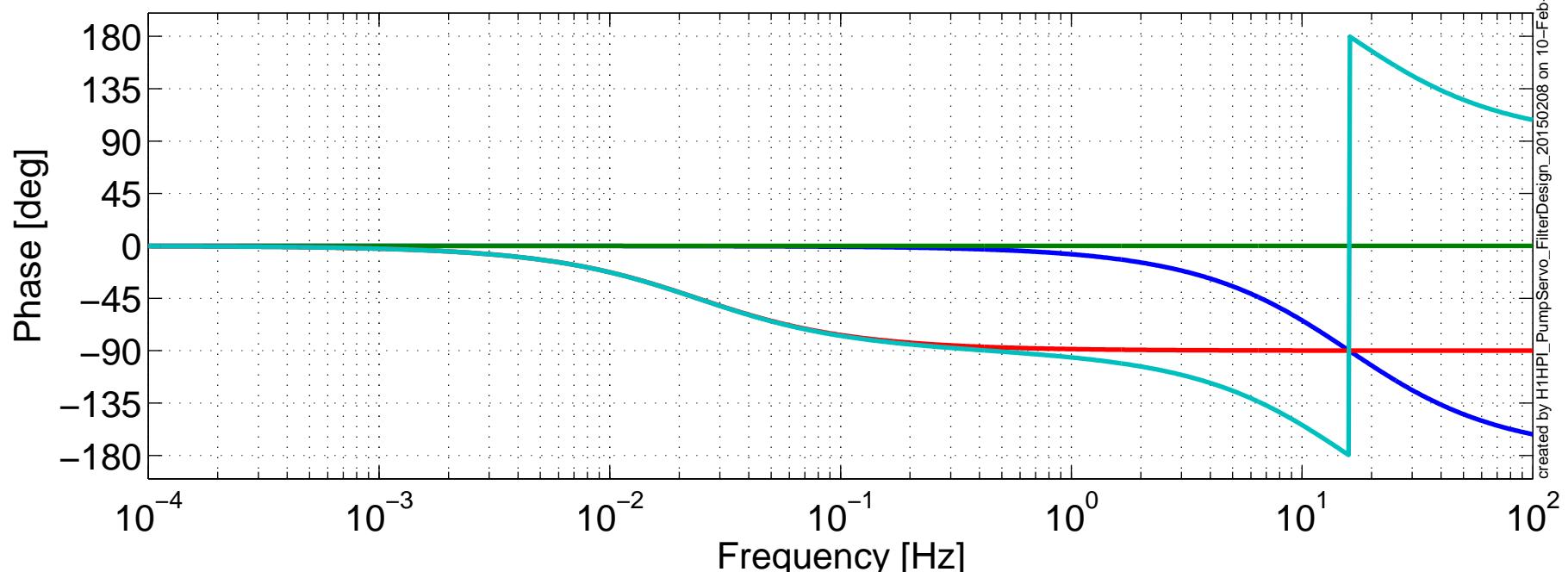
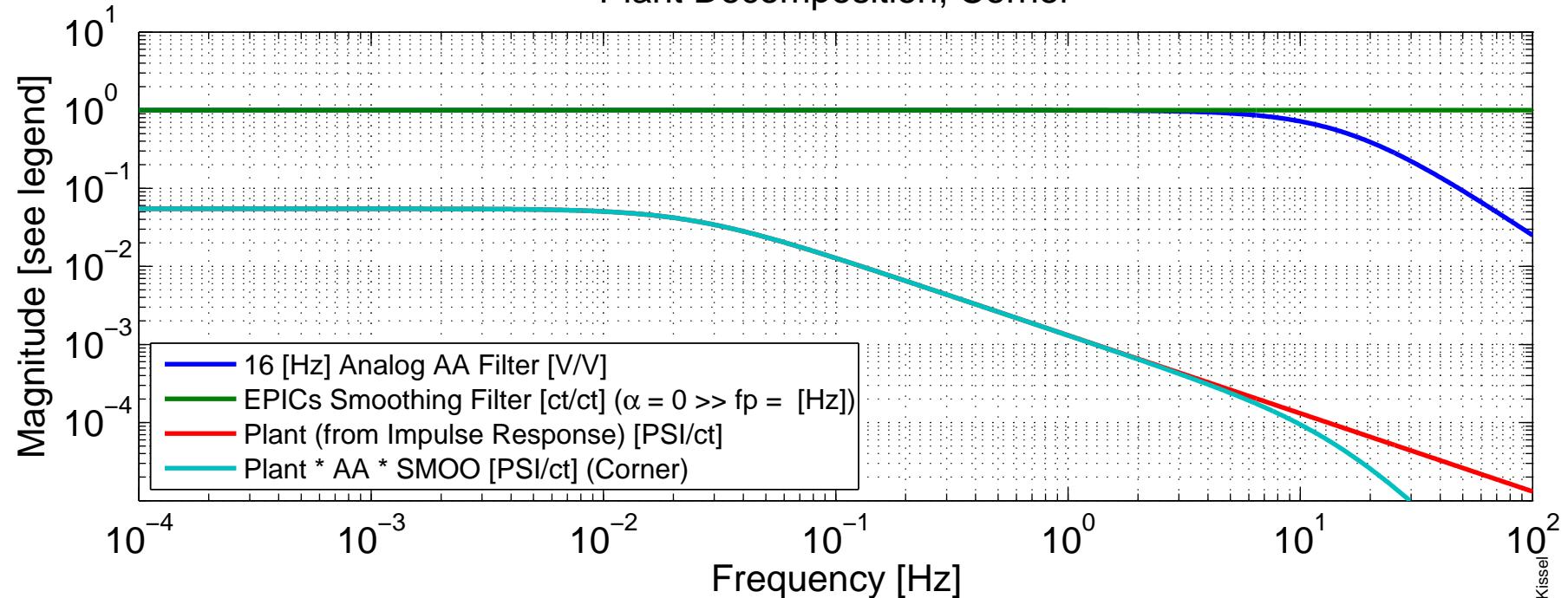


HEPI Pump Servo Design

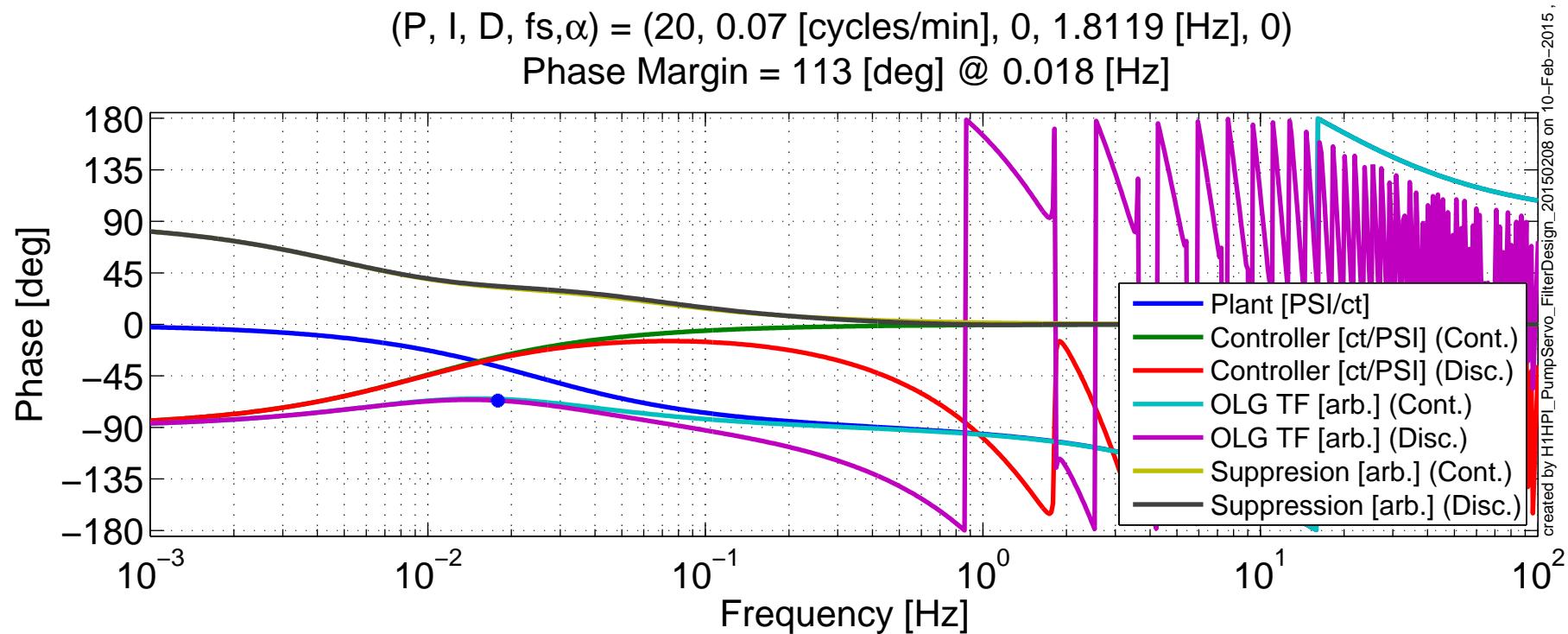
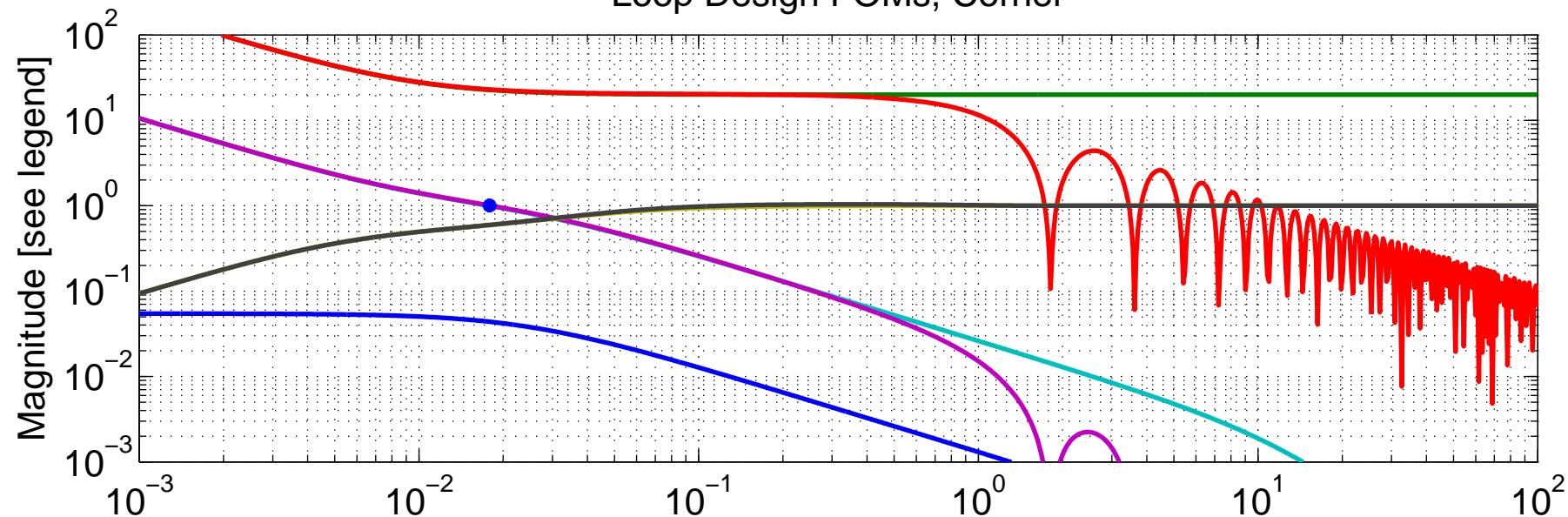
PID Control Filter ($-1 * K$), Corner



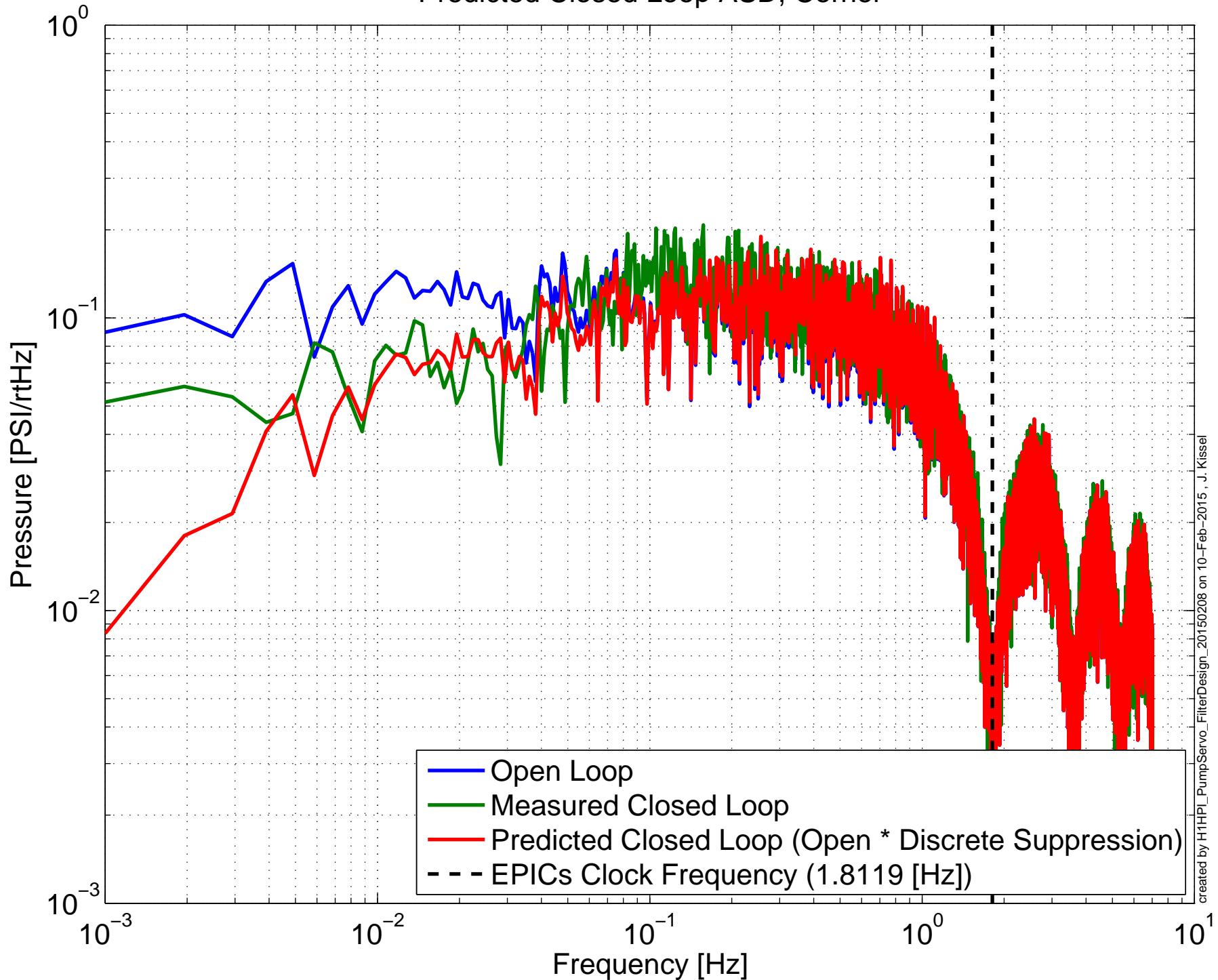
2015–02–10 H1 HPI Pump Servo
Plant Decomposition, Corner



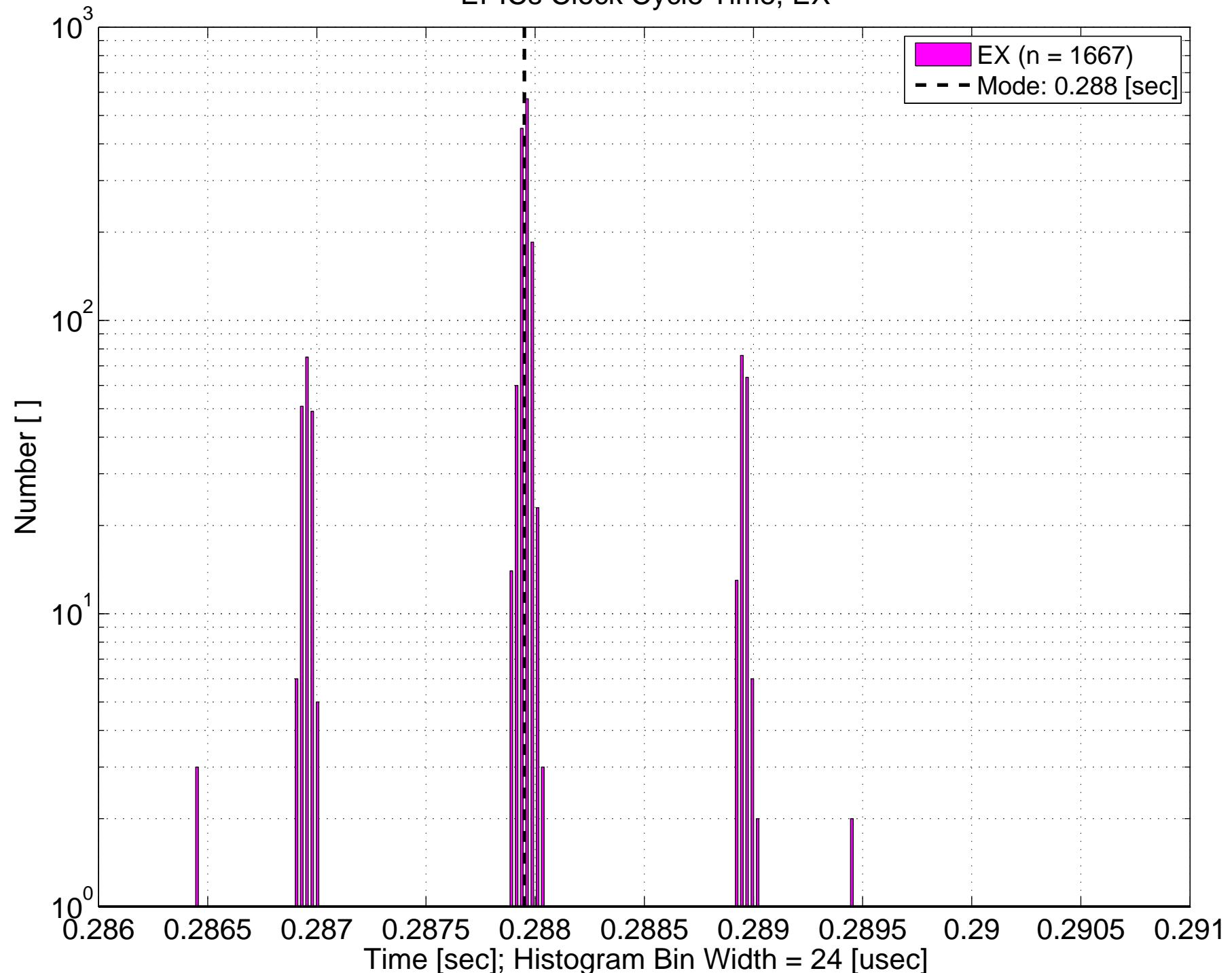
2015–02–10 H1 HPI Pump Servo
Loop Design FOMs, Corner



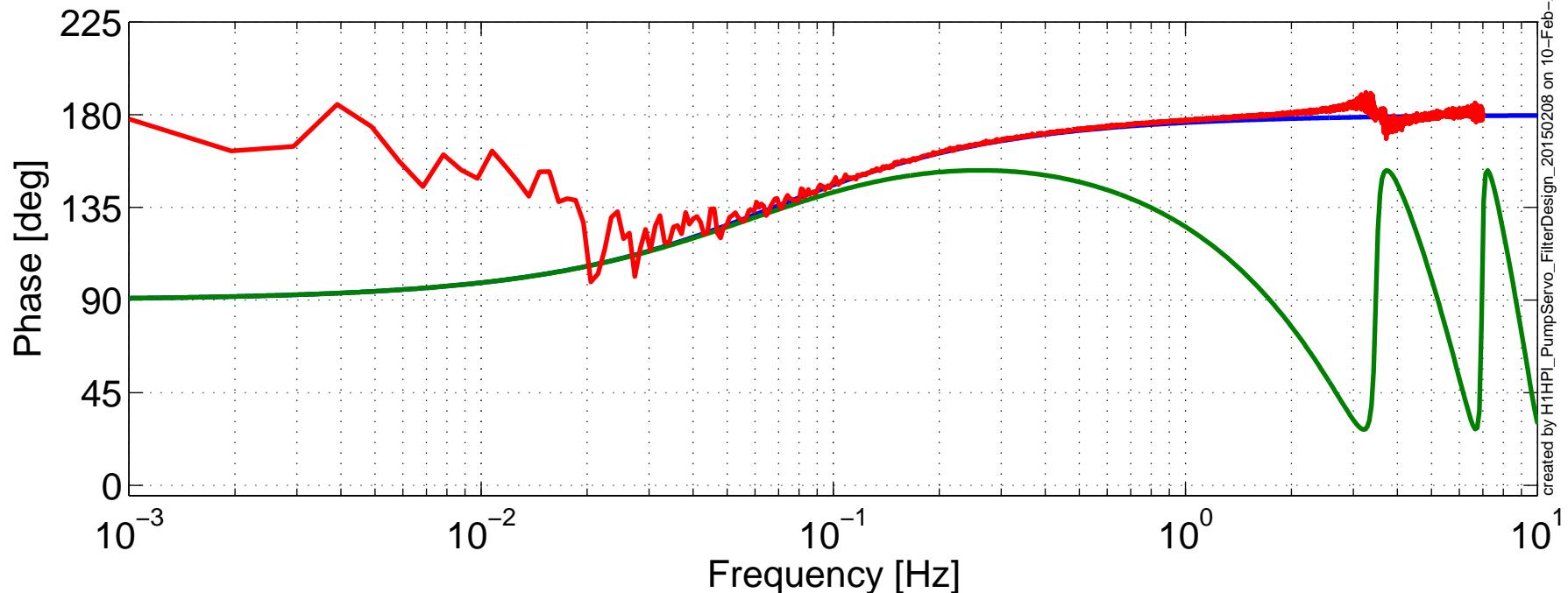
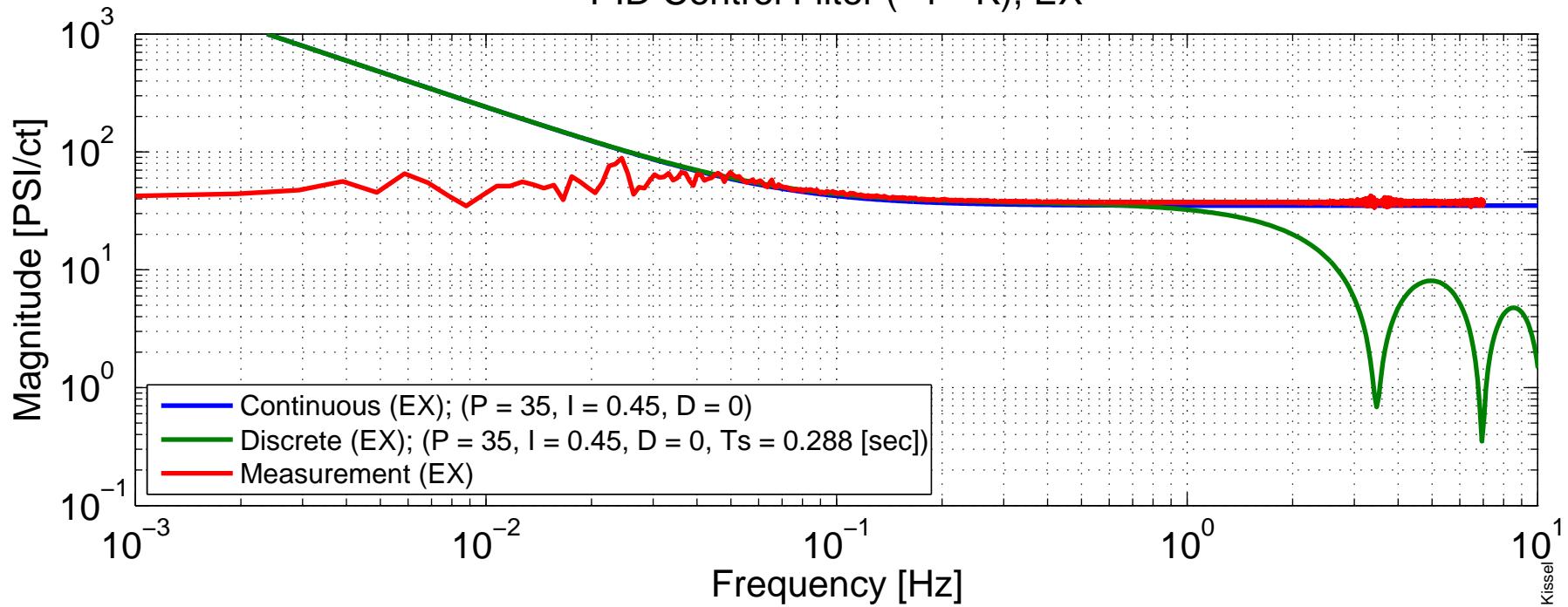
2015–02–10 H1 HPI Pump Servo
Predicted Closed Loop ASD, Corner



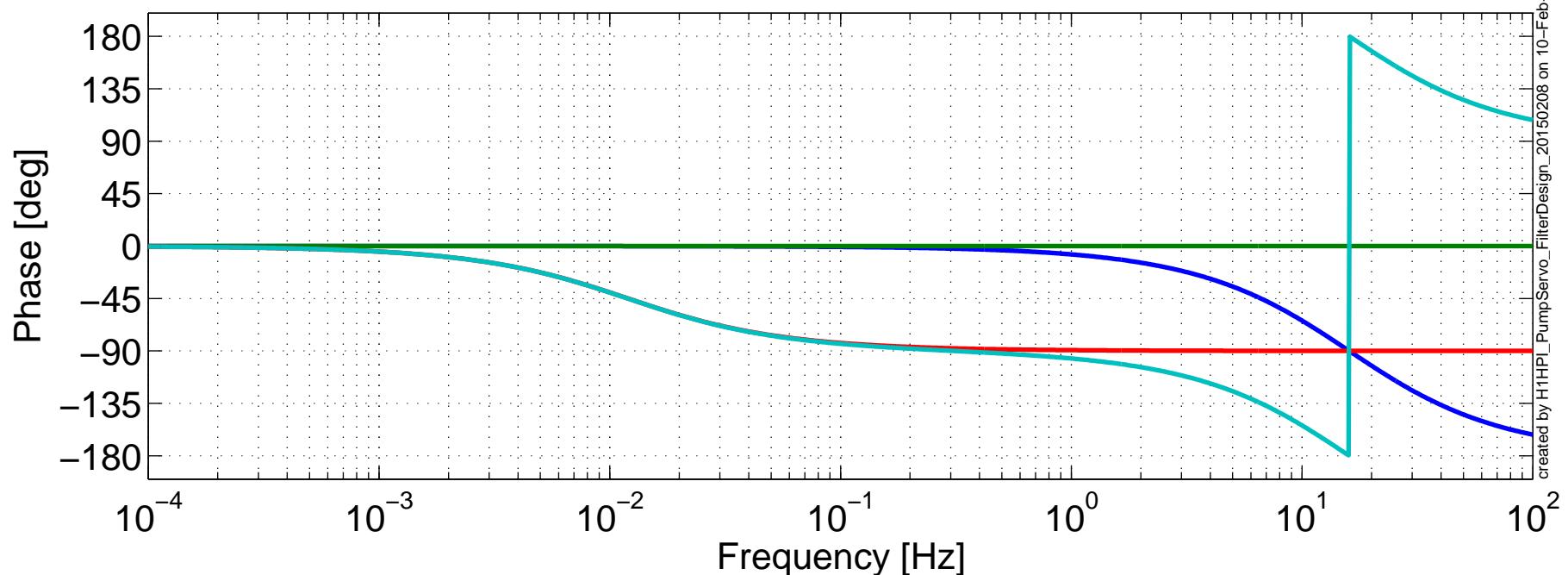
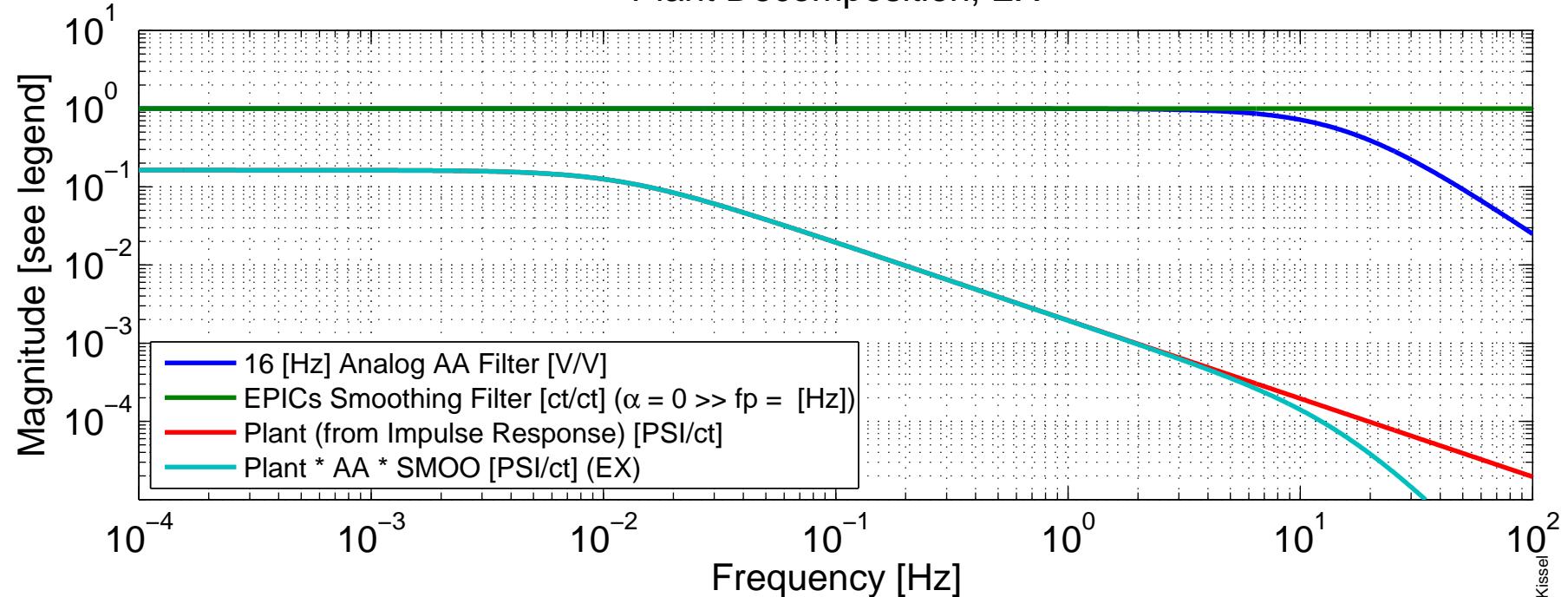
2015-02-10 H1 HPI Pump Servo Design
EPICs Clock Cycle Time, EX



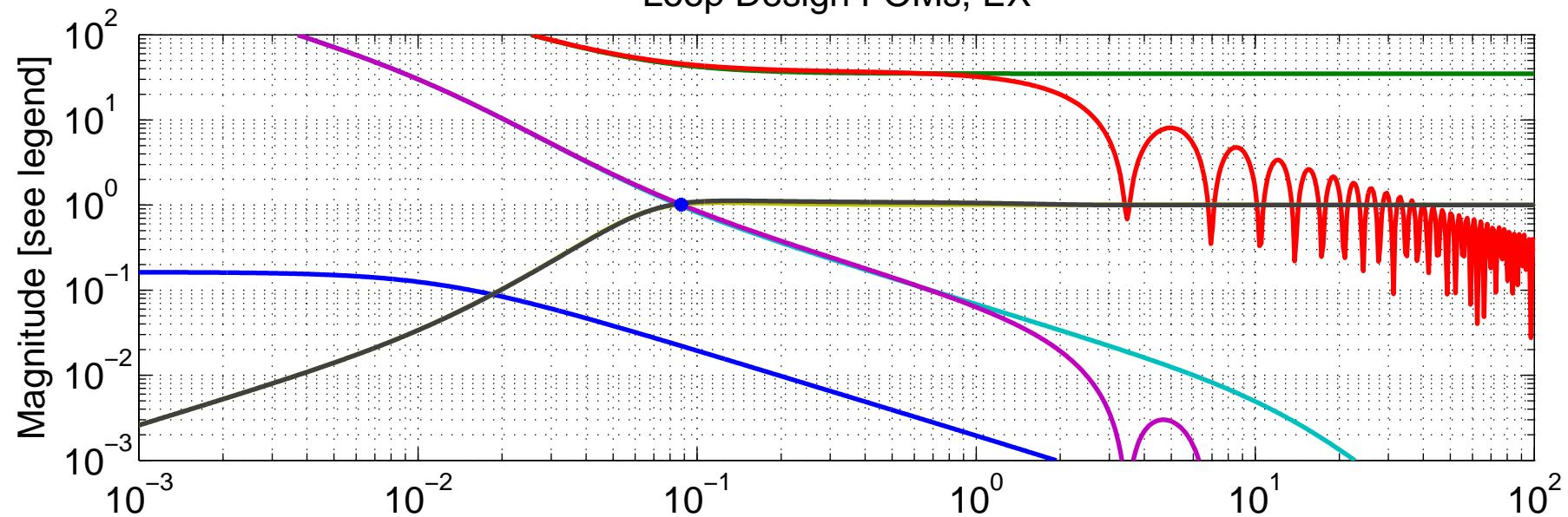
HEPI Pump Servo Design
PID Control Filter ($-1 * K$), EX



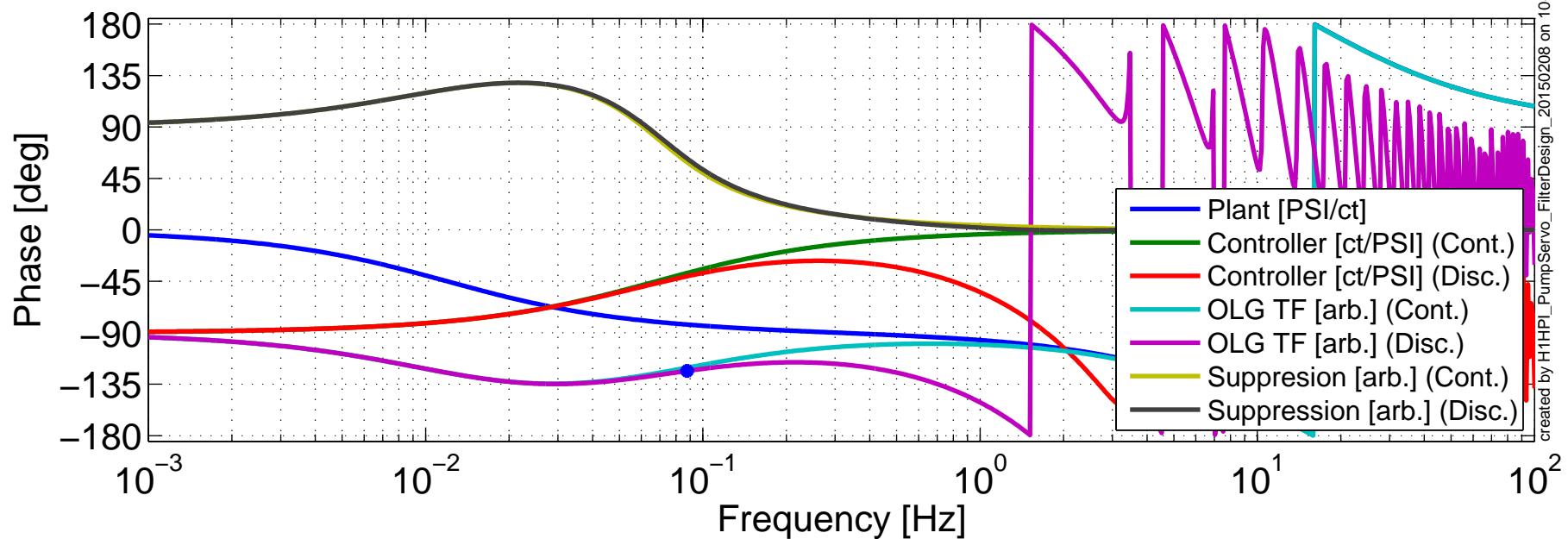
2015–02–10 H1 HPI Pump Servo
Plant Decomposition, EX



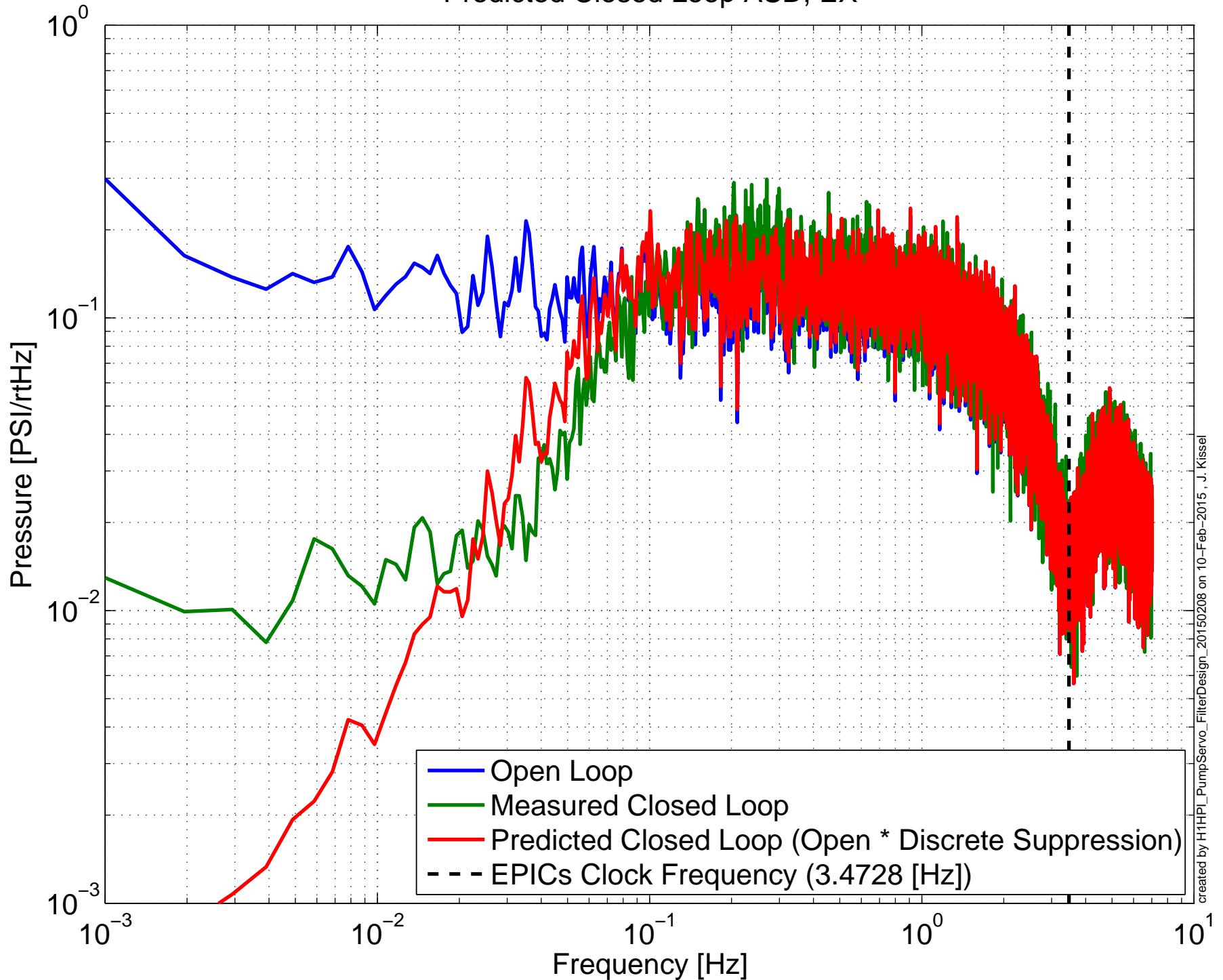
2015–02–10 H1 HPI Pump Servo
Loop Design FOMs, EX



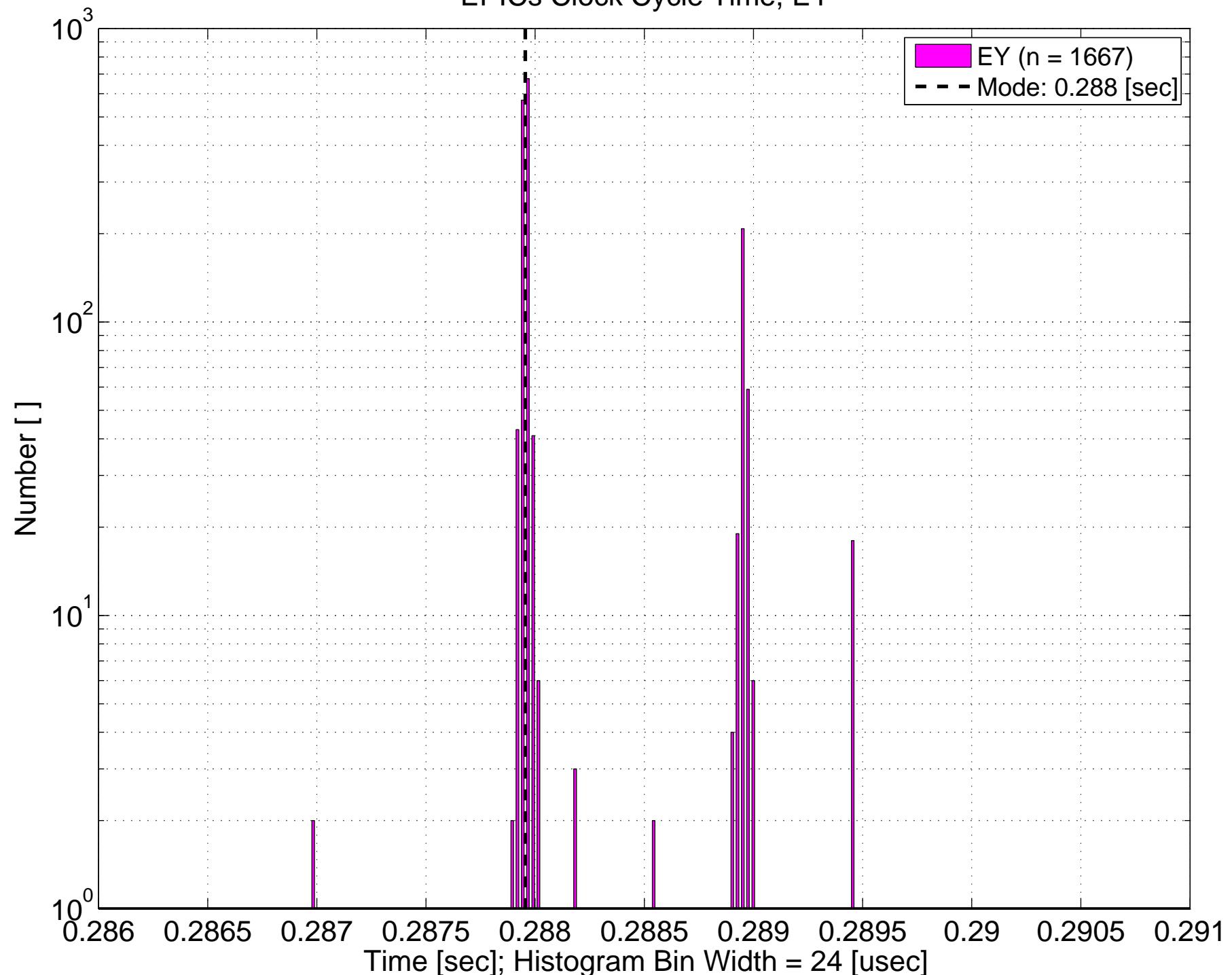
$(P, I, D, fs, \alpha) = (35, 0.45 \text{ [cycles/min]}, 0, 3.4728 \text{ [Hz]}, 0)$
Phase Margin = 56.6 [deg] @ 0.088 [Hz]



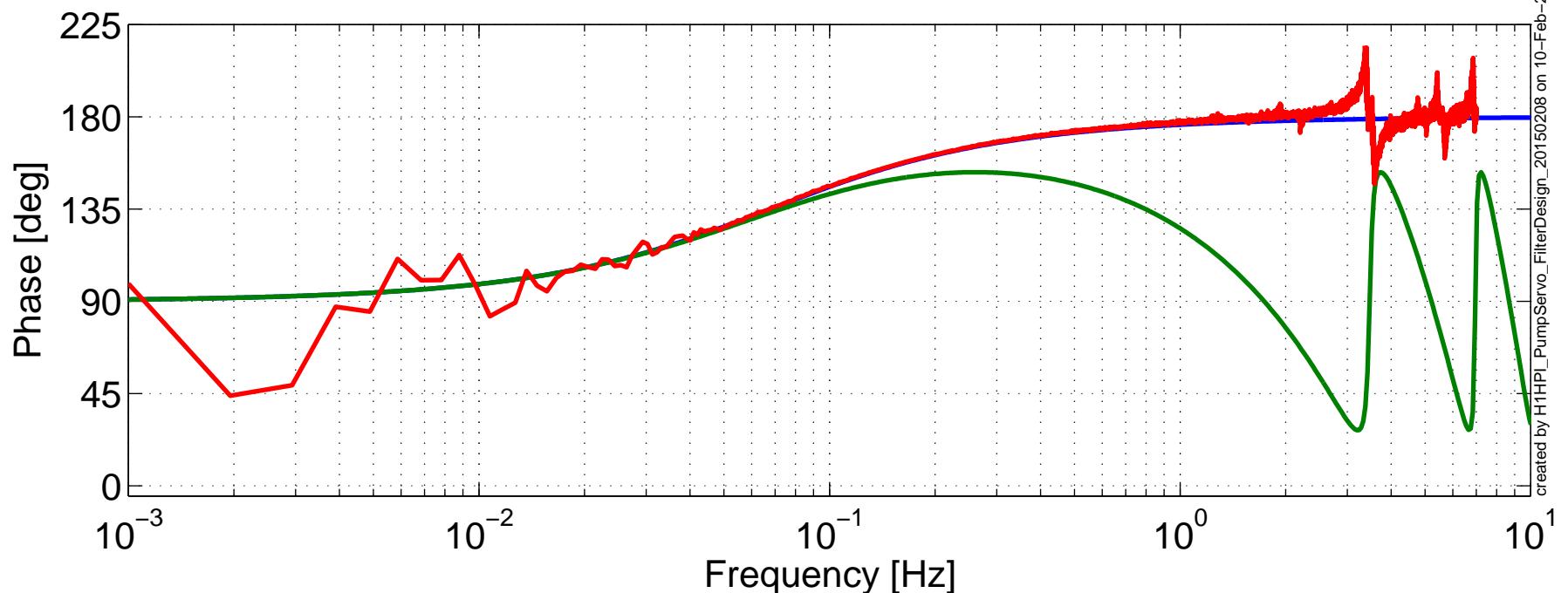
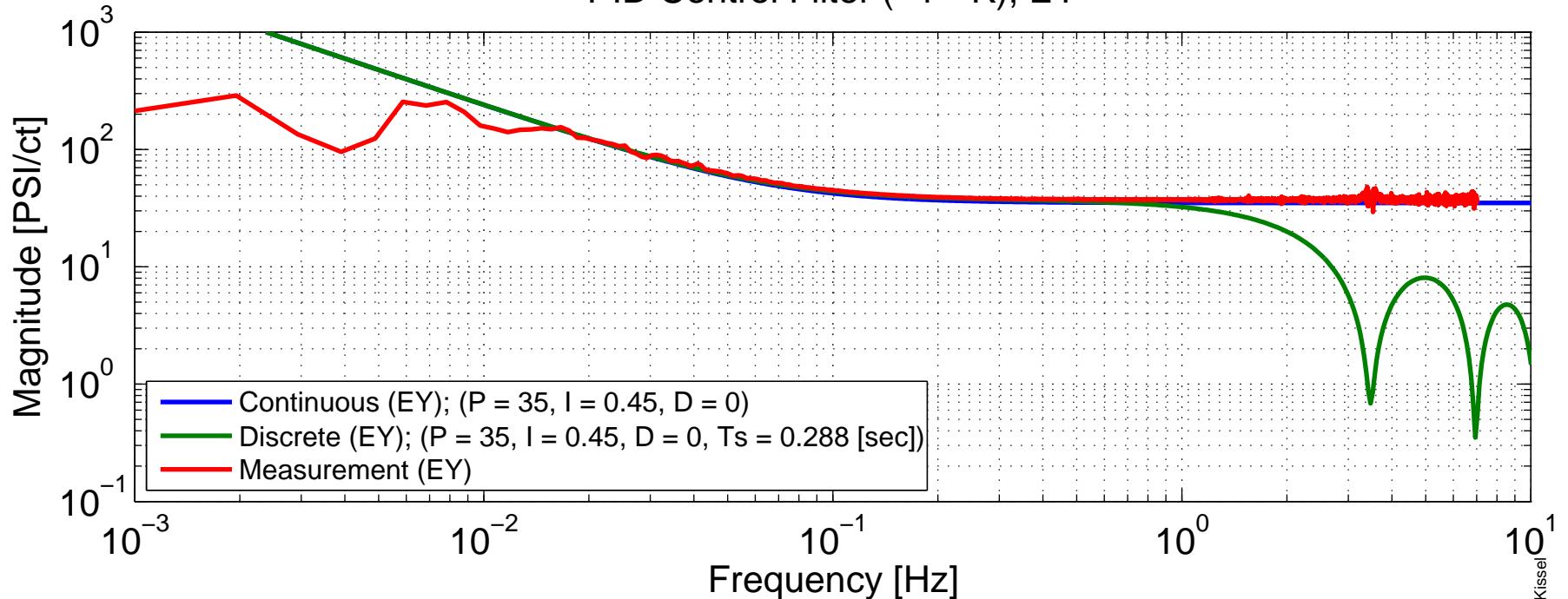
2015–02–10 H1 HPI Pump Servo
Predicted Closed Loop ASD, EX



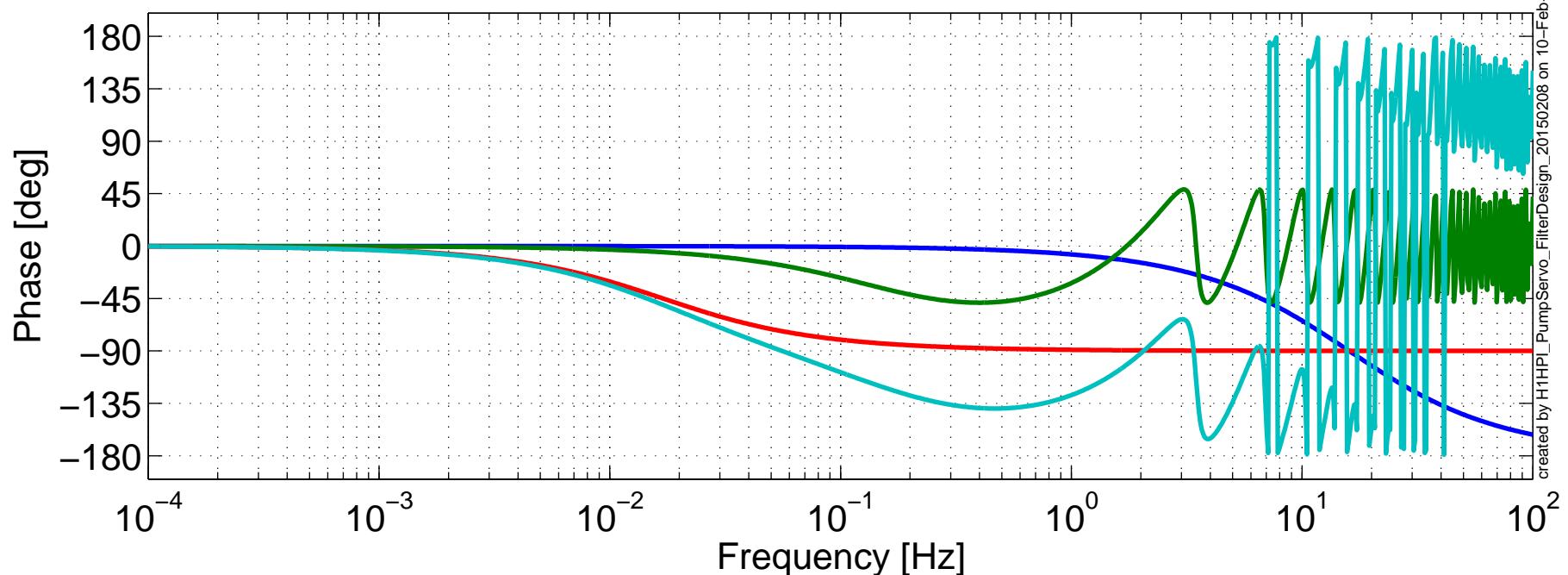
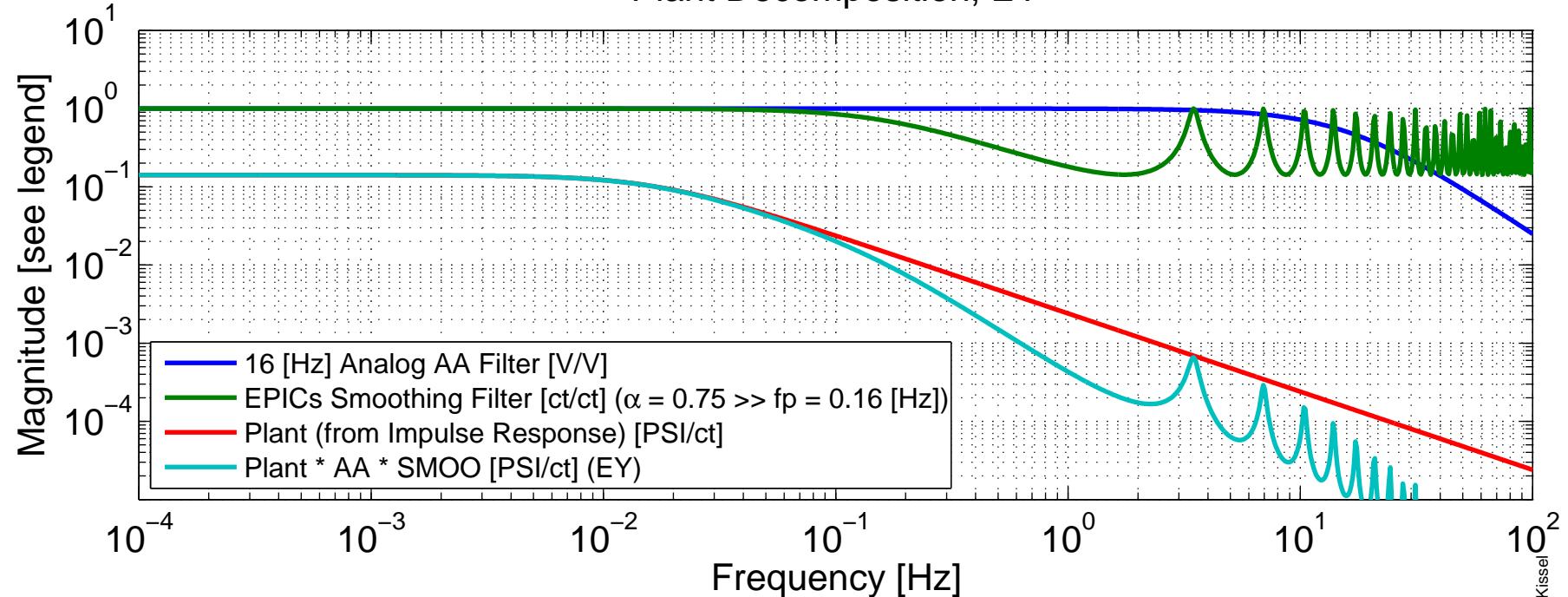
2015-02-10 H1 HPI Pump Servo Design
EPICs Clock Cycle Time, EY



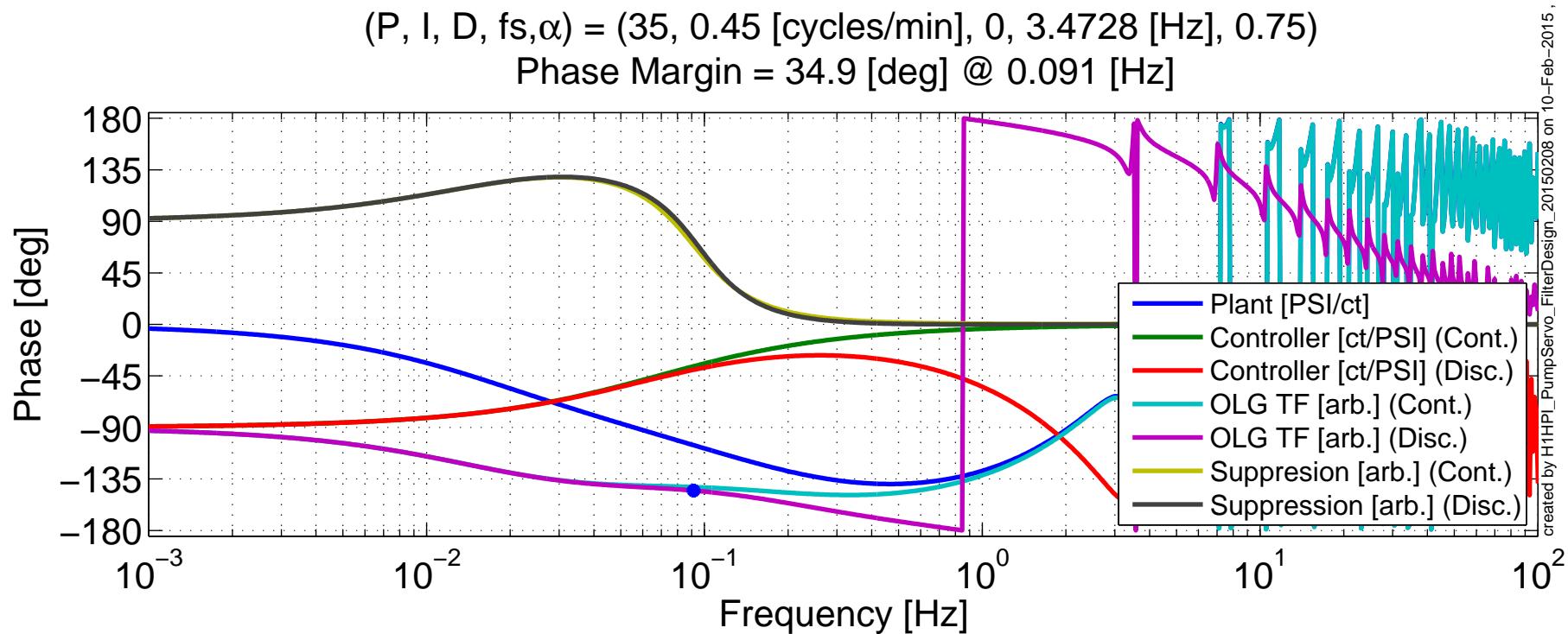
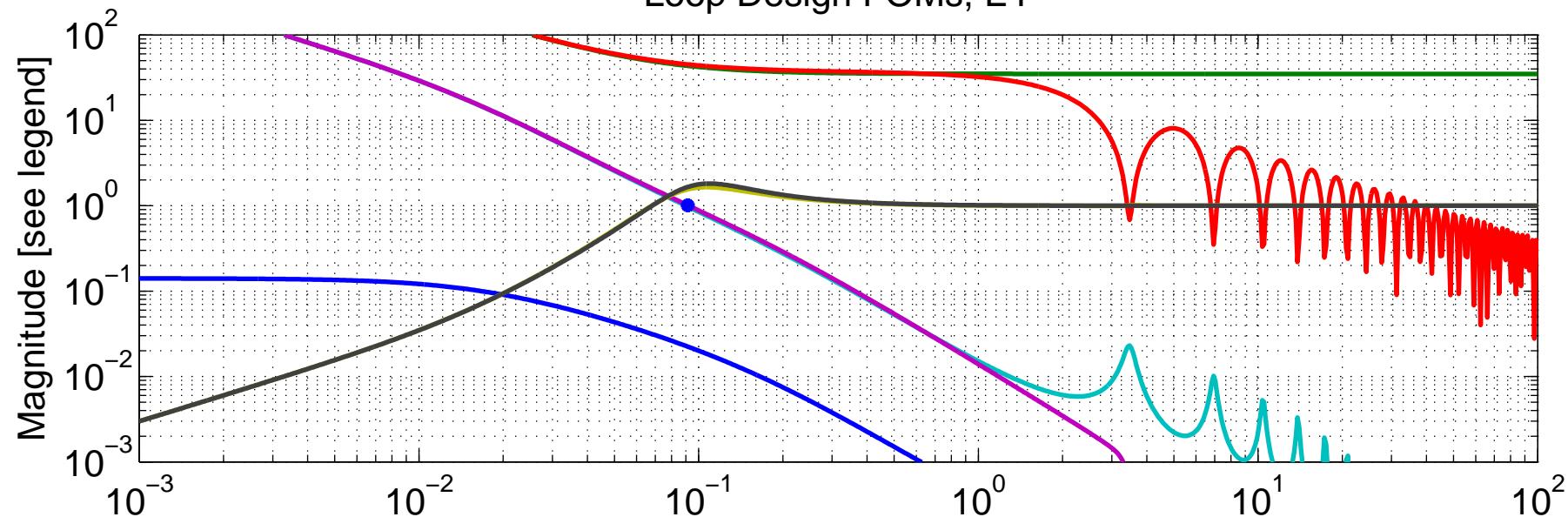
HEPI Pump Servo Design
PID Control Filter ($-1 * K$), EY



2015–02–10 H1 HPI Pump Servo
Plant Decomposition, EY



2015–02–10 H1 HPI Pump Servo
Loop Design FOMs, EY



2015–02–10 H1 HPI Pump Servo
Predicted Closed Loop ASD, EY

