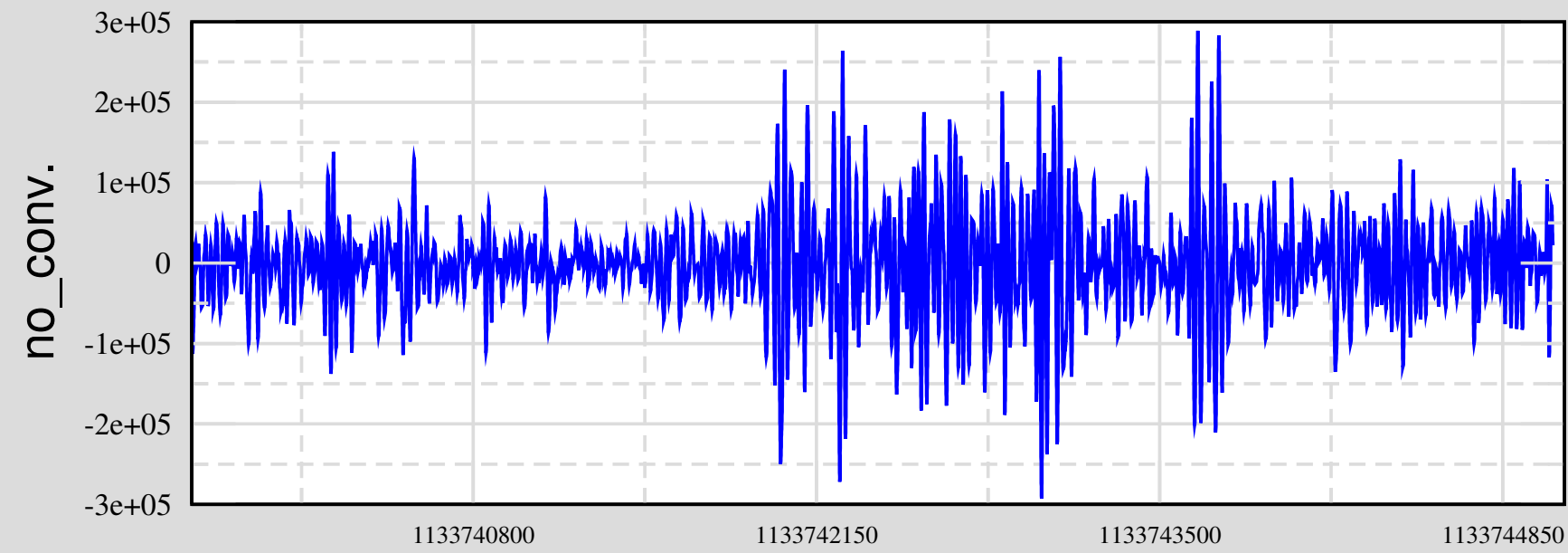
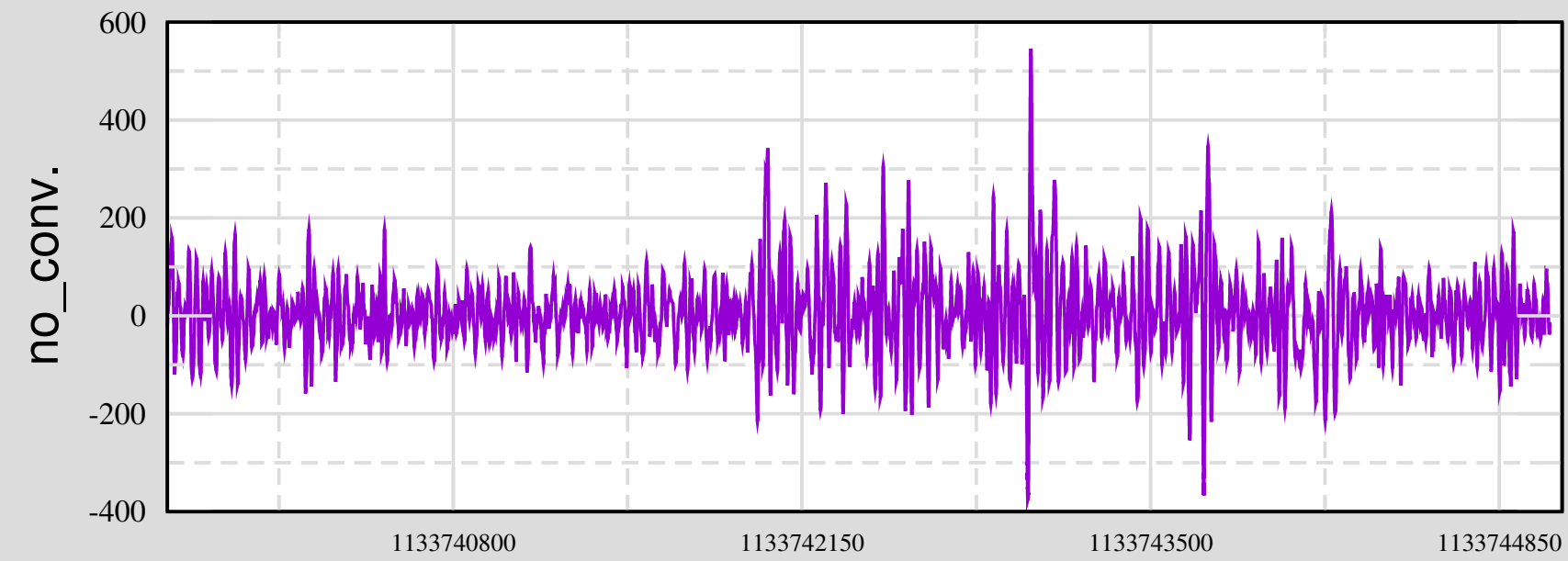


Data Display start at 15-12-09-23-41-17 (5354 seconds)

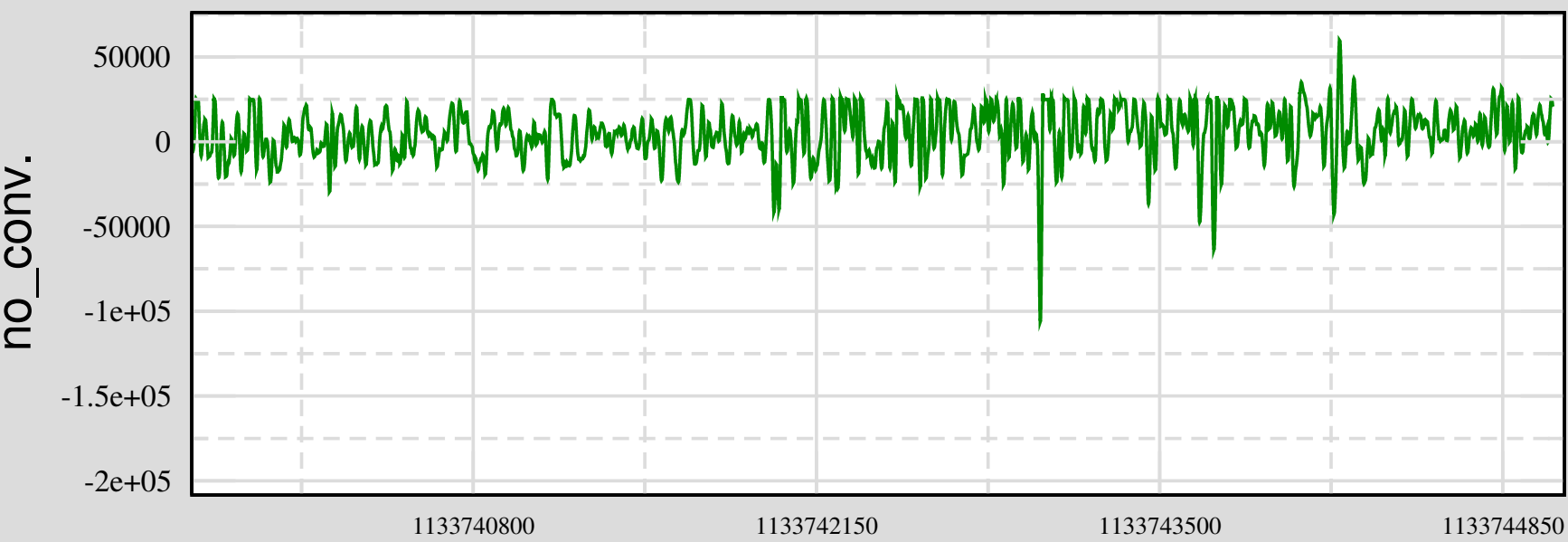
Ch 3: H1:SUS-ETMY_L1_DRIVEALIGN_L_INMON



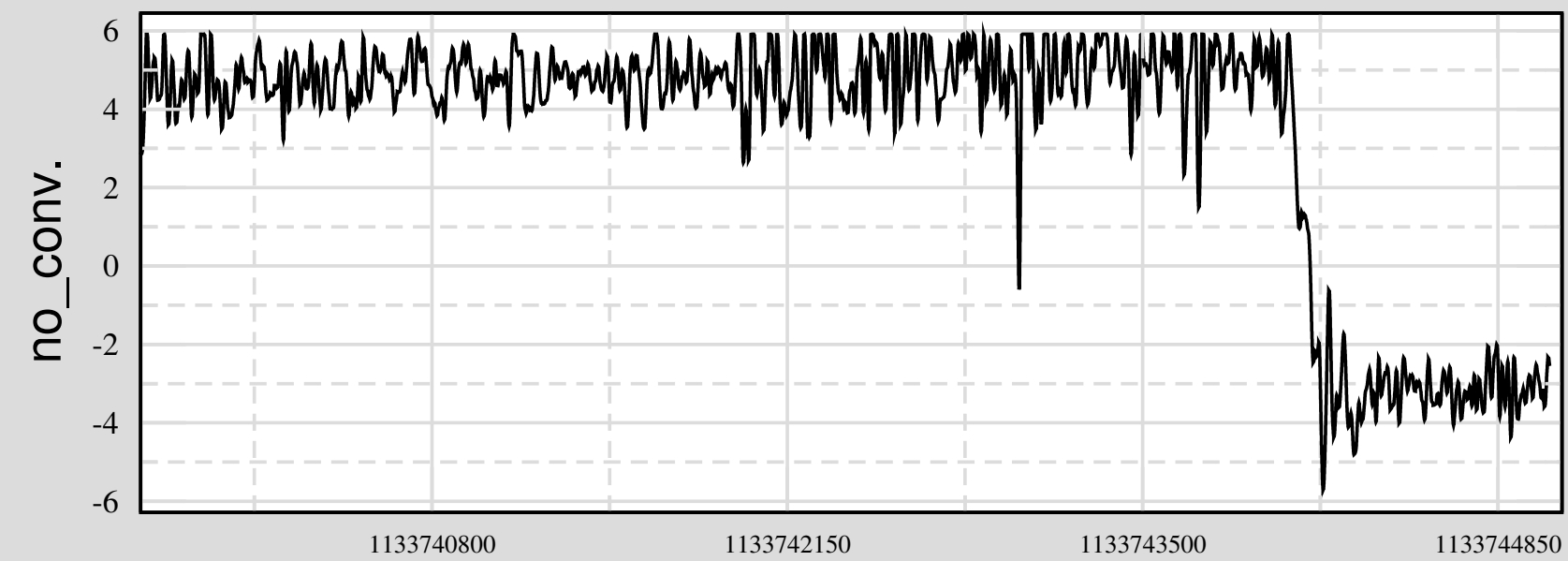
Ch 6: H1:IMC-F_OUT16



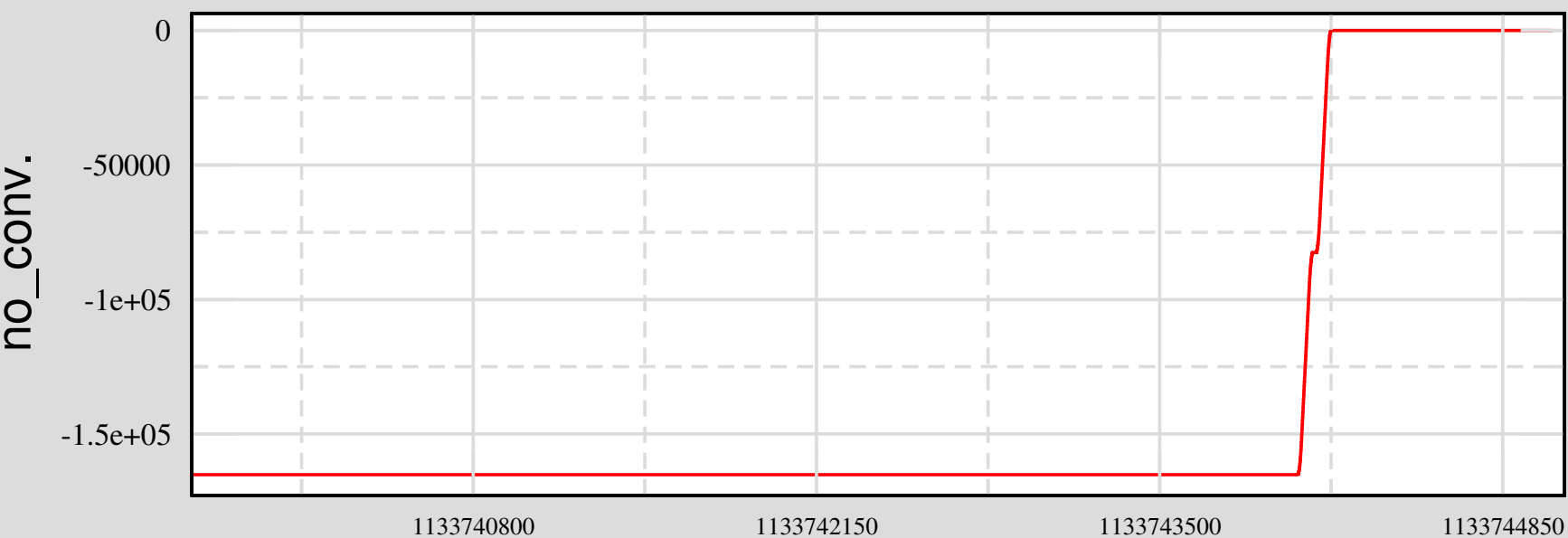
Ch 2: H1:SUS-ETMX_L1_DRIVEALIGN_L_INMON



Ch 5: H1:LSC-Y_ARM_MON



Ch 1: H1:SUS-ETMX_L1_LOCK_L_OUTPUT



Ch 4: H1:LSC-X_ARM_MON

