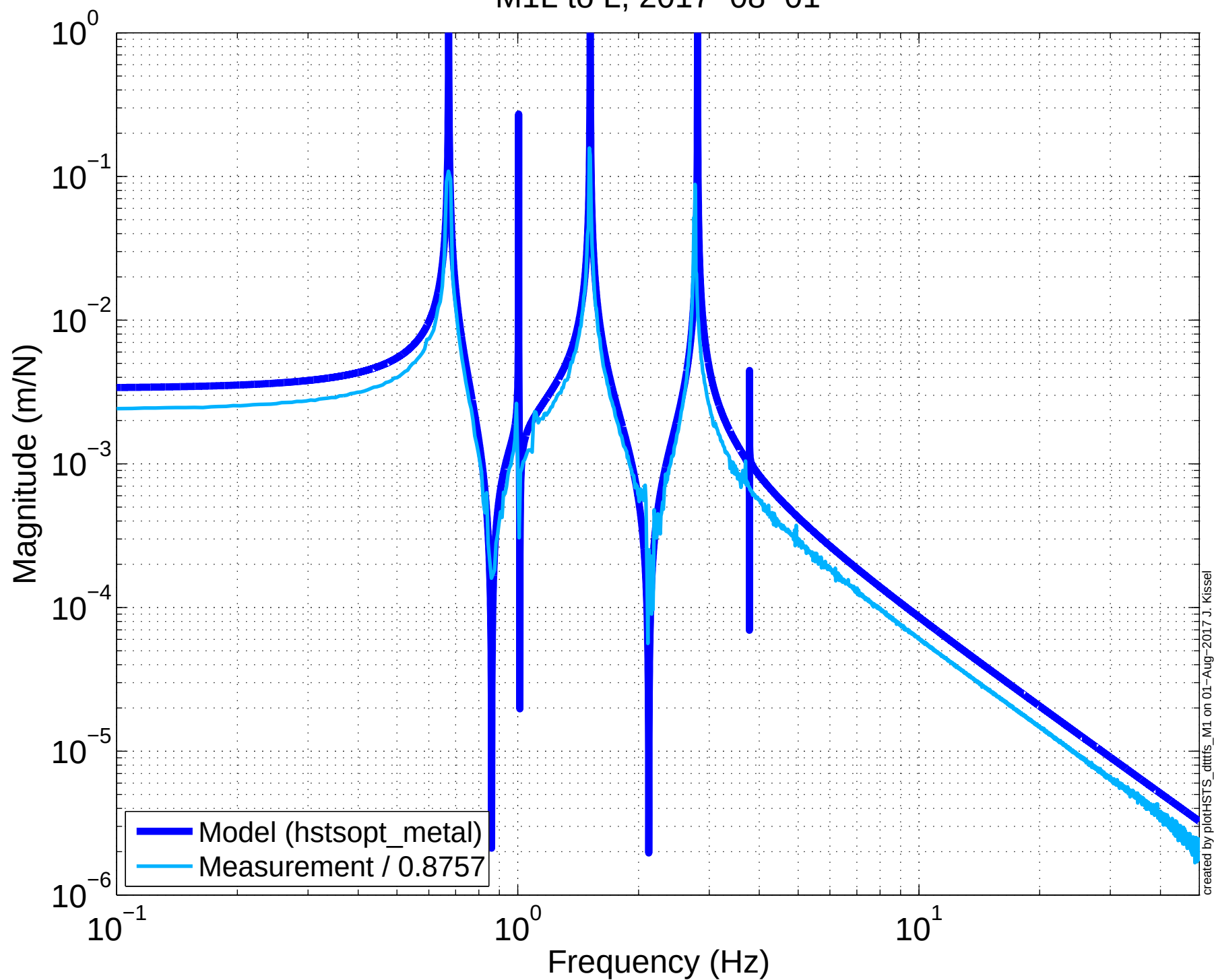
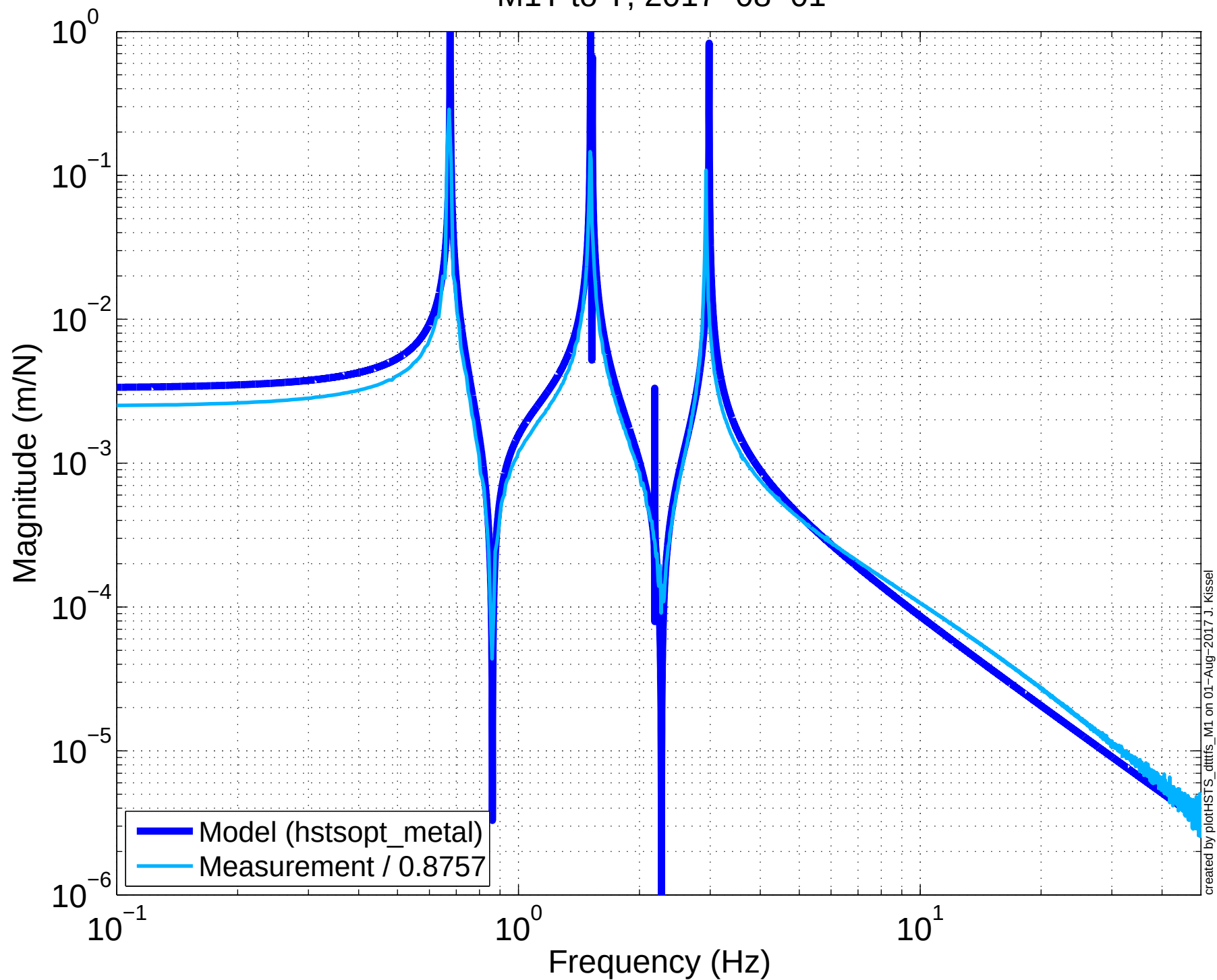


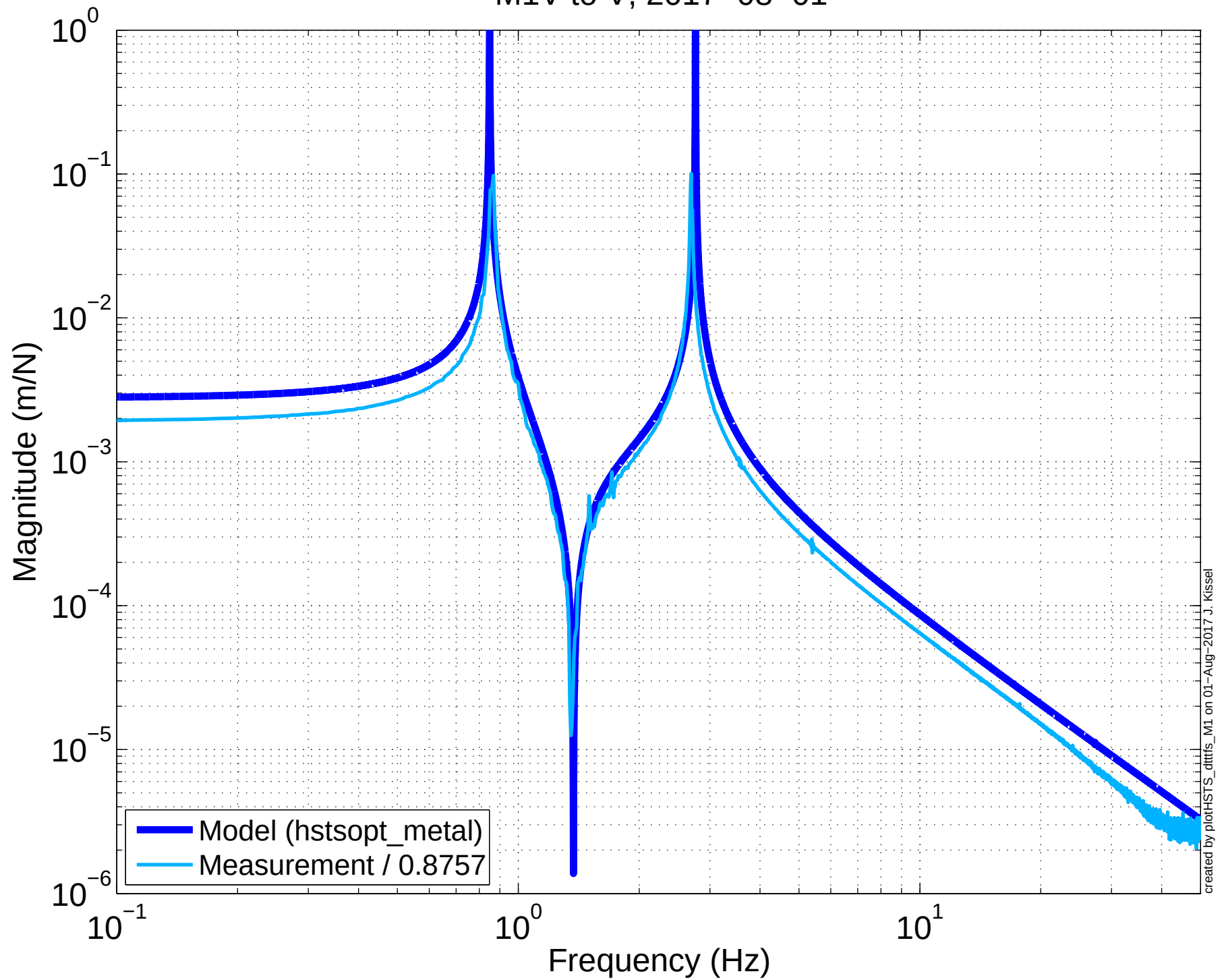
H1SUSMC2 BUILD
M1L to L, 2017-08-01



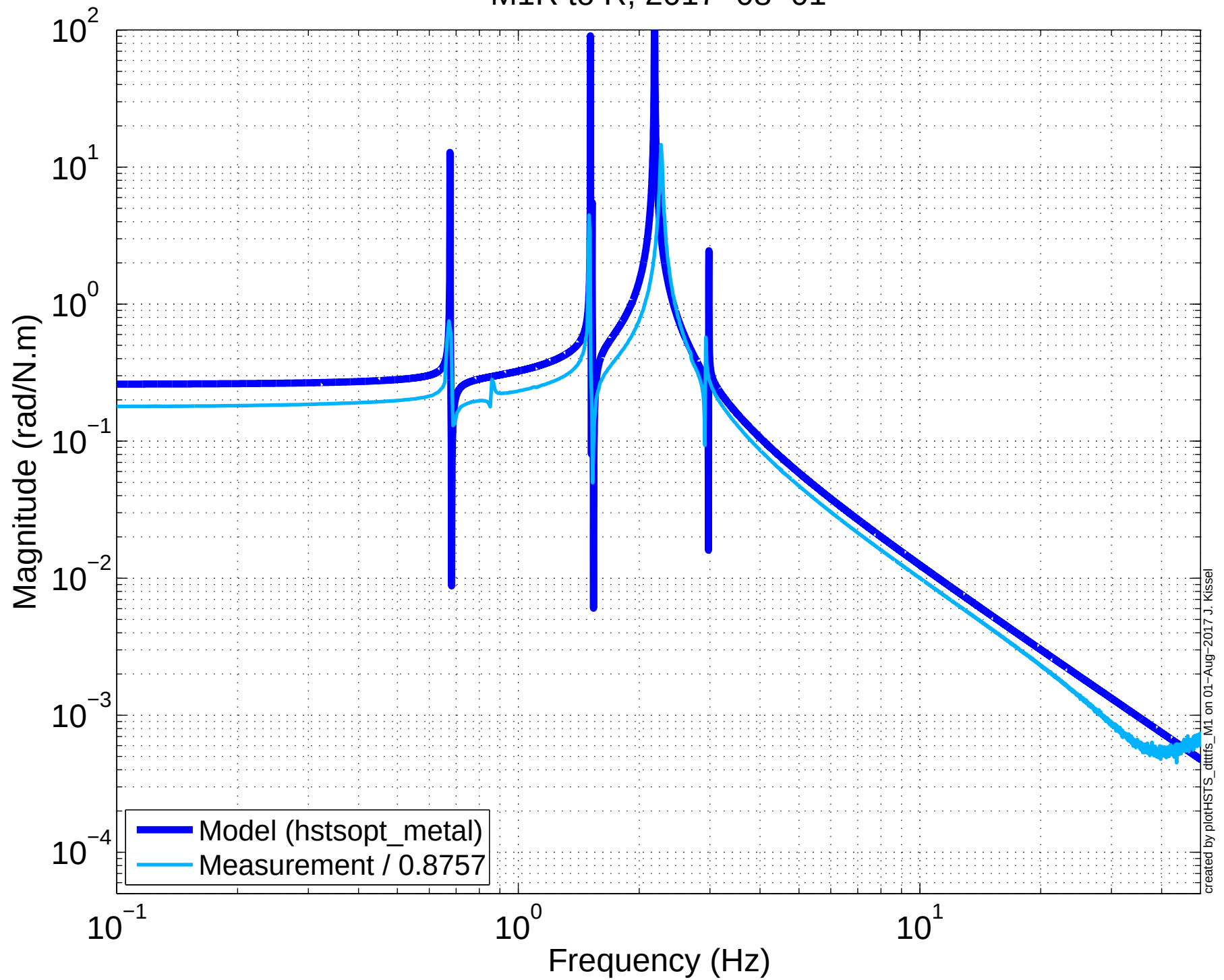
H1SUSMC2 BUILD
M1T to T, 2017-08-01



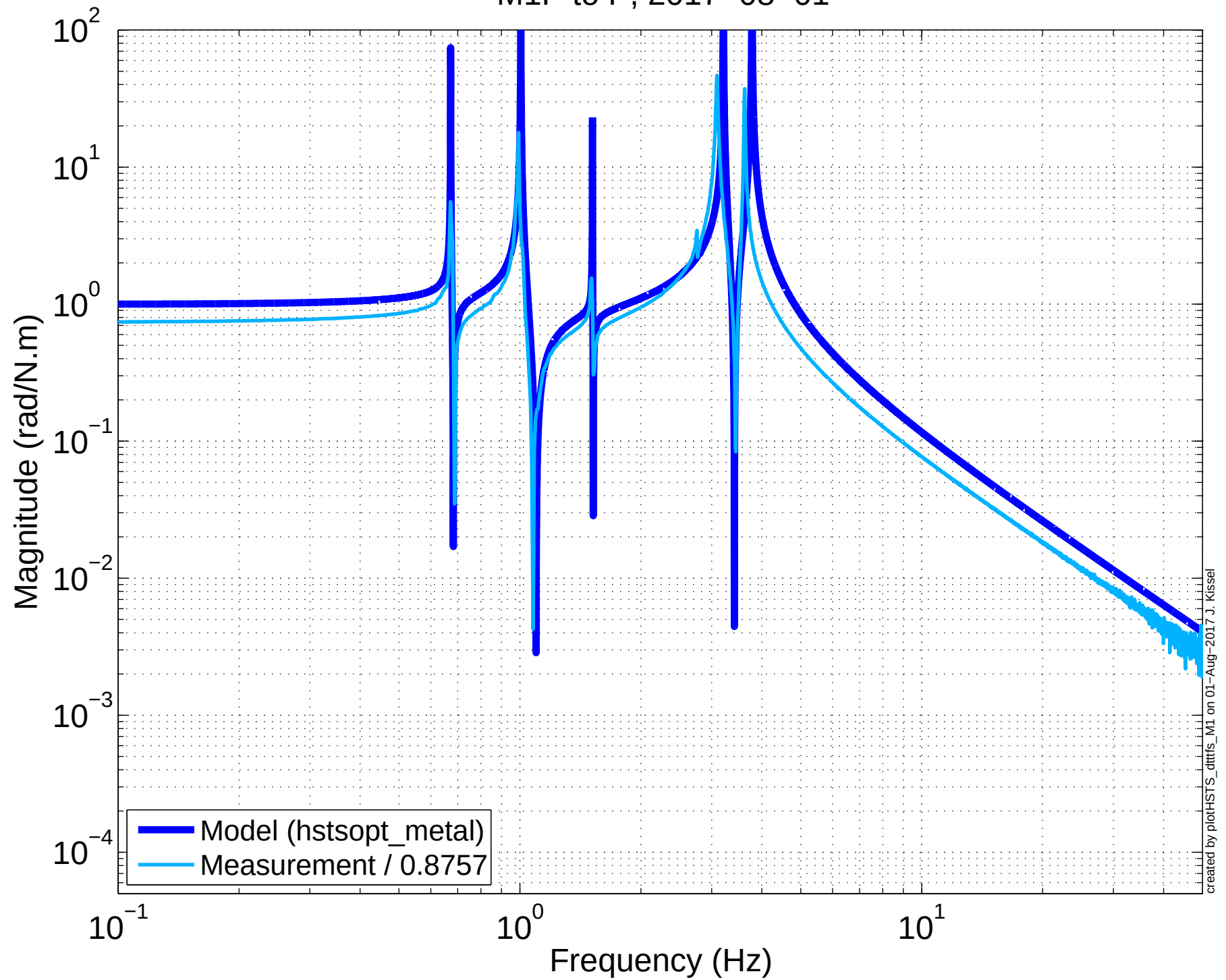
H1SUSMC2 BUILD
M1V to V, 2017-08-01



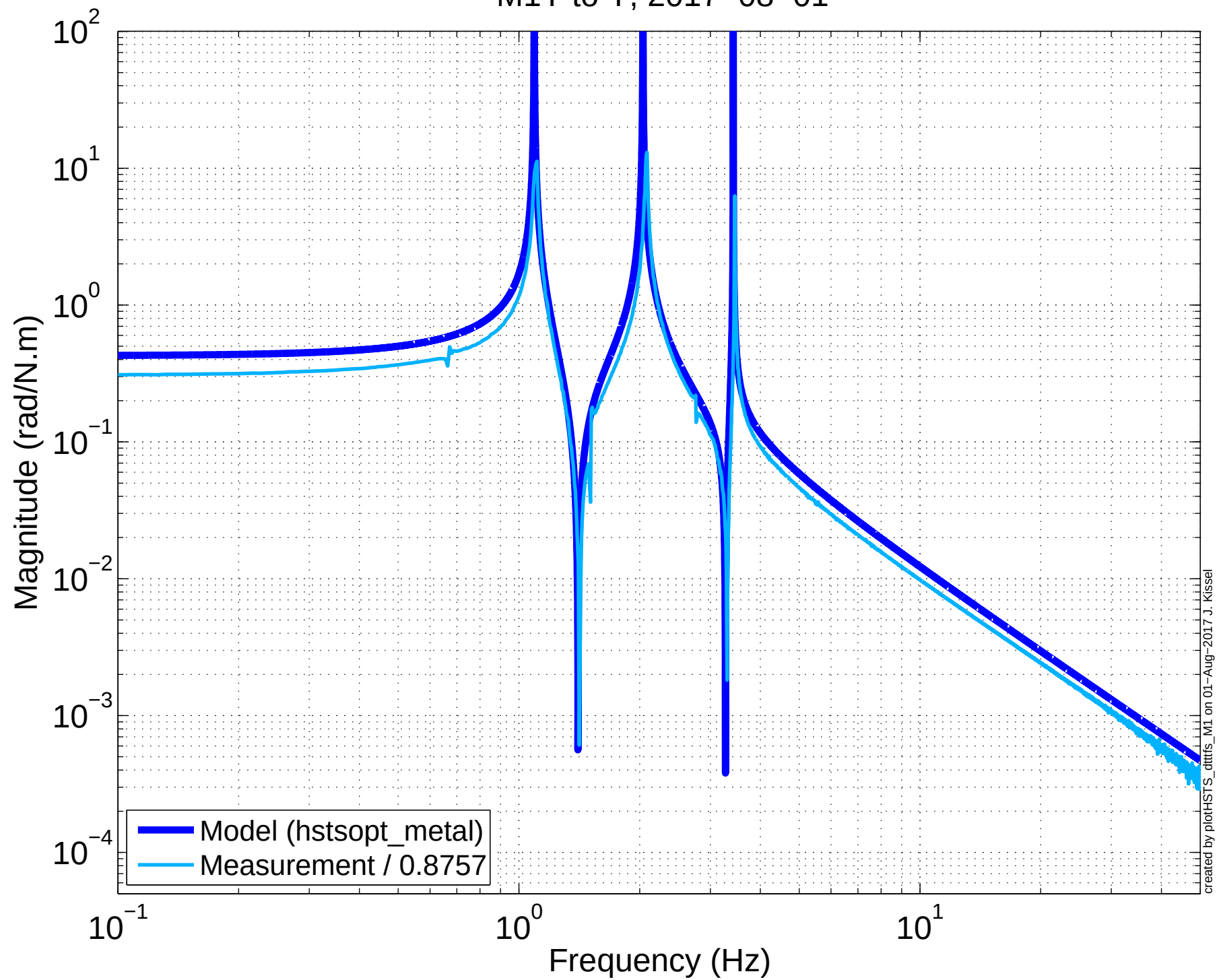
H1SUSMC2 BUILD
M1R to R, 2017-08-01



H1SUSMC2 BUILD
M1P to P, 2017-08-01

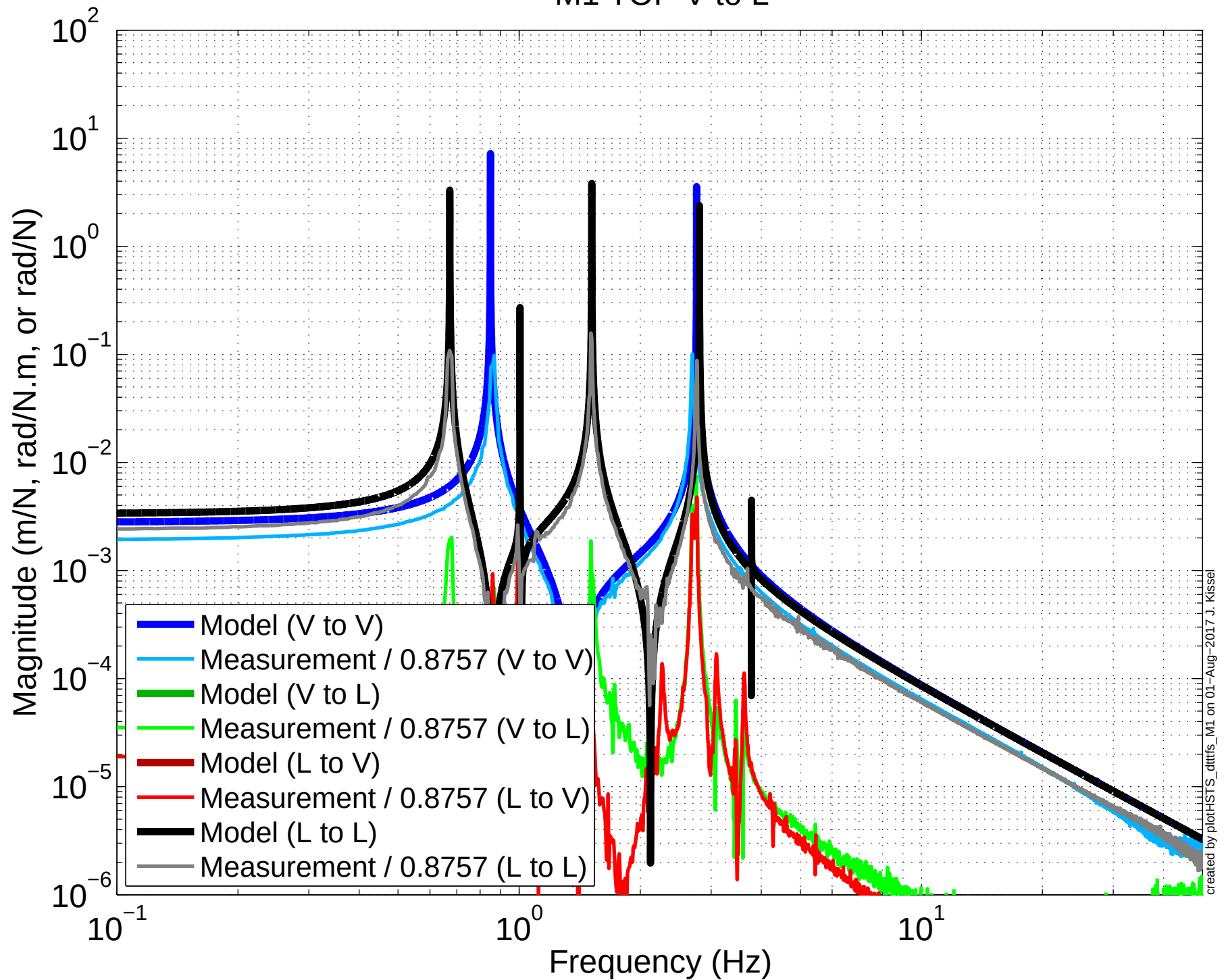


H1SUSMC2 BUILD
M1Y to Y, 2017-08-01



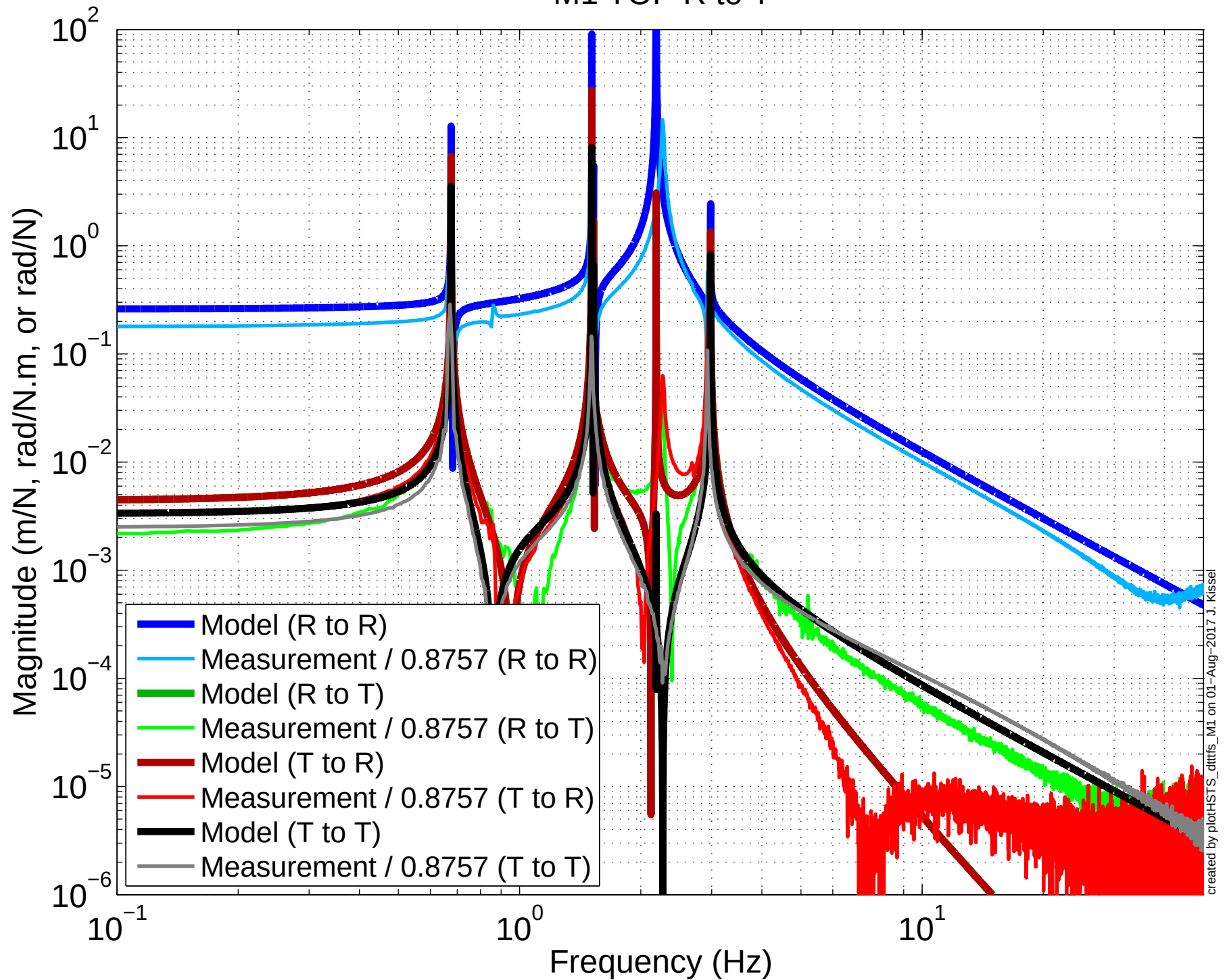
H1SUSMC2 BUILD

M1 TOP V to L



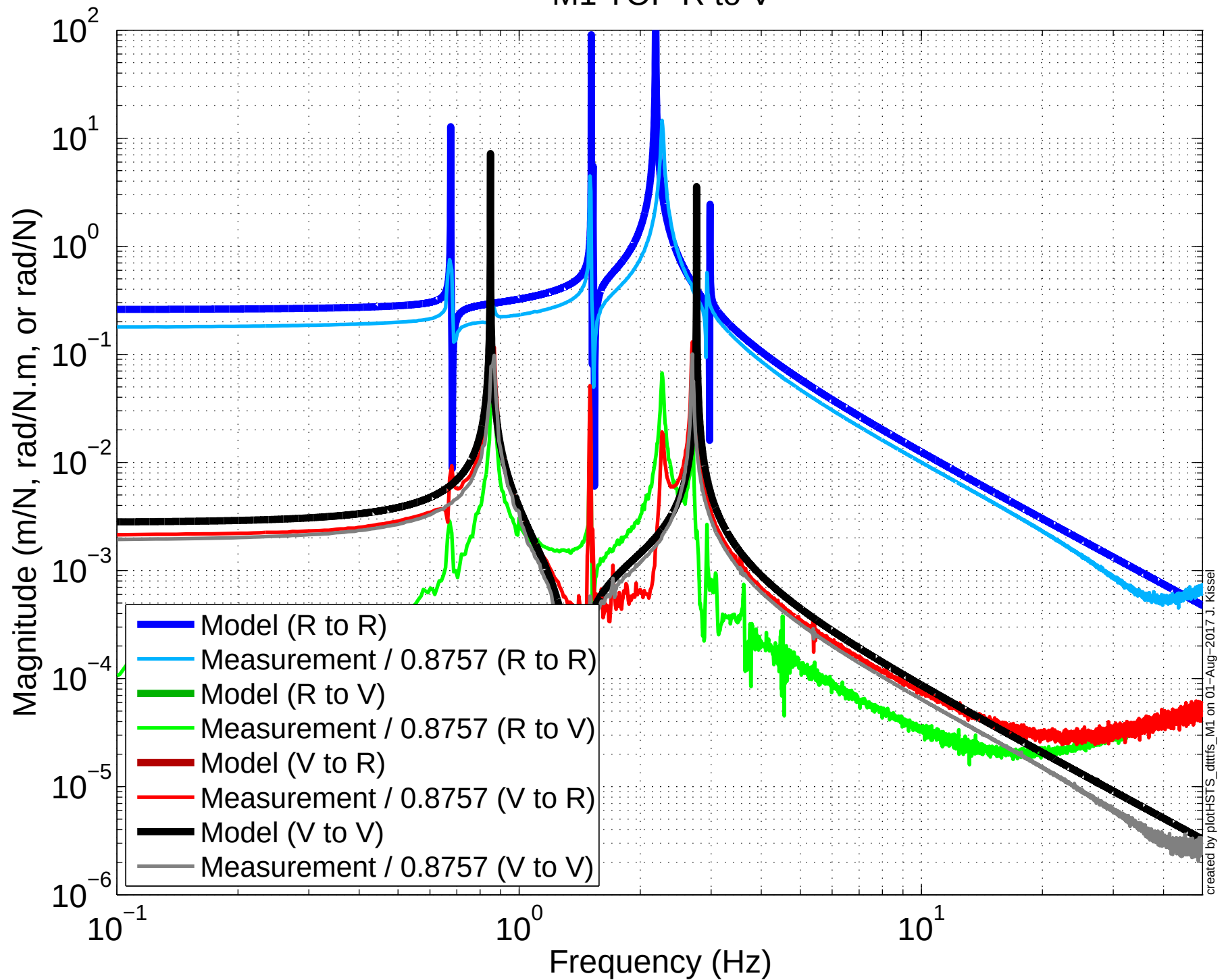
H1SUSMC2 BUILD

M1 TOP R to T



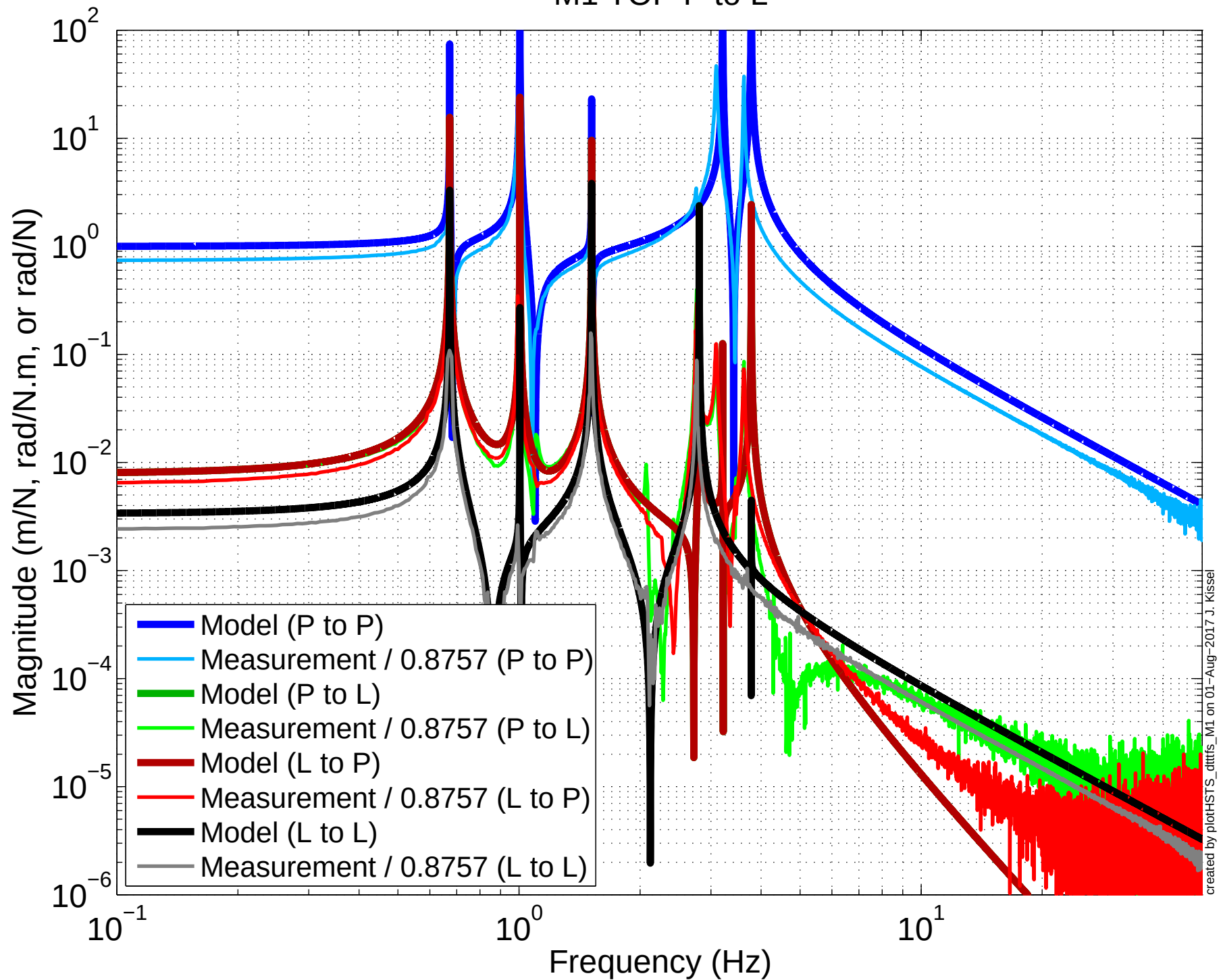
H1SUSMC2 BUILD

M1 TOP R to V



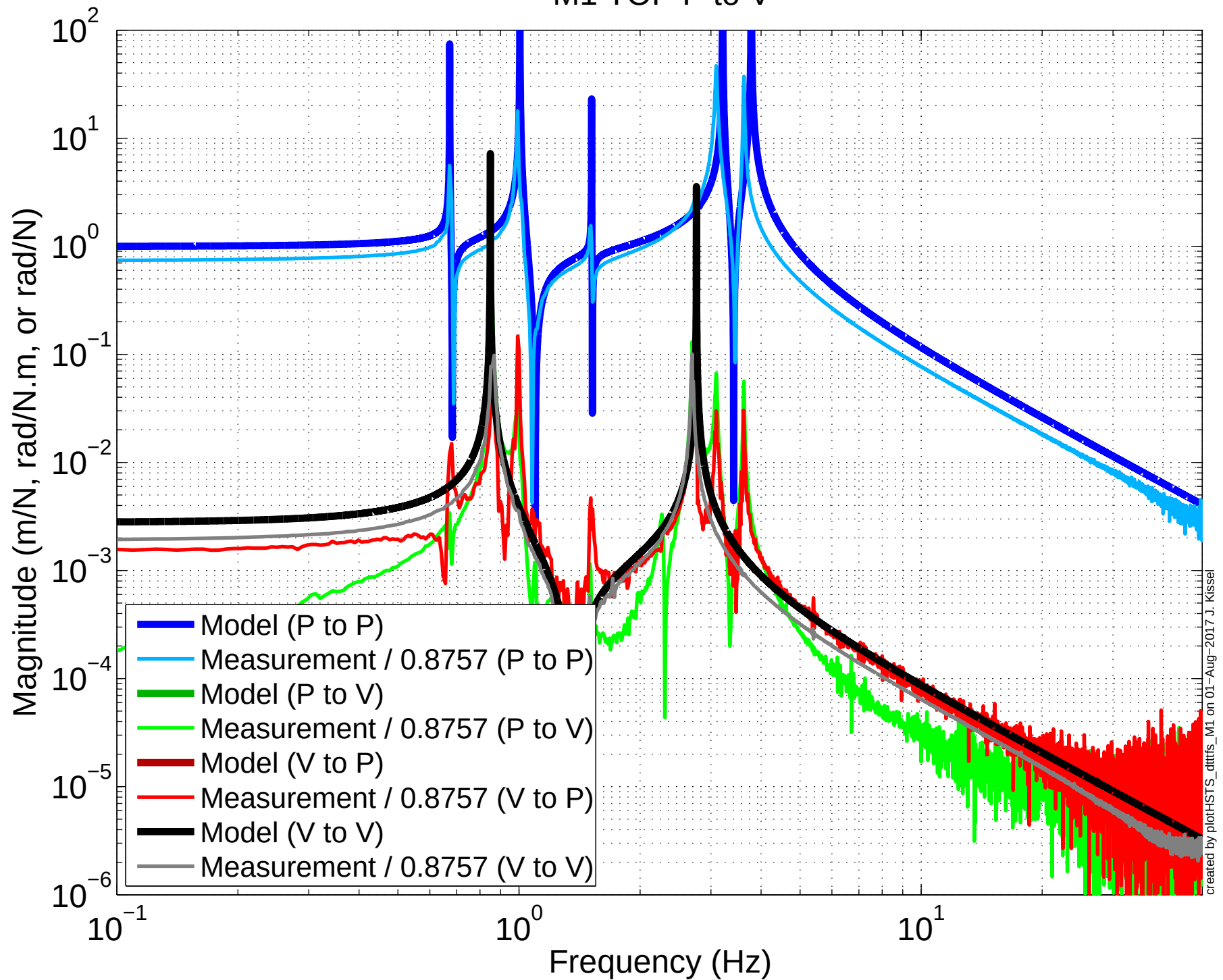
H1SUSMC2 BUILD

M1 TOP P to L



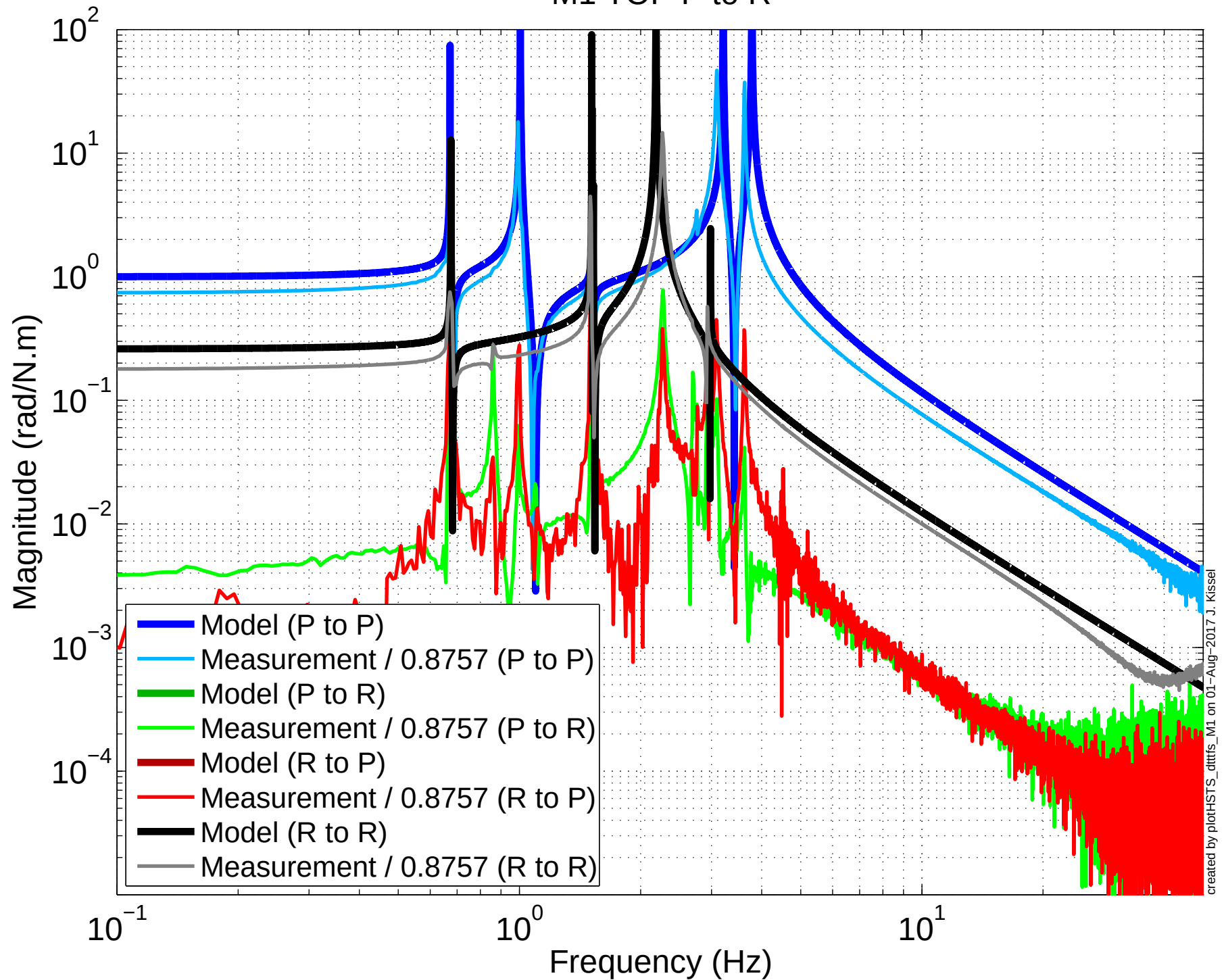
H1SUSMC2 BUILD

M1 TOP P to V



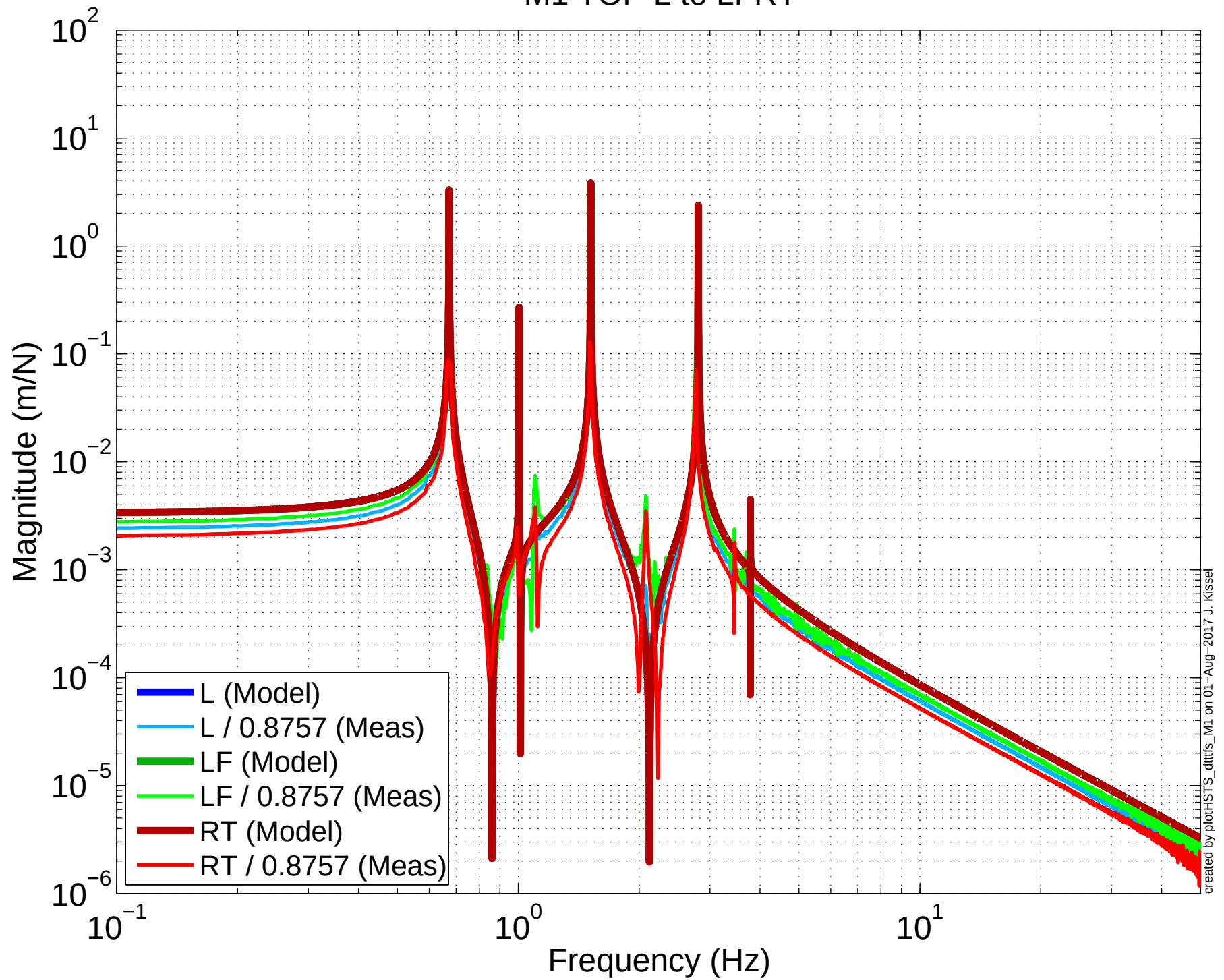
H1SUSMC2 BUILD

M1 TOP P to R



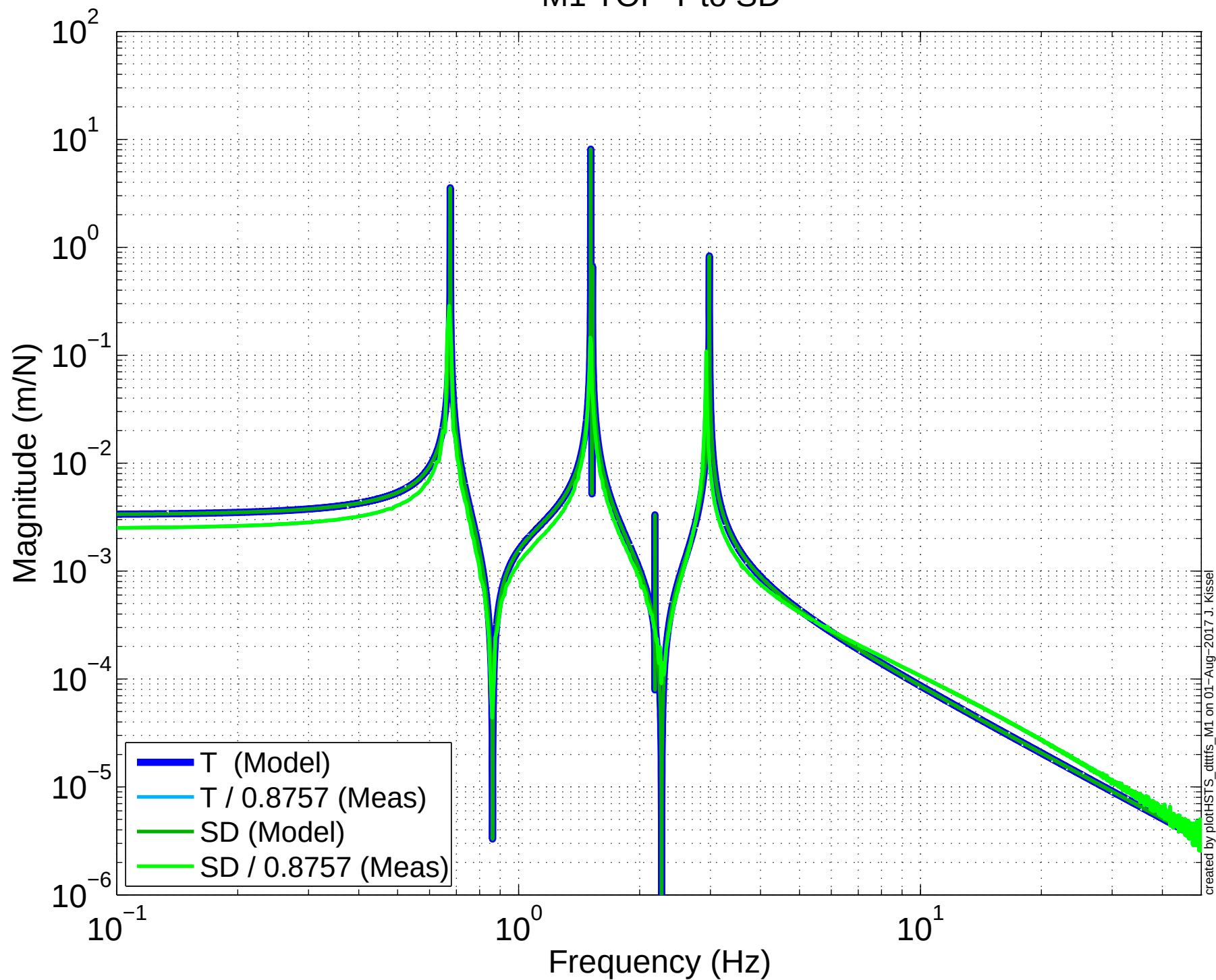
H1SUSMC2 BUILD

M1 TOP L to LFRT



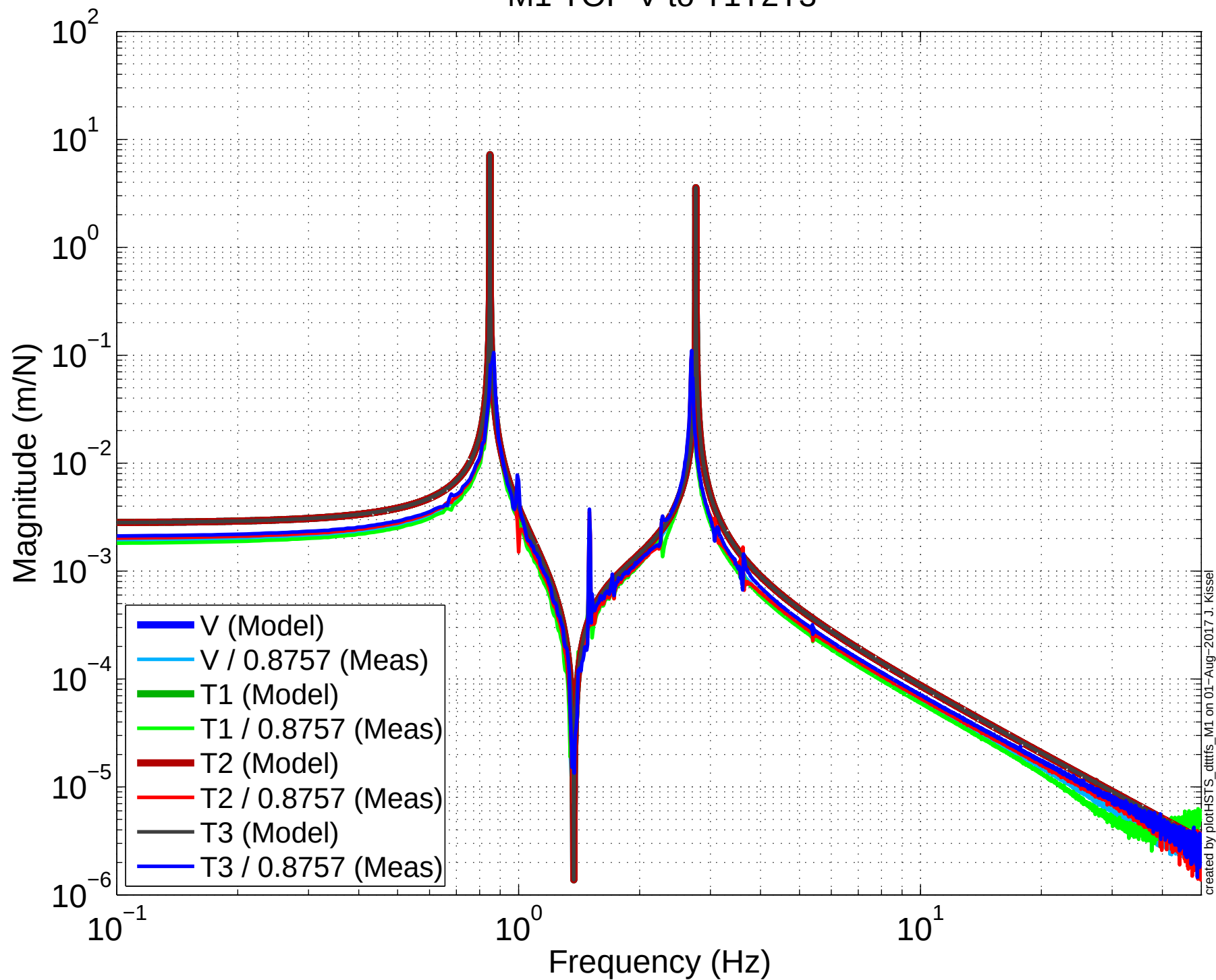
H1SUSMC2 BUILD

M1 TOP T to SD



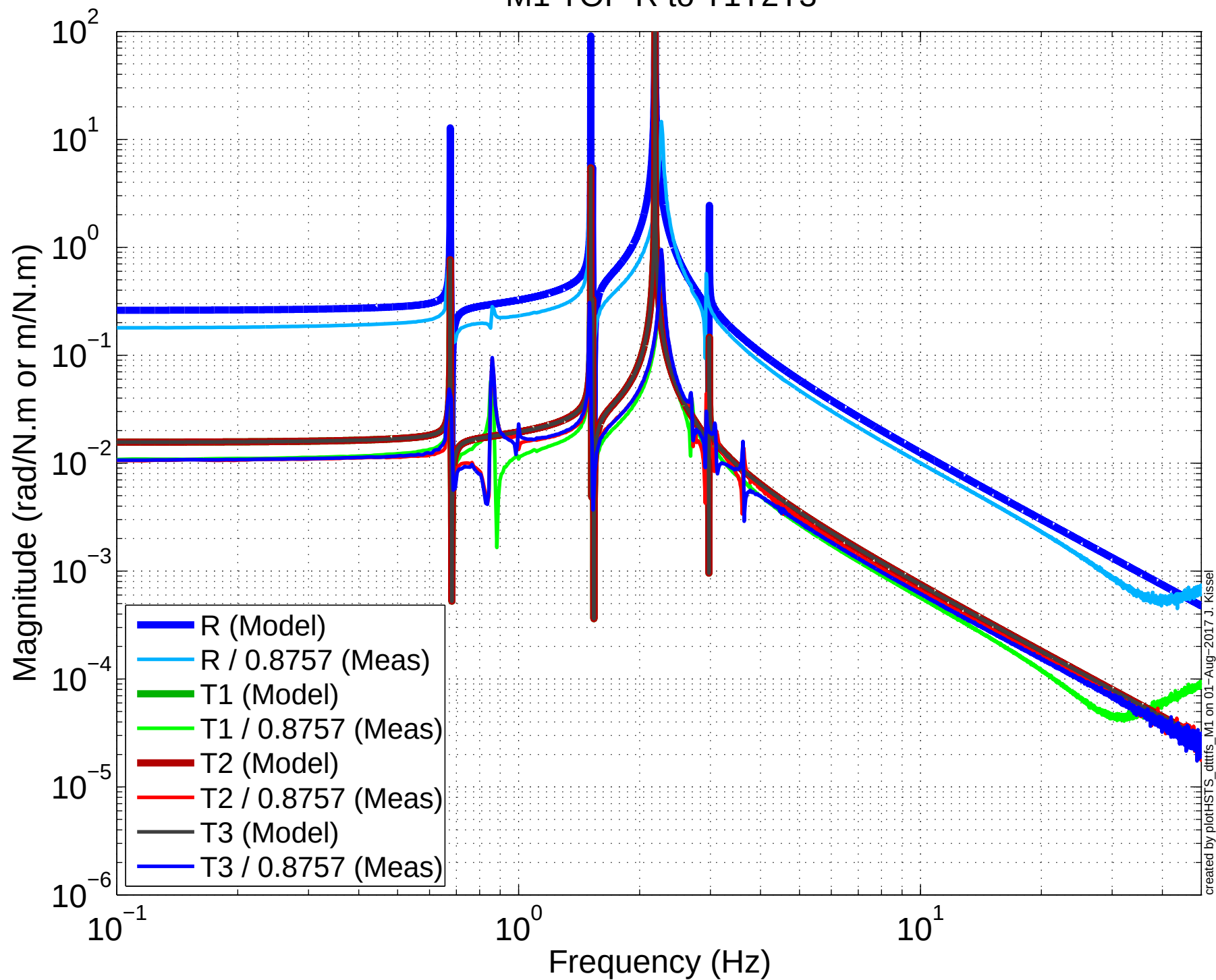
H1SUSMC2 BUILD

M1 TOP V to T1T2T3



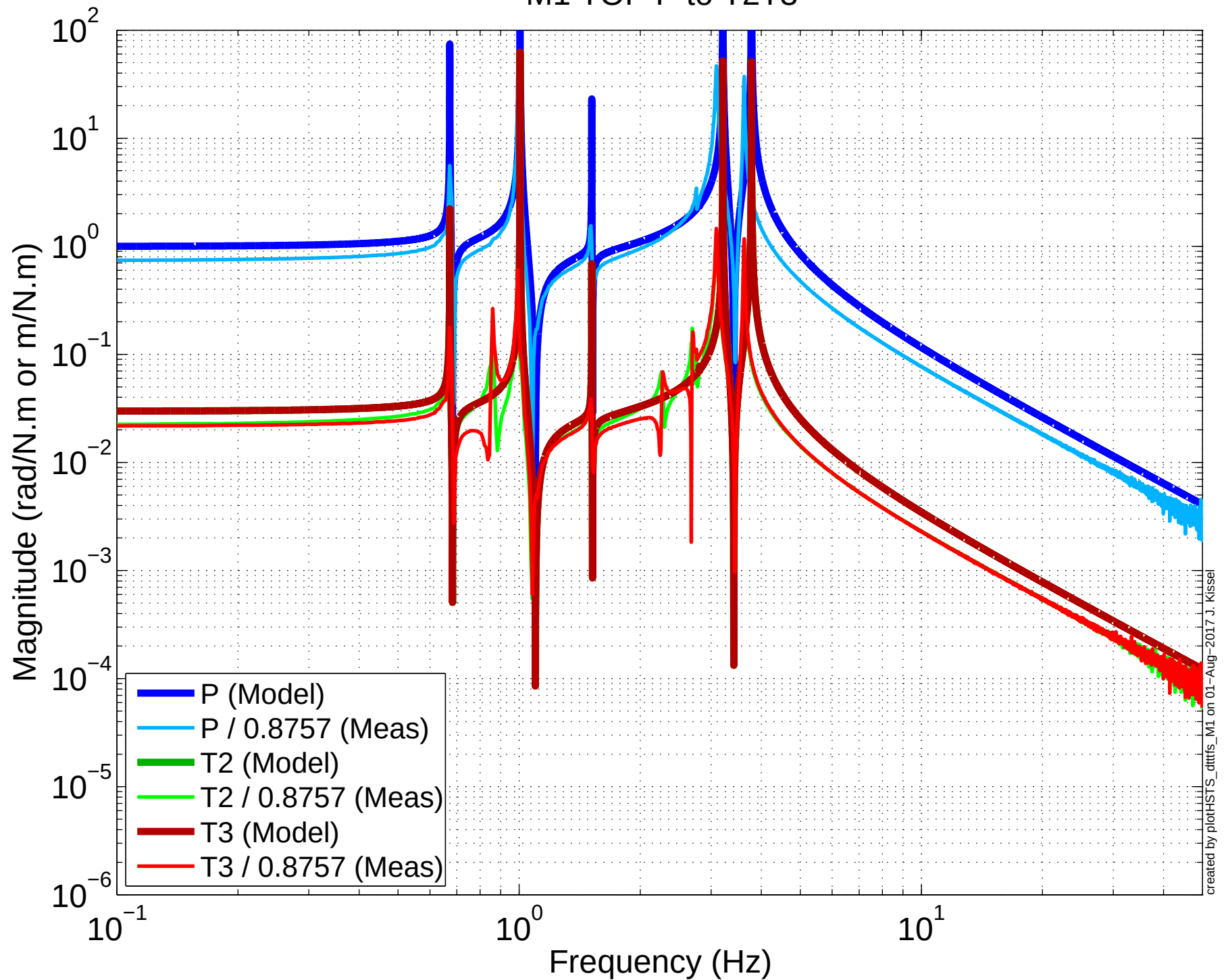
H1SUSMC2 BUILD

M1 TOP R to T1T2T3



H1SUSMC2 BUILD

M1 TOP P to T2T3



H1SUSMC2 BUILD

M1 TOP Y to LFRT

