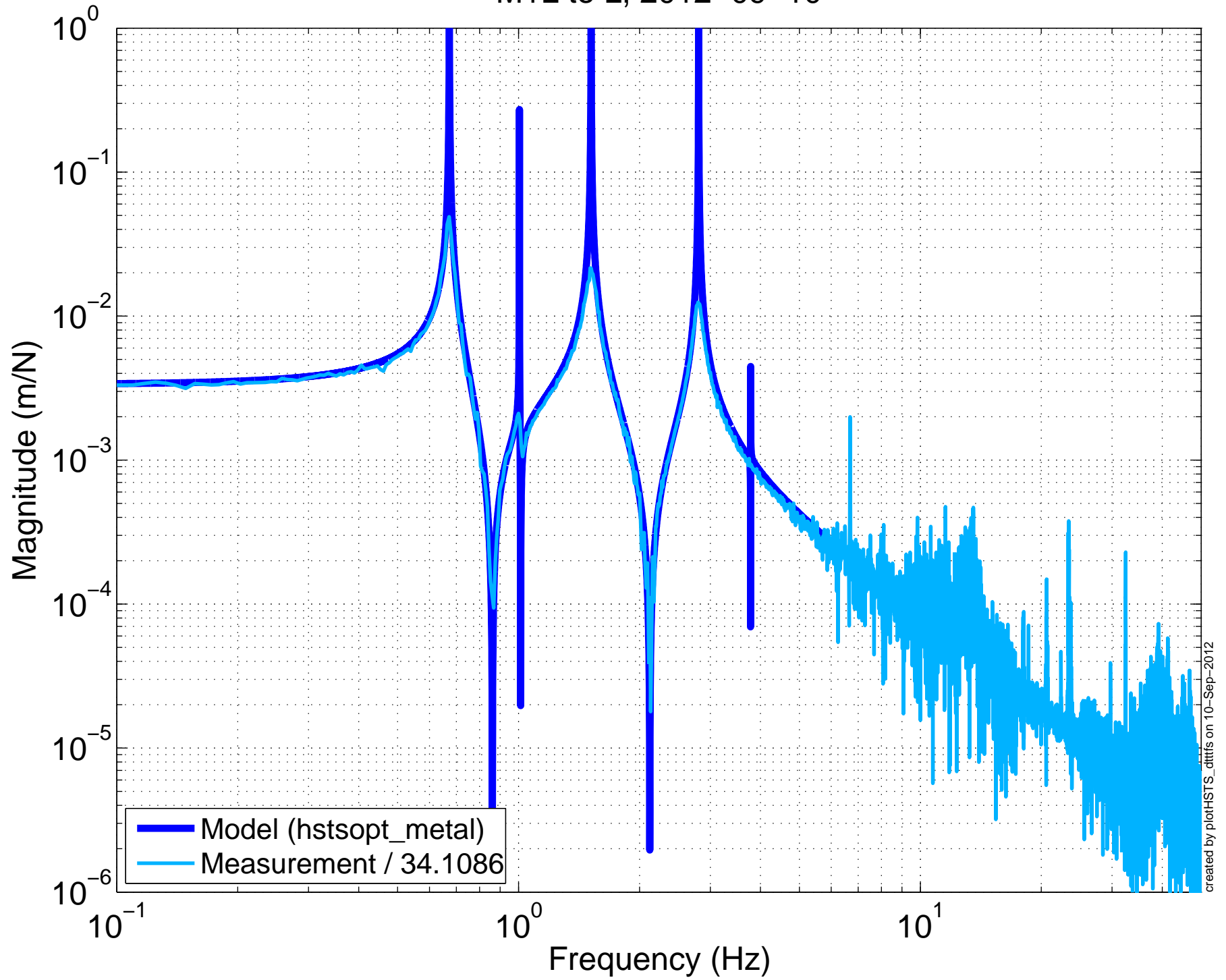
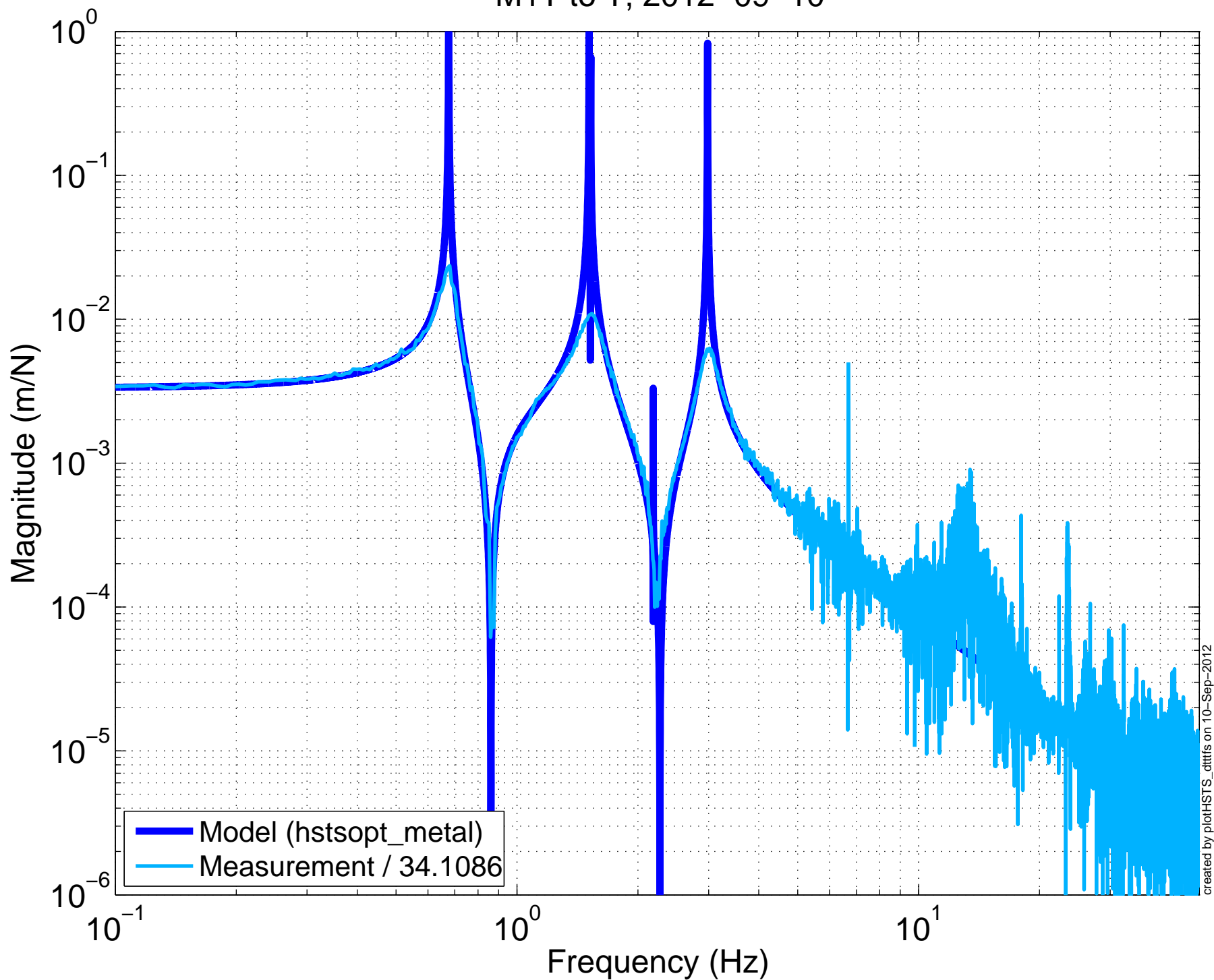


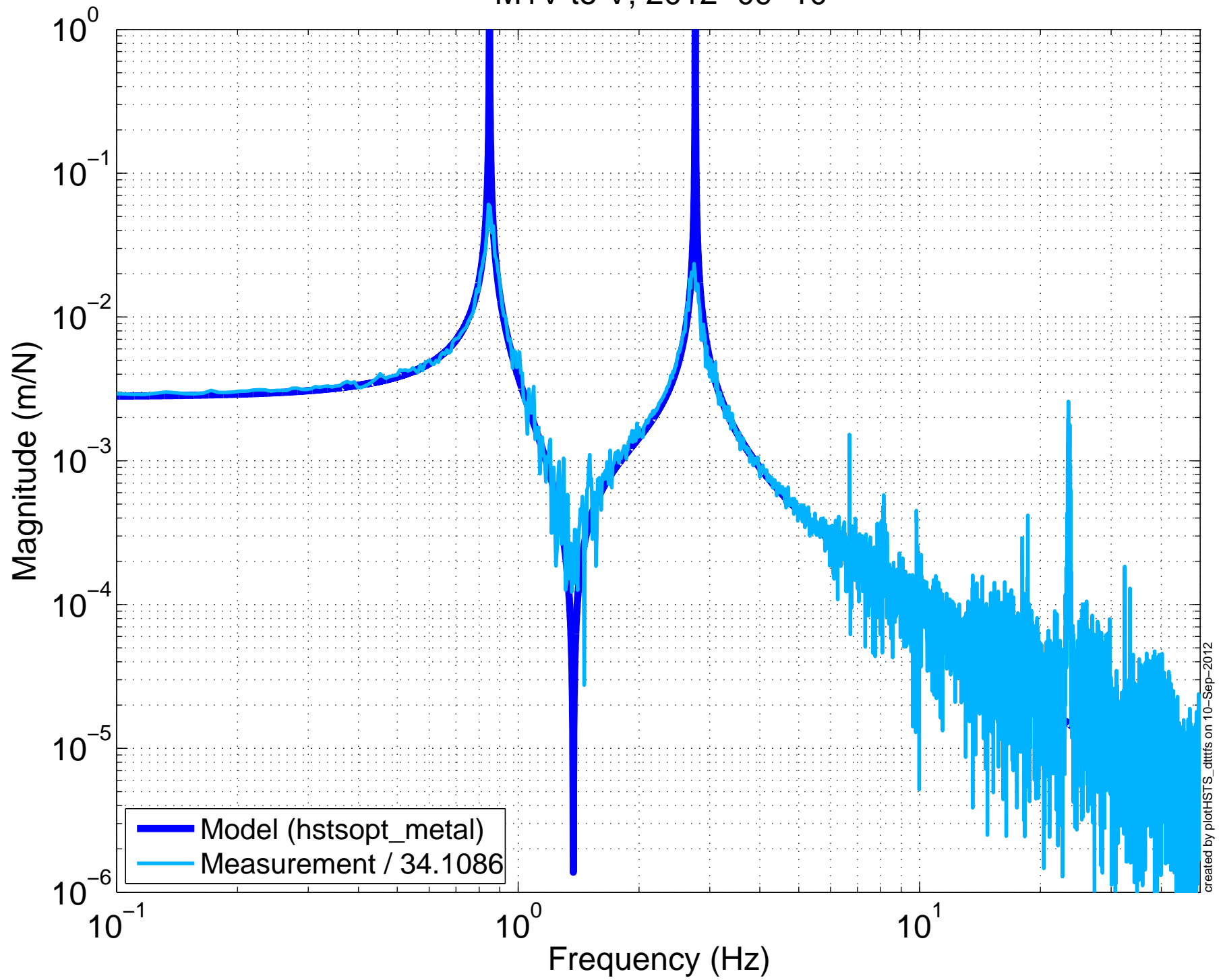
X1SUSPRM BUILD  
M1L to L, 2012-09-10



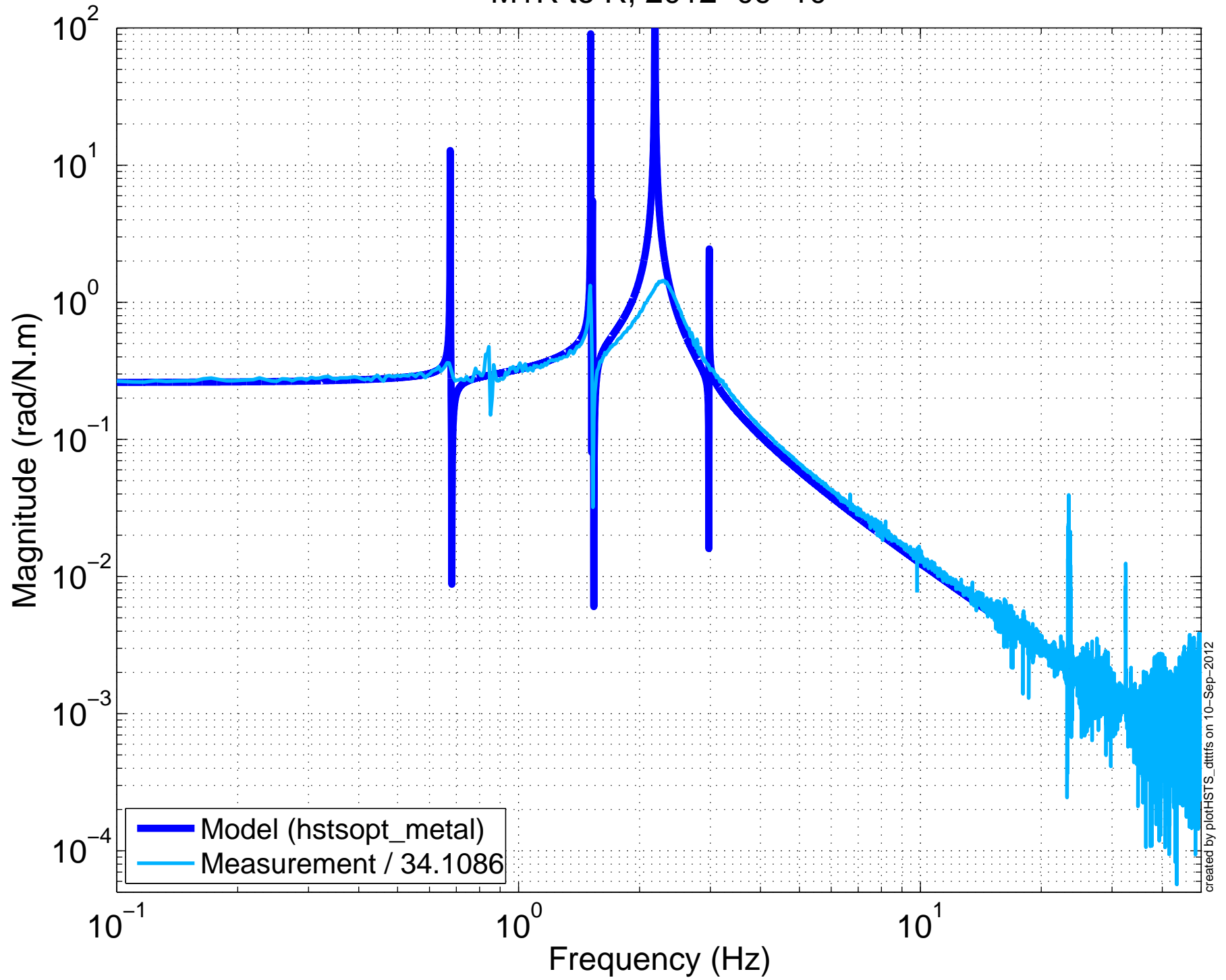
X1SUSPRM BUILD  
M1T to T, 2012-09-10



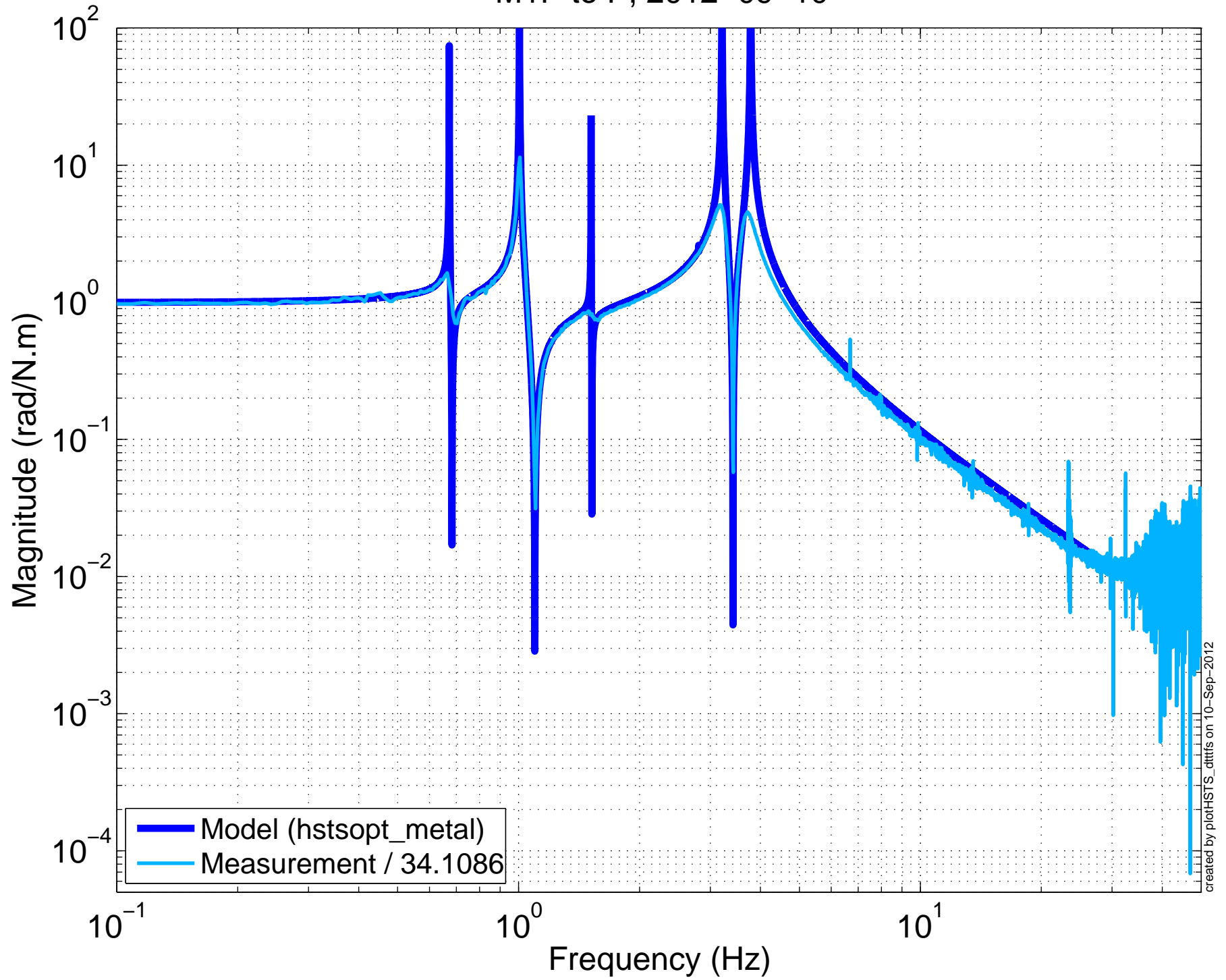
X1SUSPRM BUILD  
M1V to V, 2012-09-10



X1SUSPRM BUILD  
M1R to R, 2012-09-10

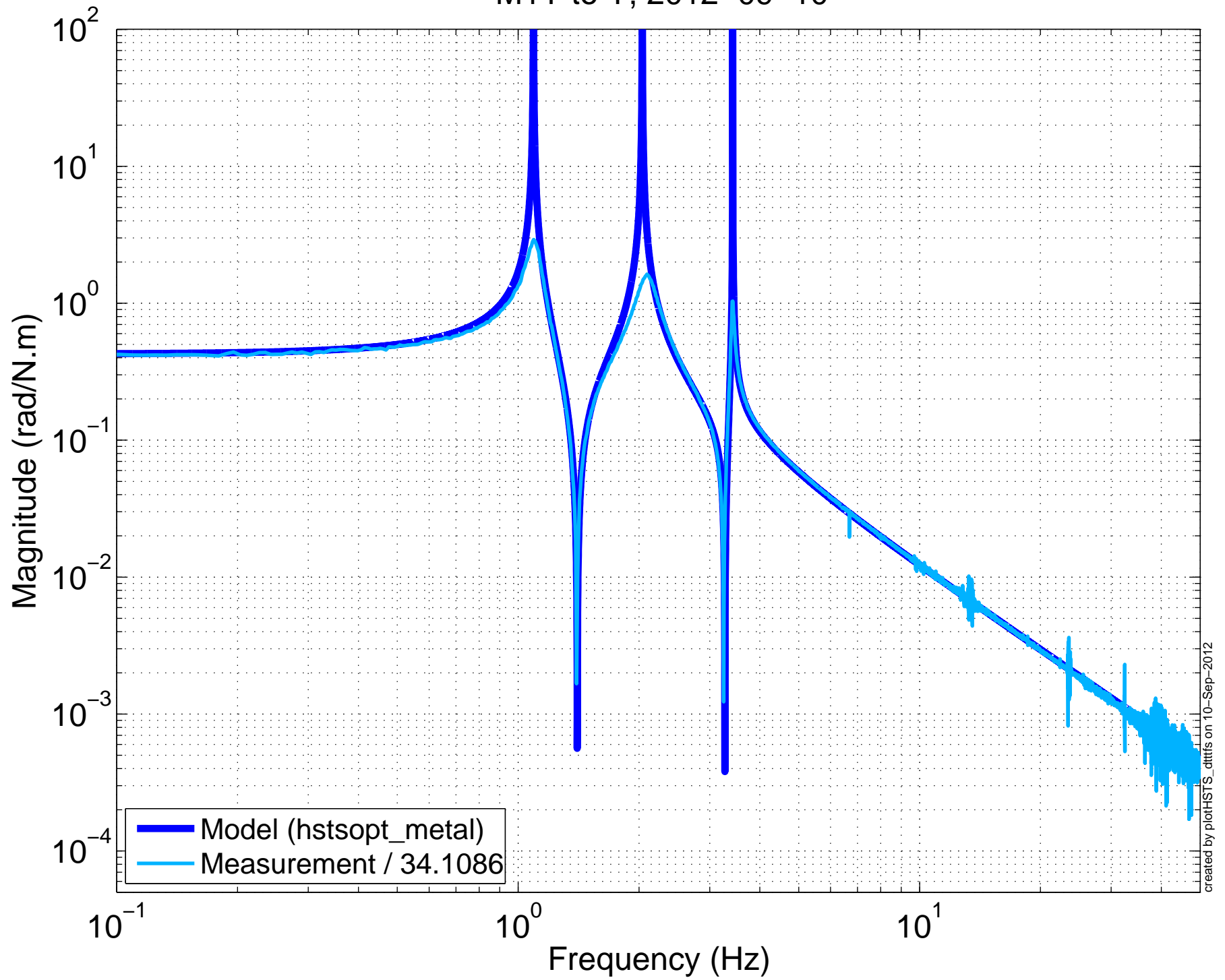


X1SUSPRM BUILD  
M1P to P, 2012-09-10

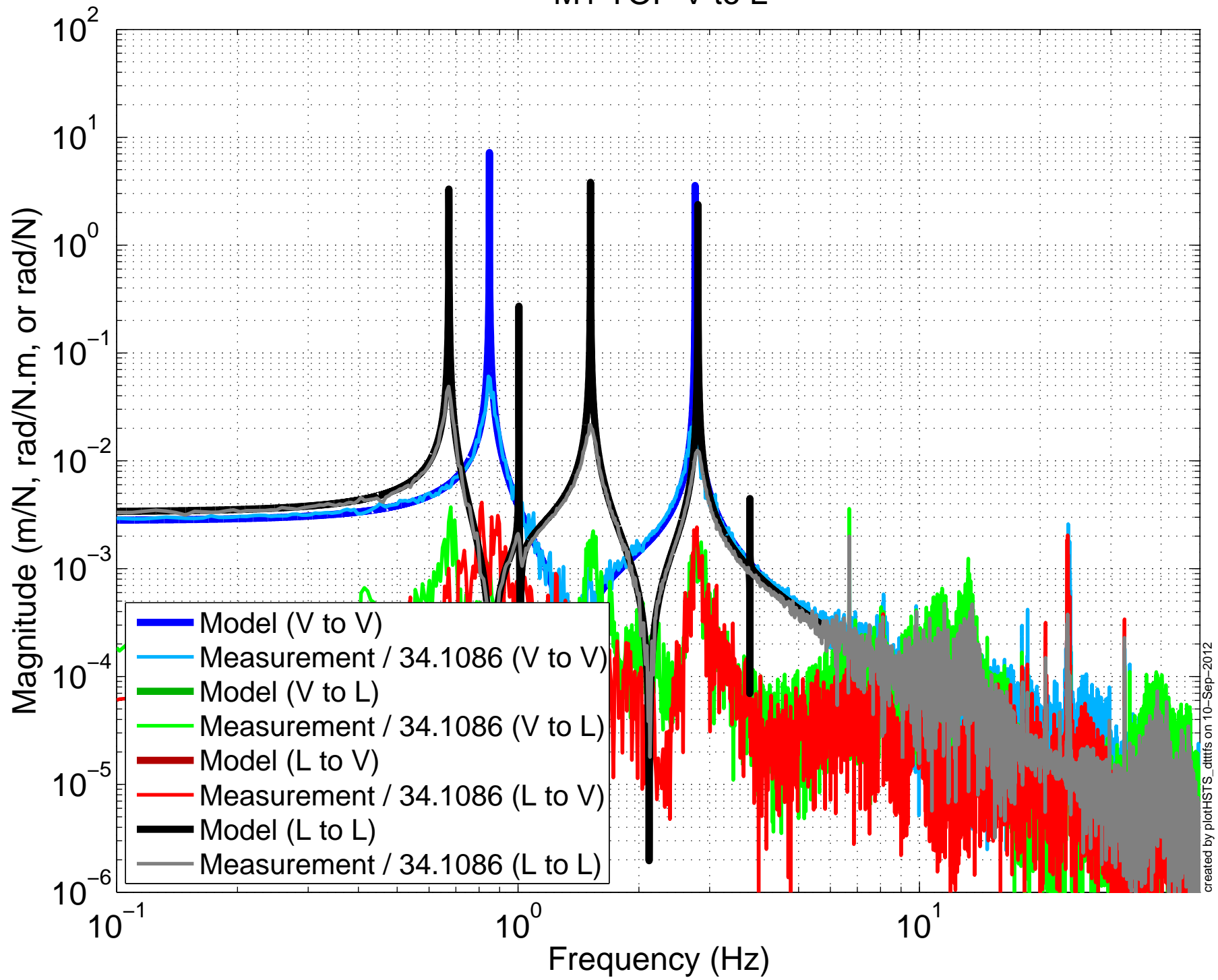


# X1SUSPRM BUILD

M1Y to Y, 2012-09-10

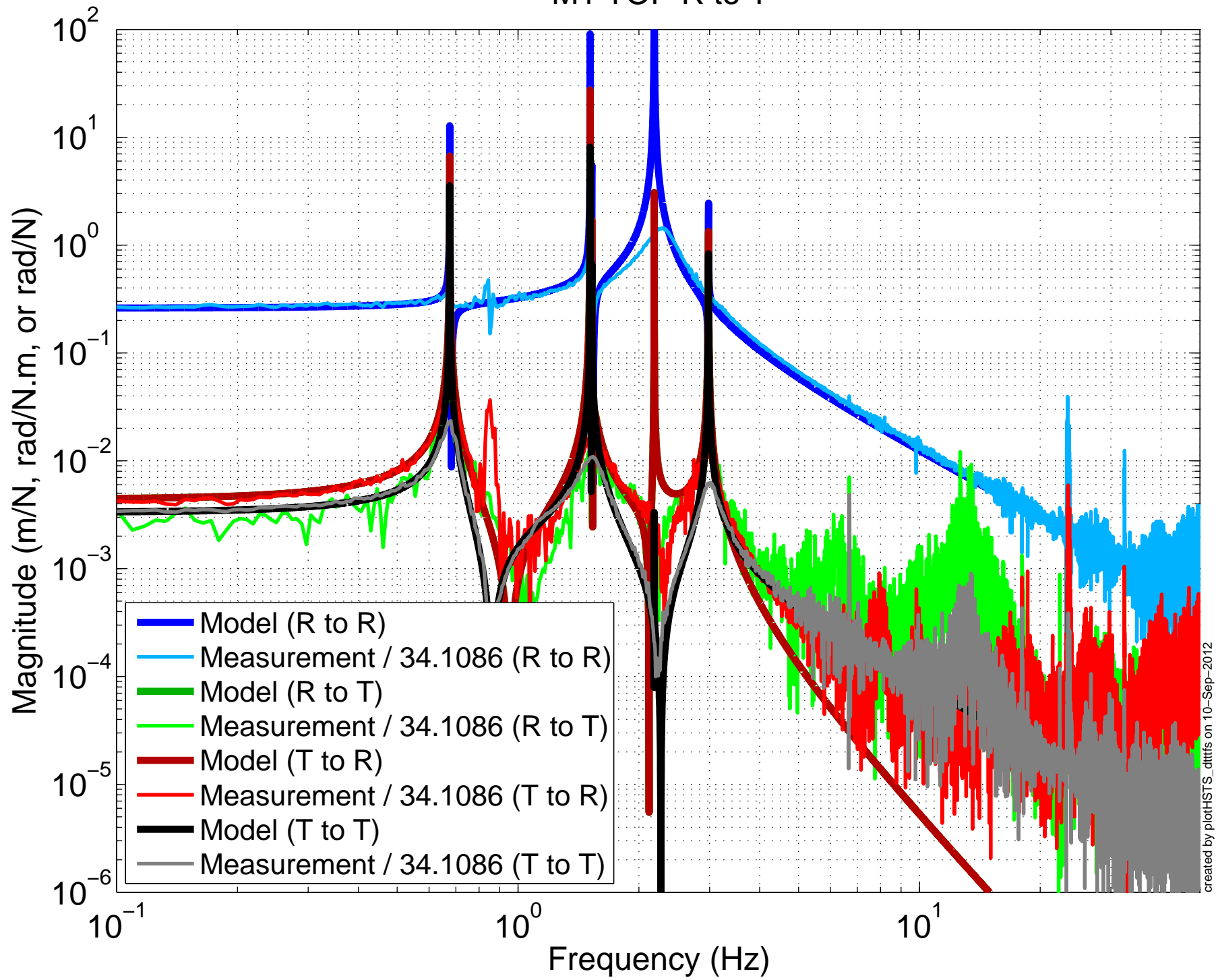


X1SUSPRM BUILD  
M1 TOP V to L



# X1SUSPRM BUILD

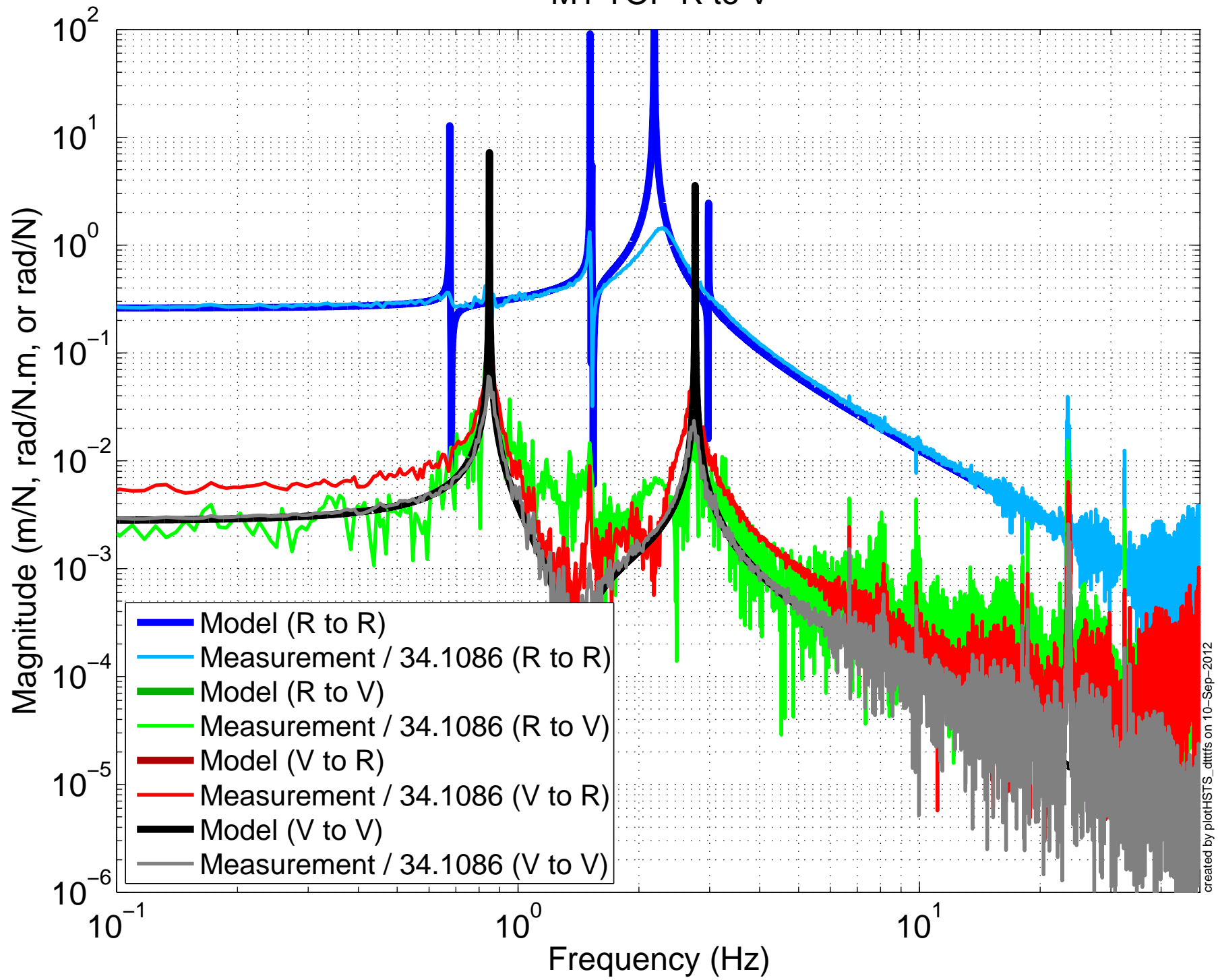
## M1 TOP R to T



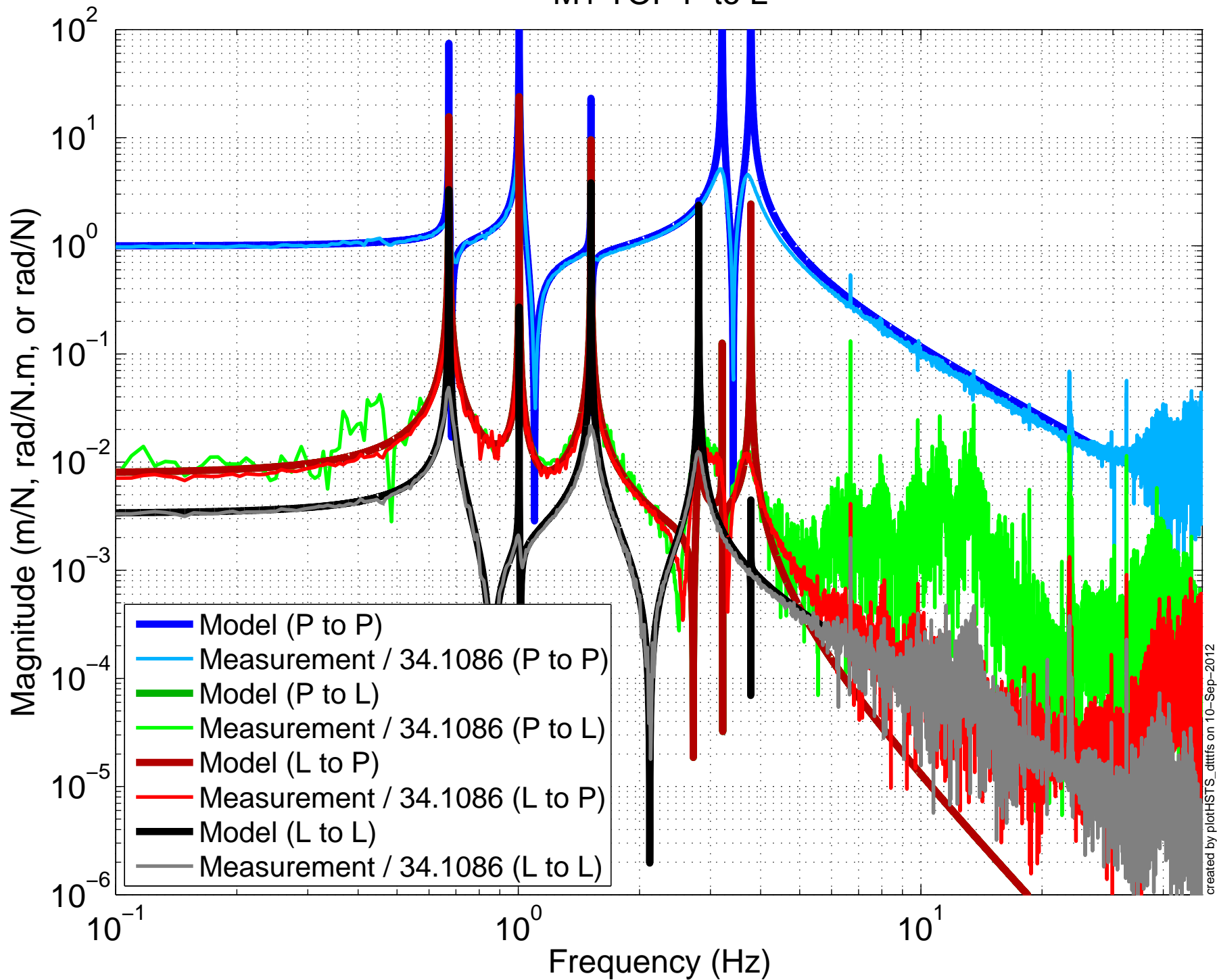


# X1SUSPRM BUILD

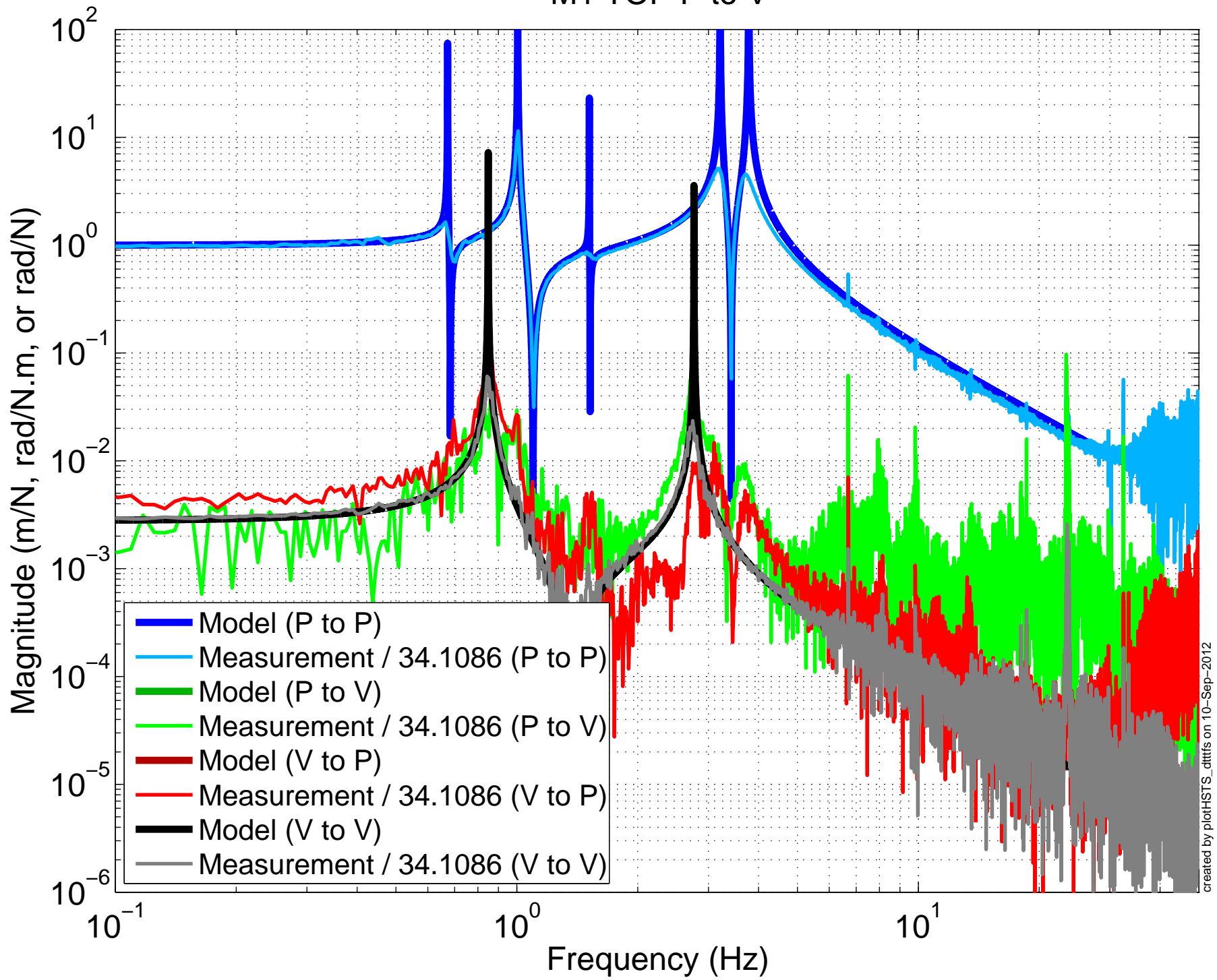
## M1 TOP R to V



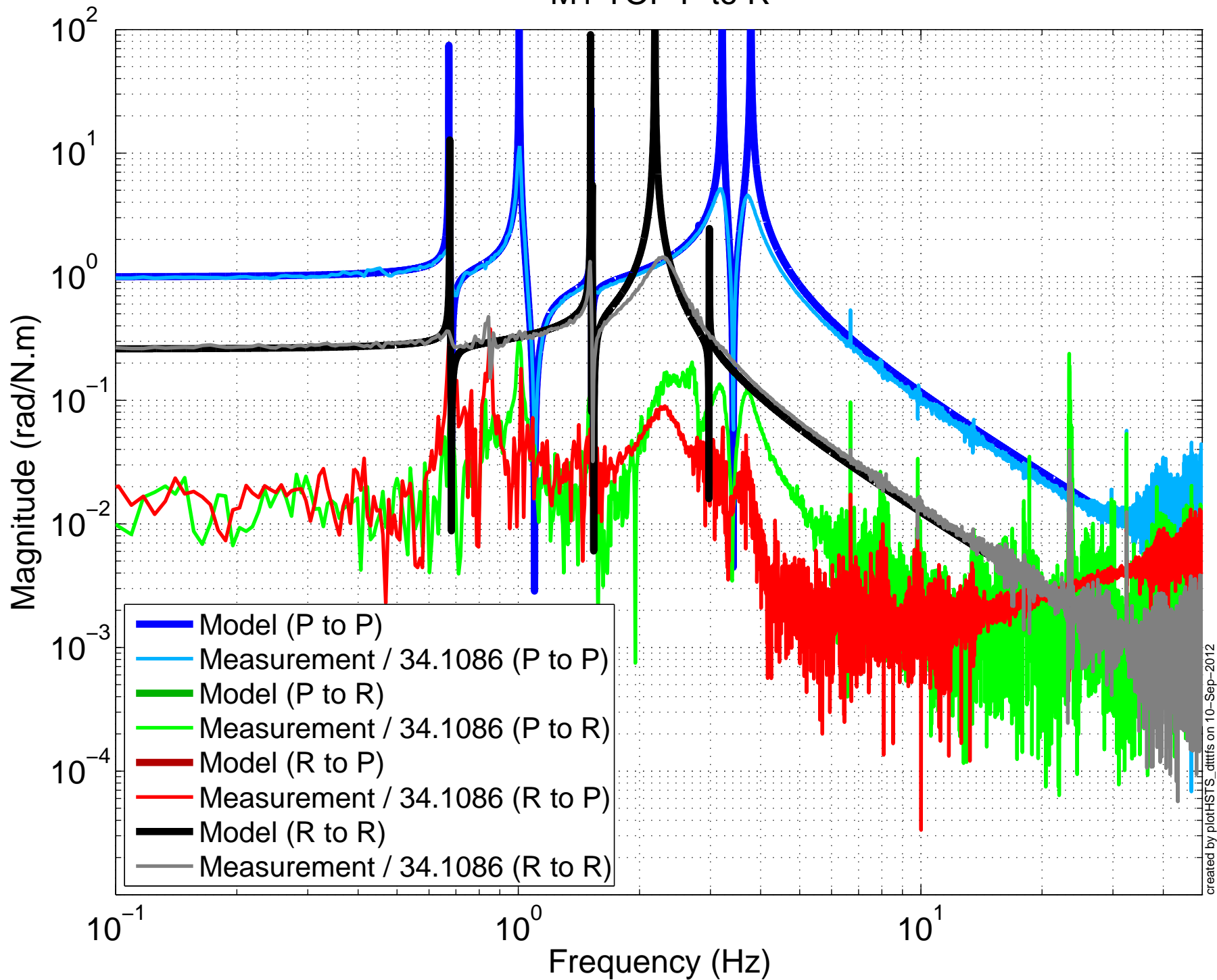
X1SUSPRM BUILD  
M1 TOP P to L



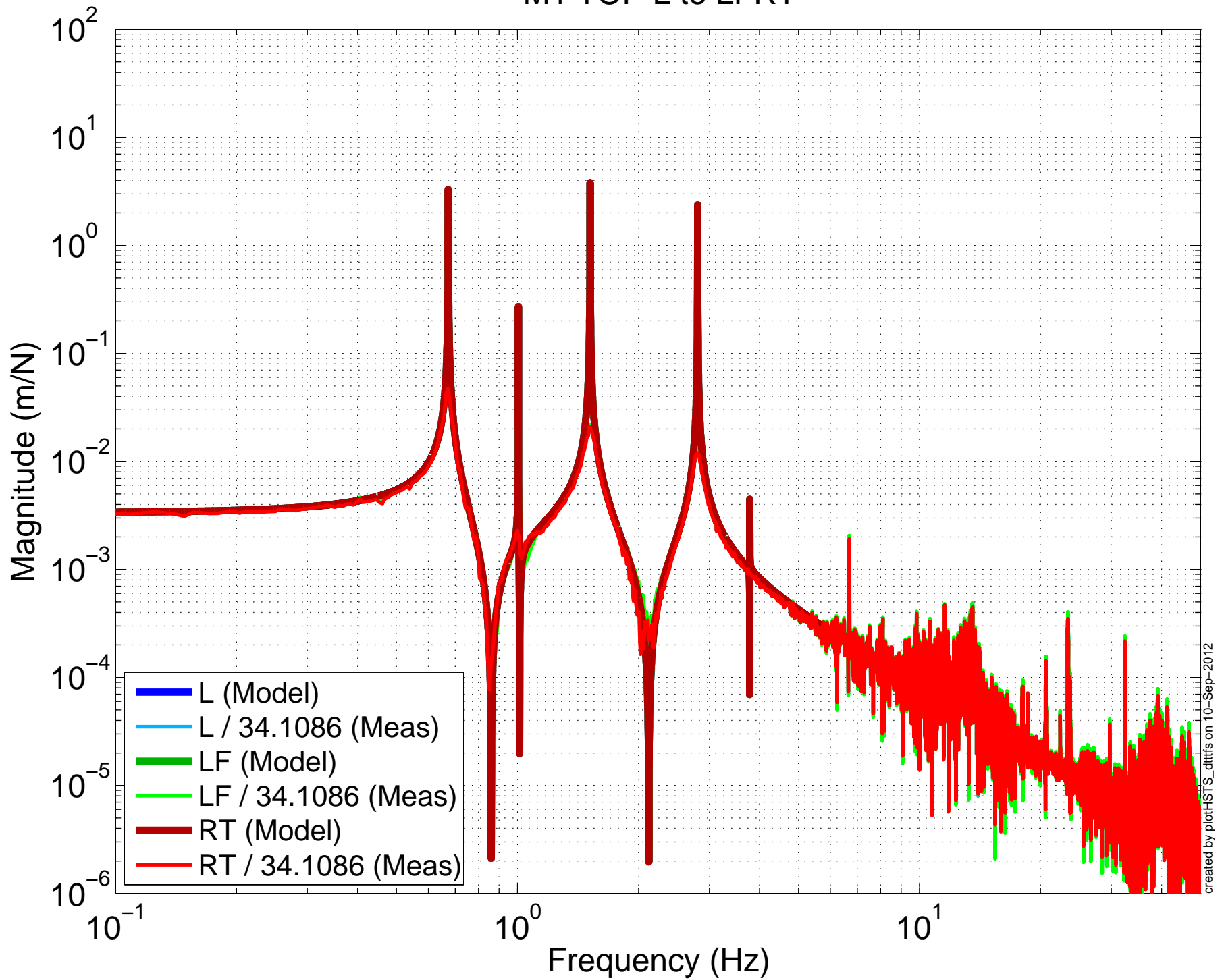
X1SUSPRM BUILD  
M1 TOP P to V



X1SUSPRM BUILD  
M1 TOP P to R

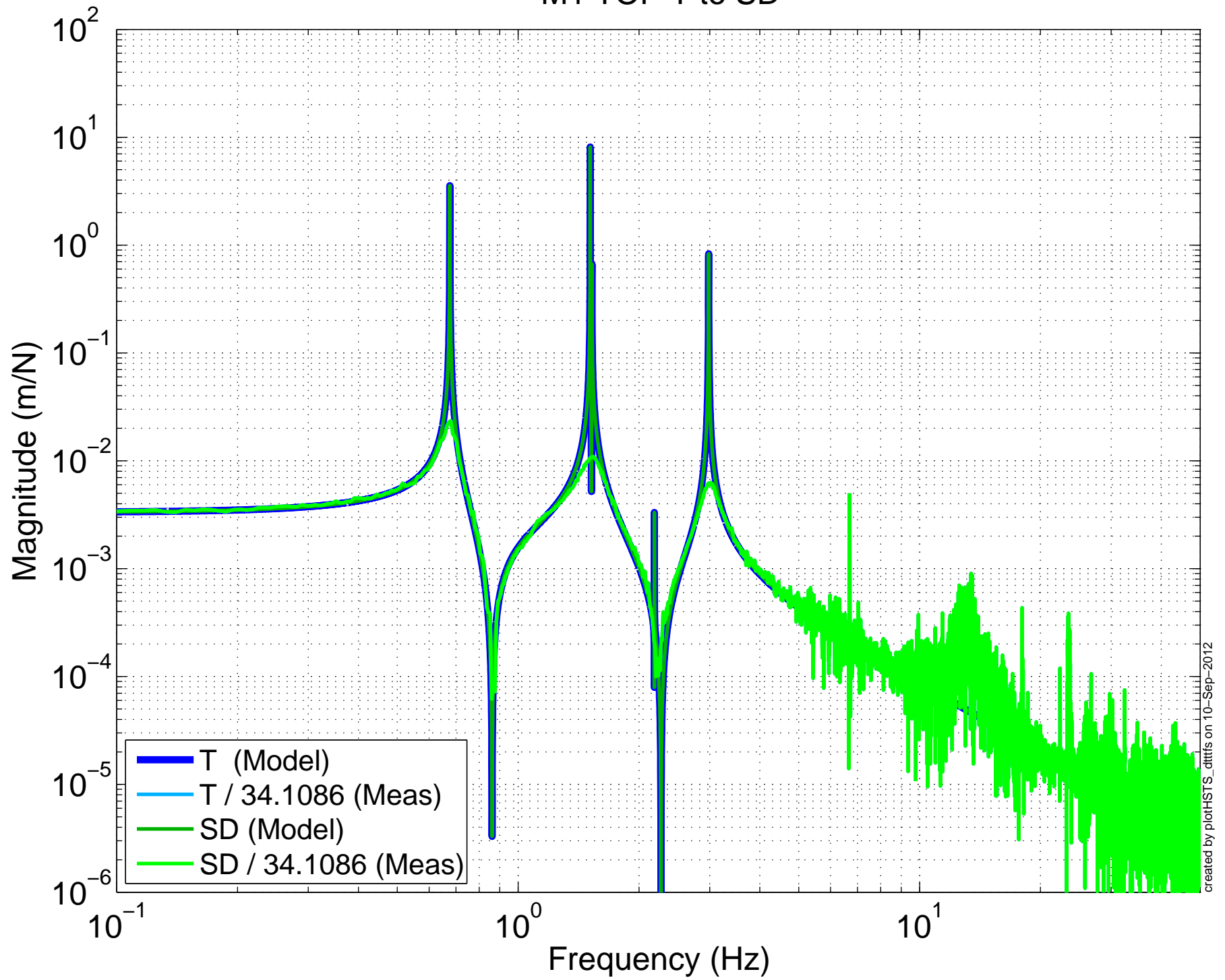


# X1SUSPRM BUILD M1 TOP L to LFRT

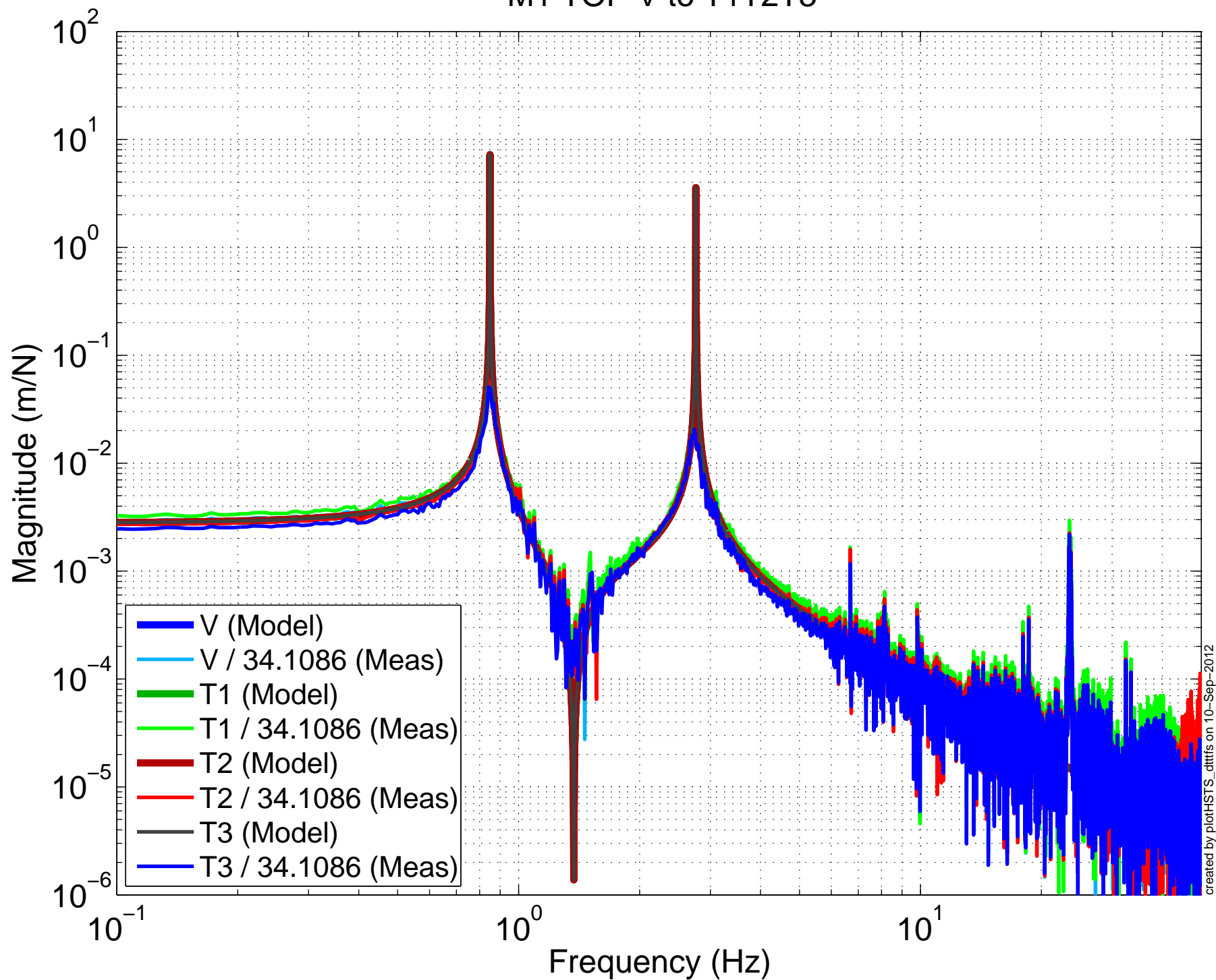


# X1SUSPRM BUILD

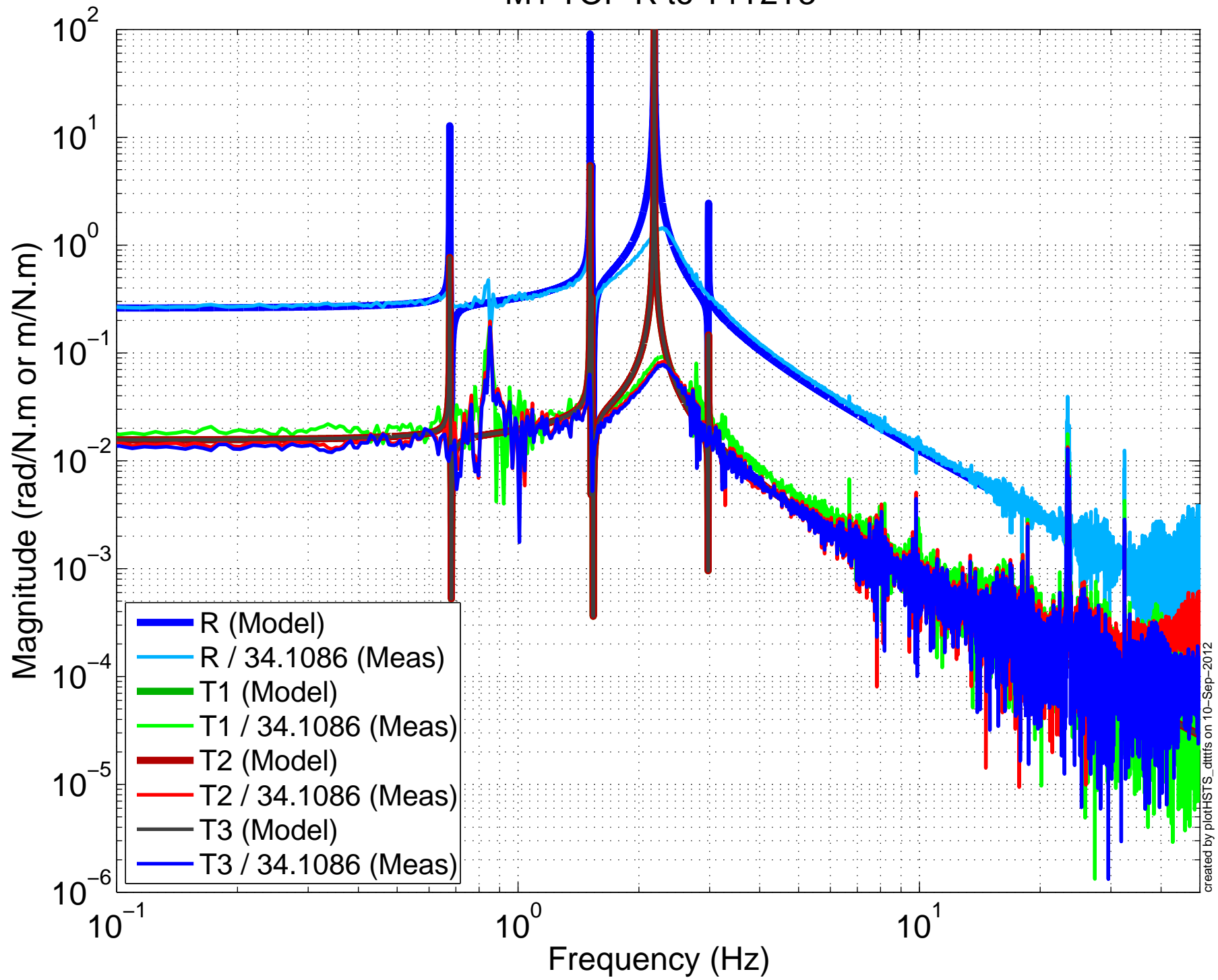
## M1 TOP T to SD



# X1SUSPRM BUILD M1 TOP V to T1T2T3



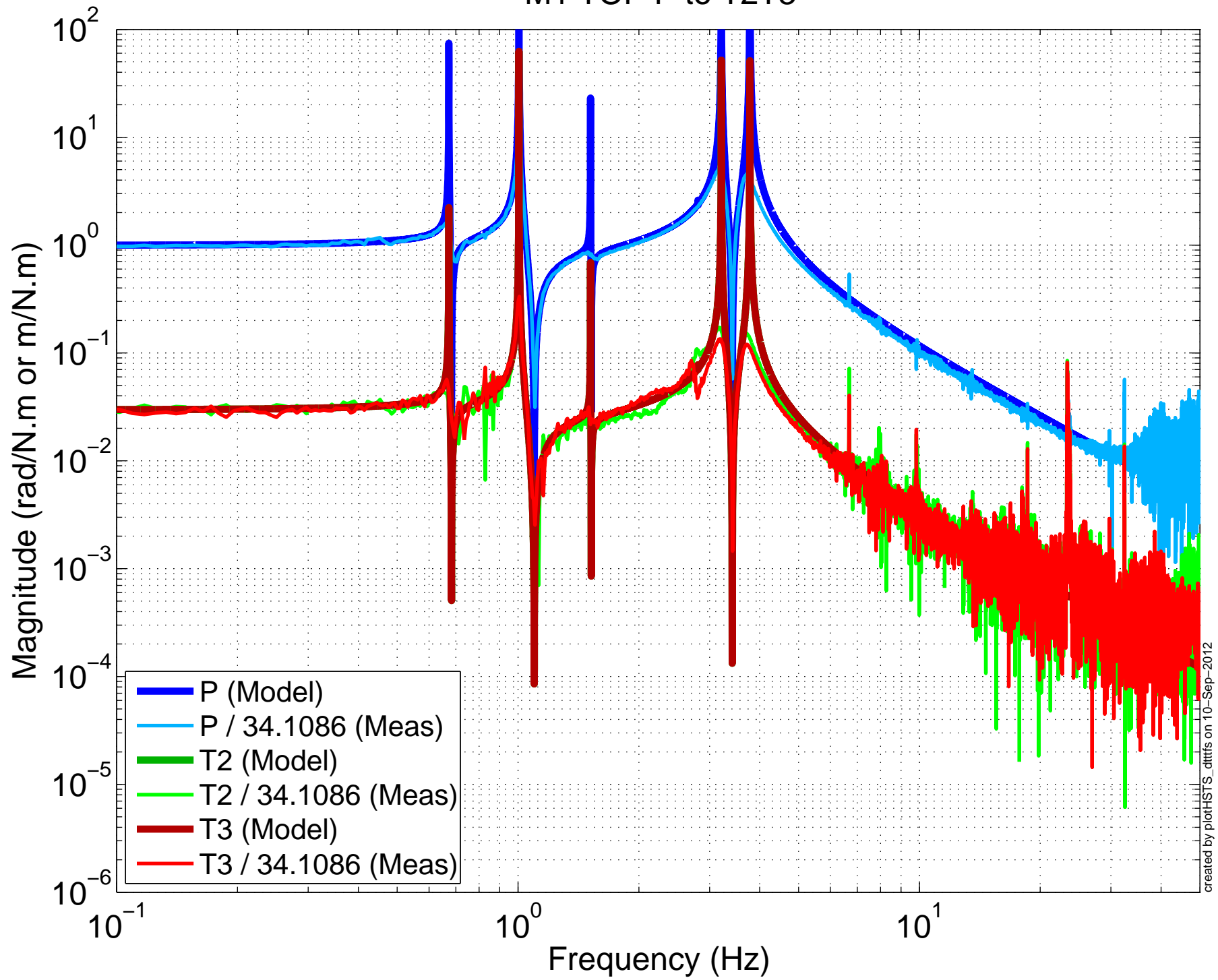
# X1SUSPRM BUILD M1 TOP R to T1T2T3





# X1SUSPRM BUILD

## M1 TOP P to T2T3



# X1SUSPRM BUILD M1 TOP Y to LFRT

