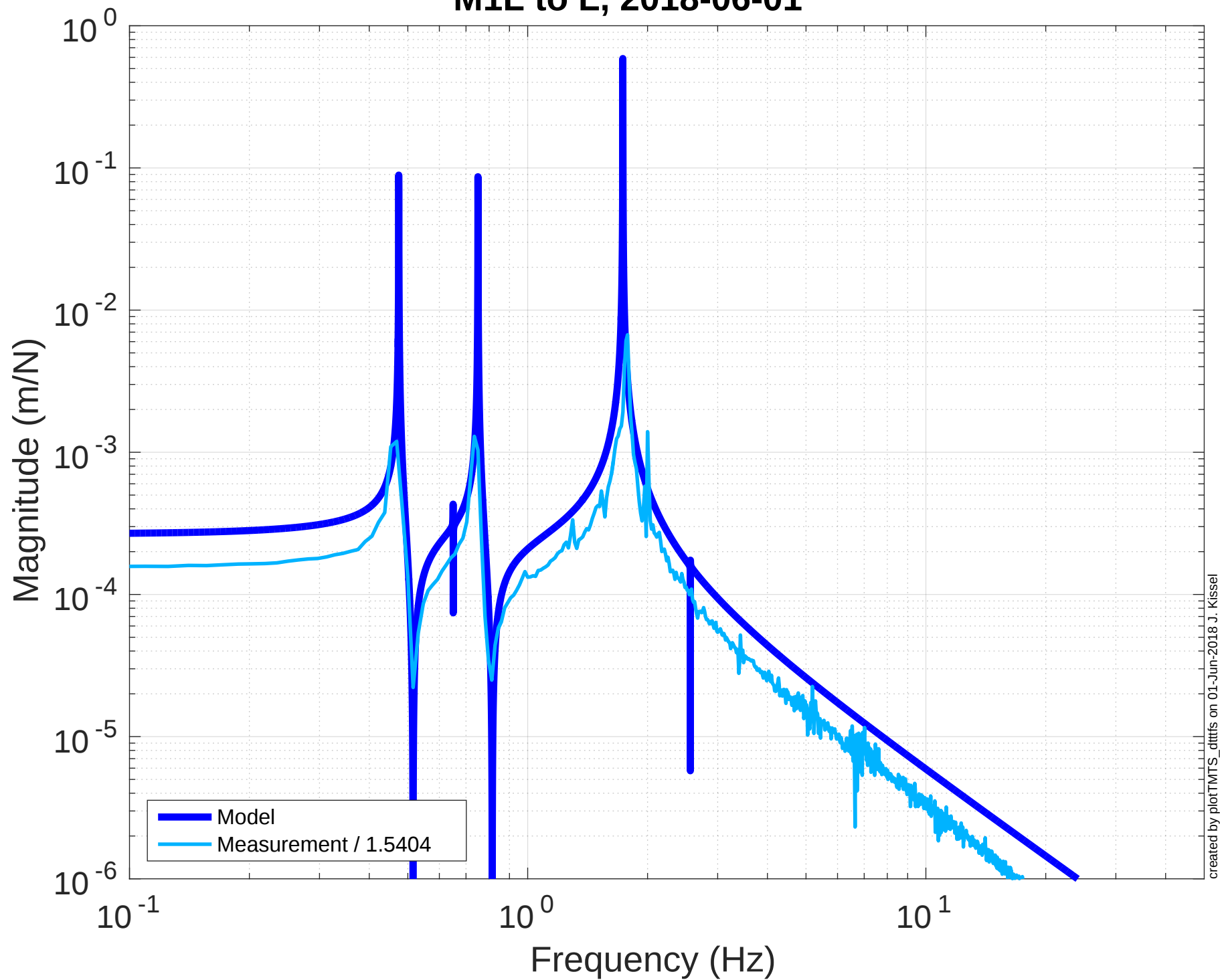


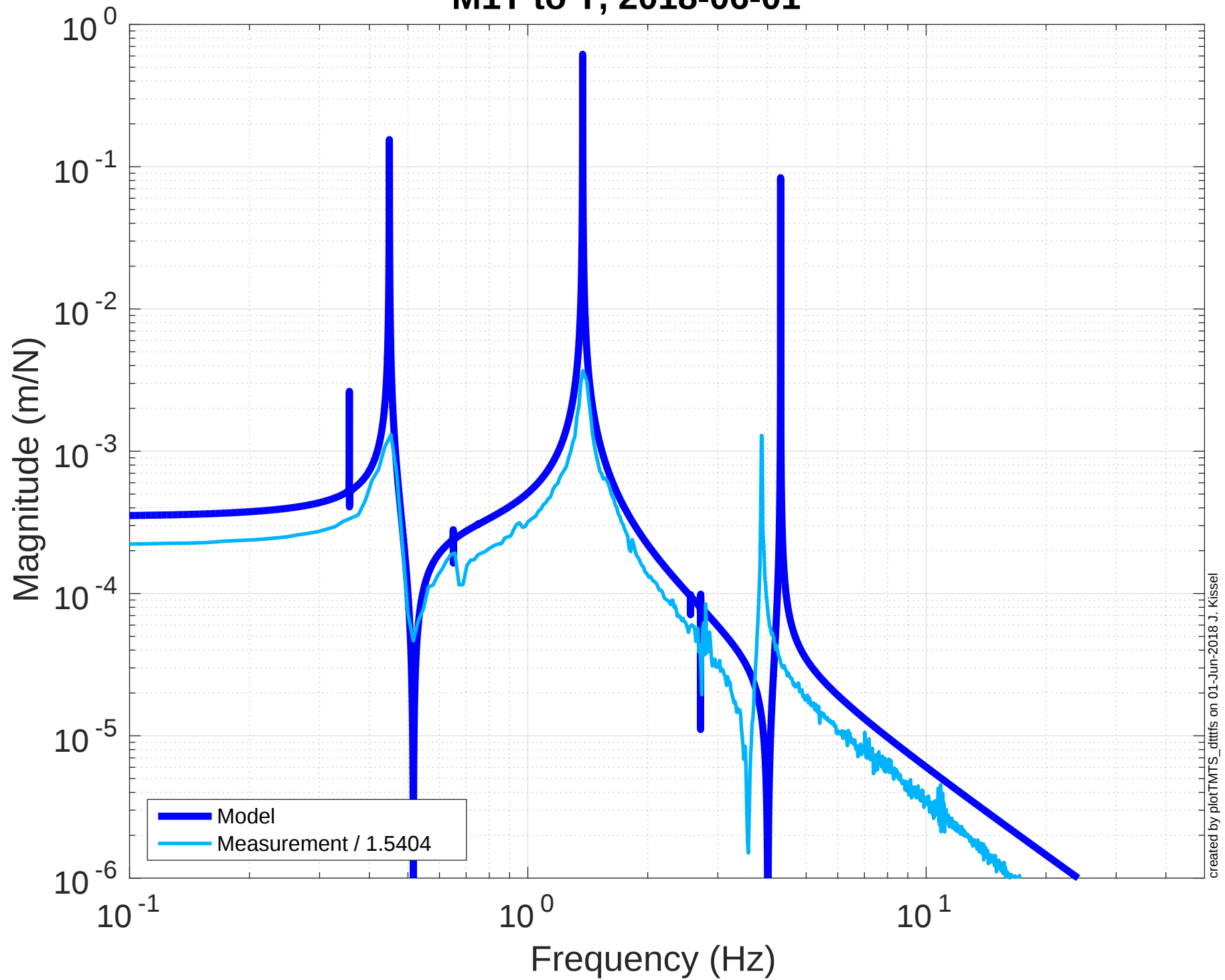
H1SUSTMSX BUILD

M1L to L, 2018-06-01



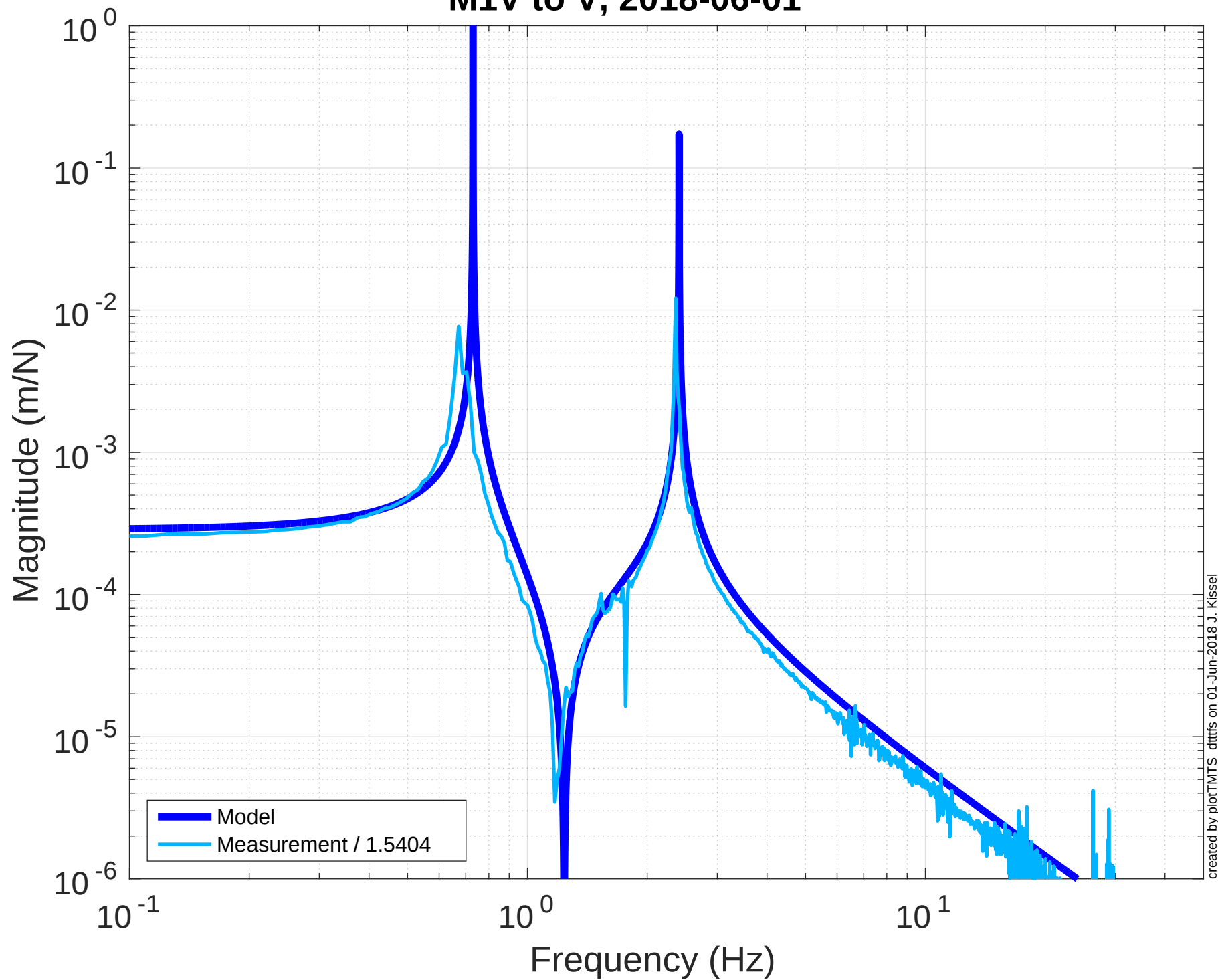
H1SUSTMSX BUILD

M1T to T, 2018-06-01



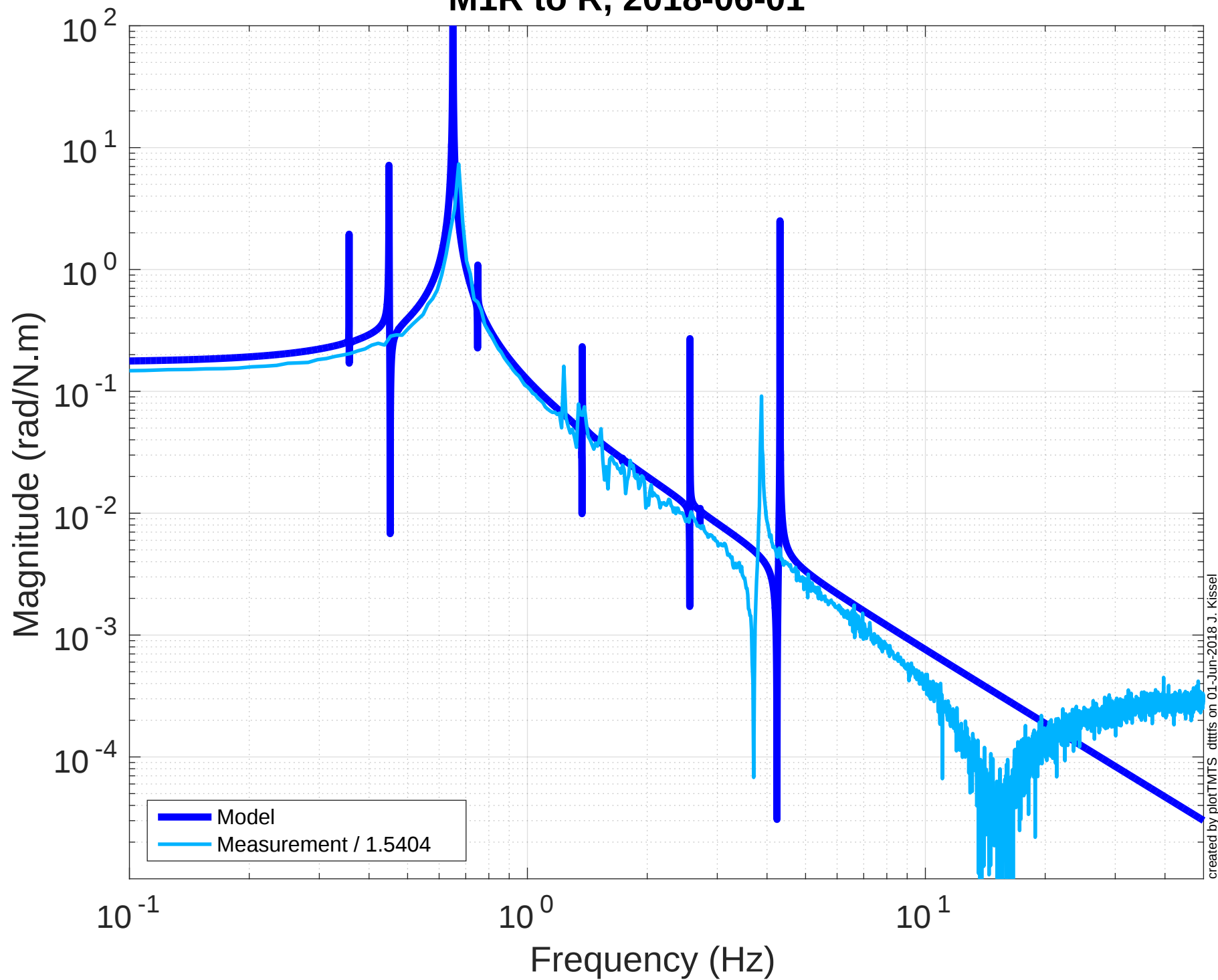
H1SUSTMSX BUILD

M1V to V, 2018-06-01



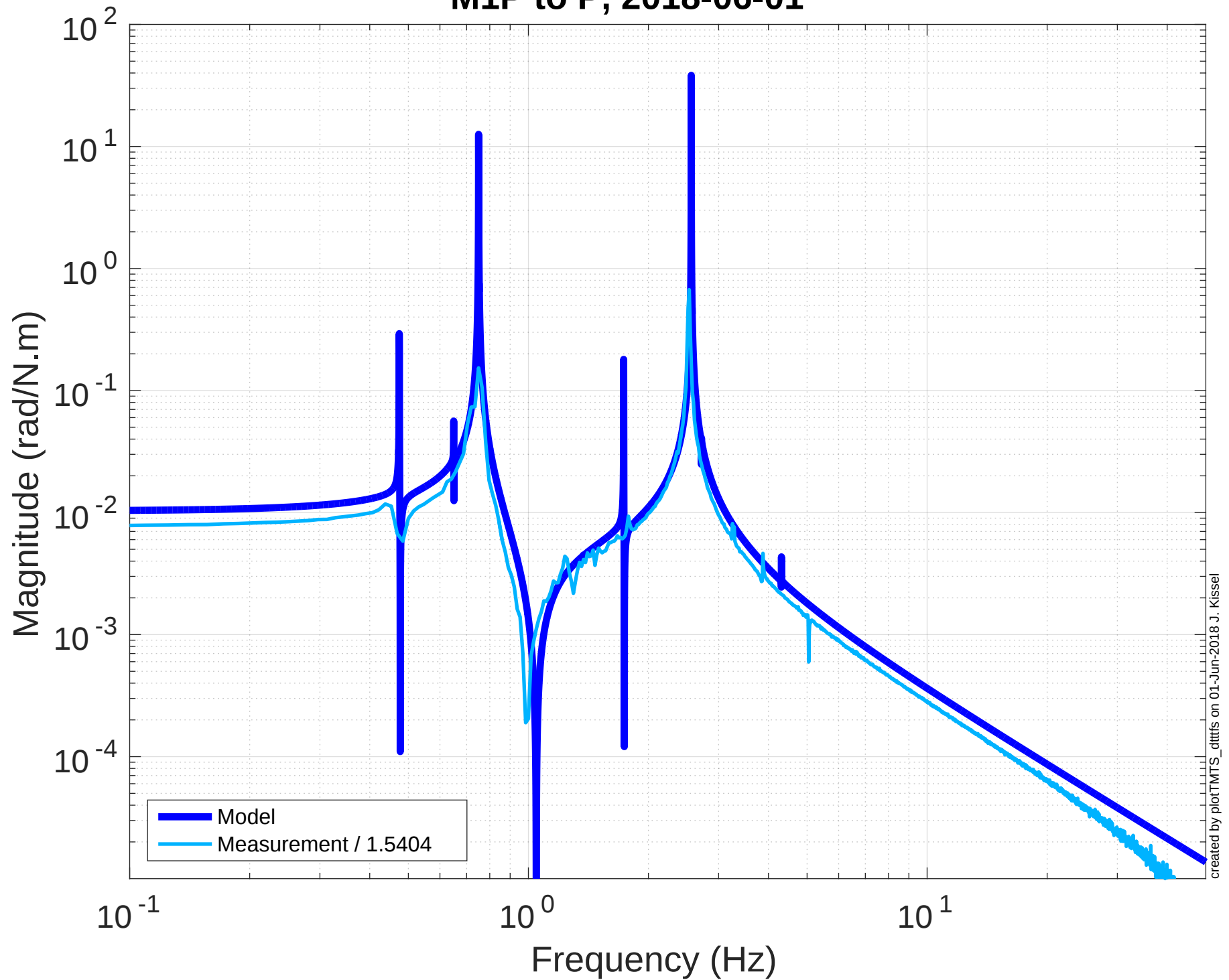
H1SUSTMSX BUILD

M1R to R, 2018-06-01



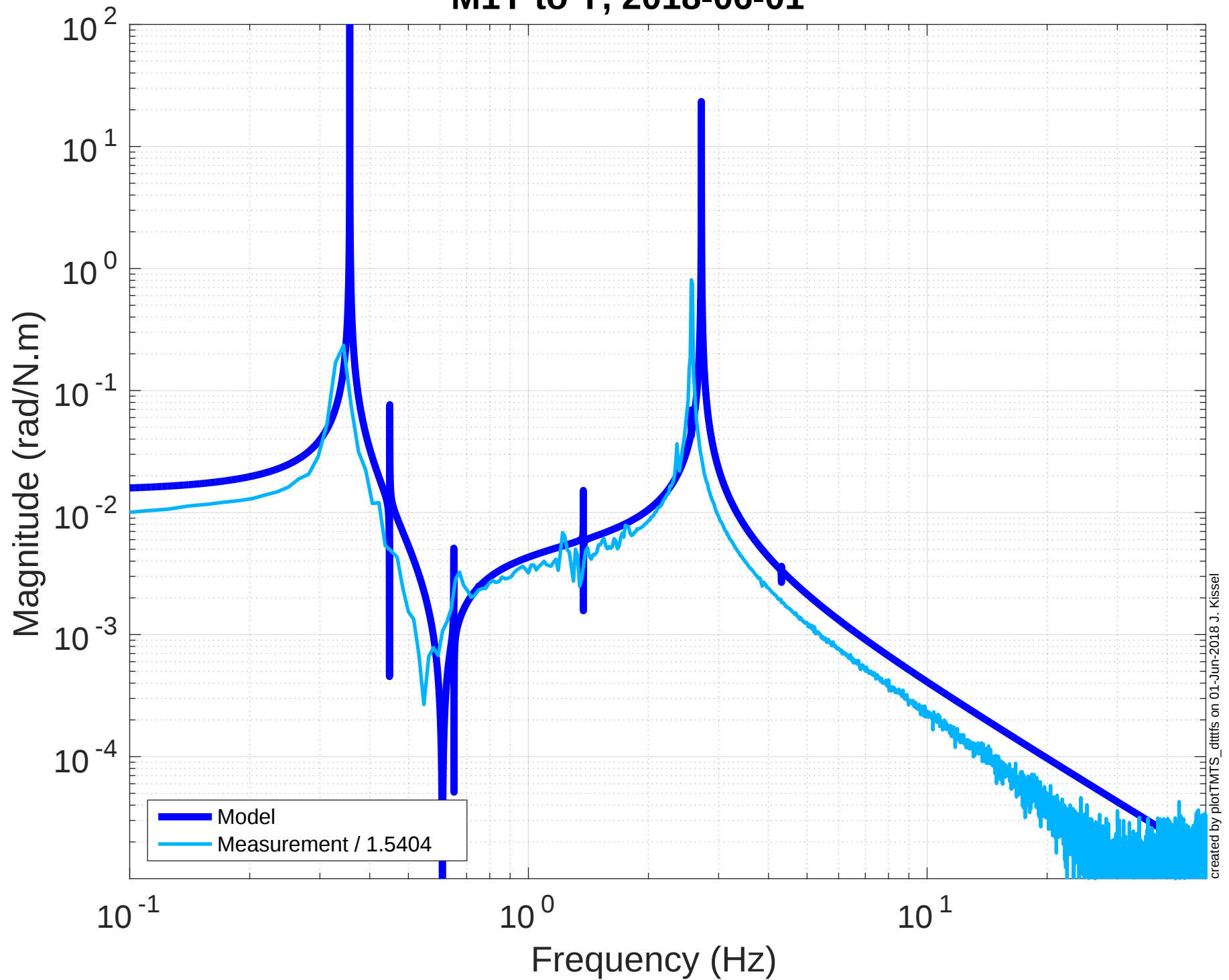
H1SUSTMSX BUILD

M1P to P, 2018-06-01



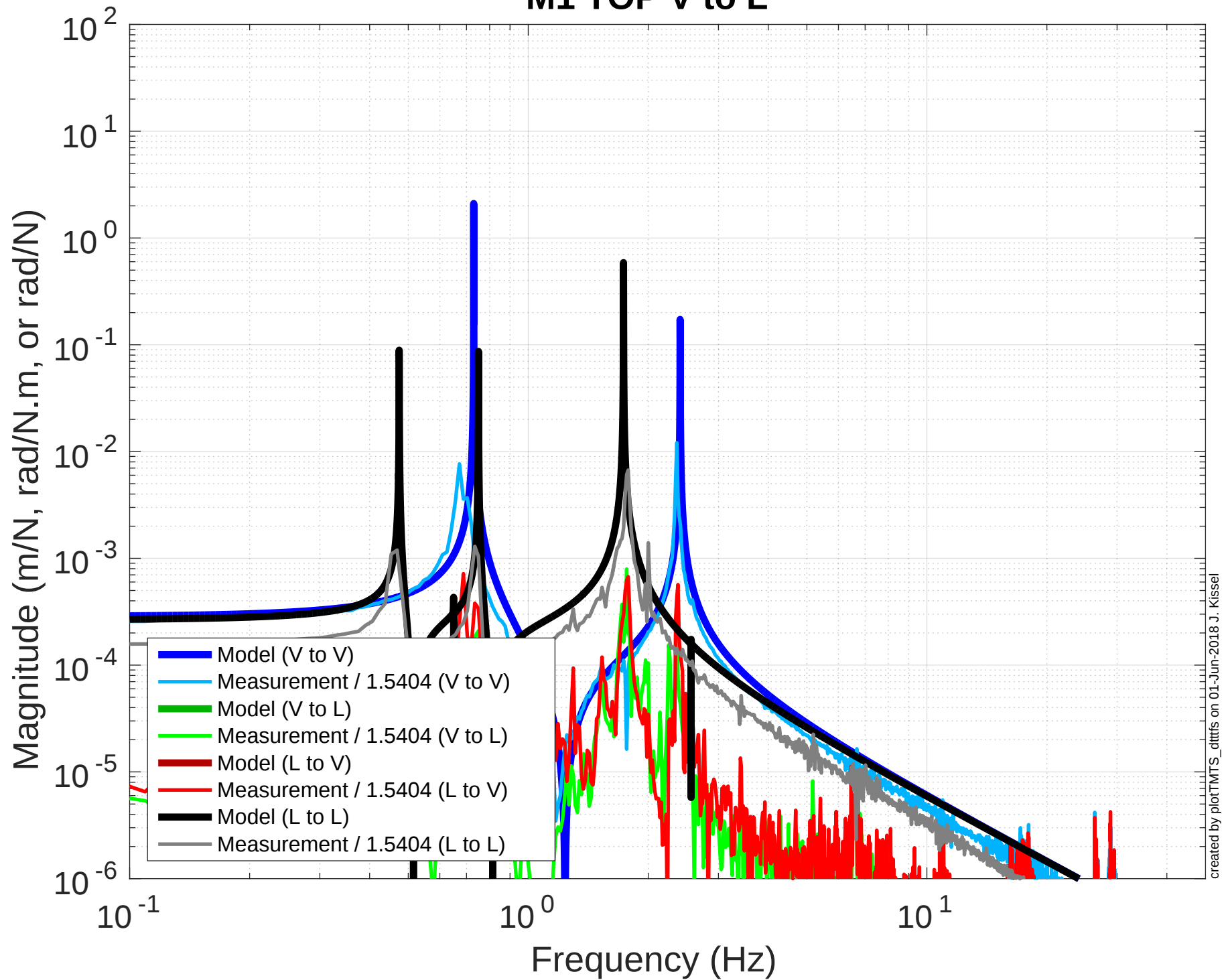
H1SUSTMSX BUILD

M1Y to Y, 2018-06-01



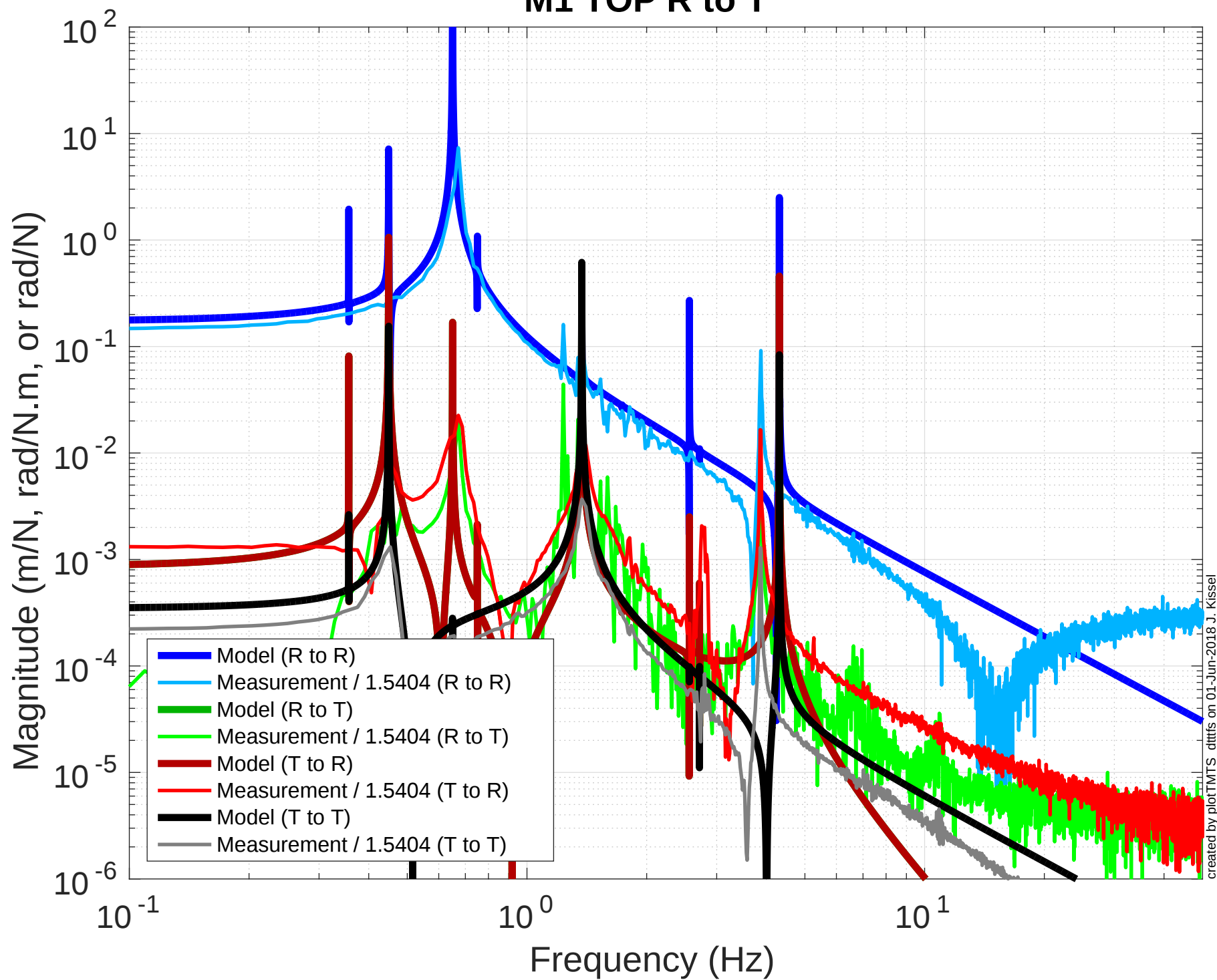
H1SUSTMSX BUILD

M1 TOP V to L



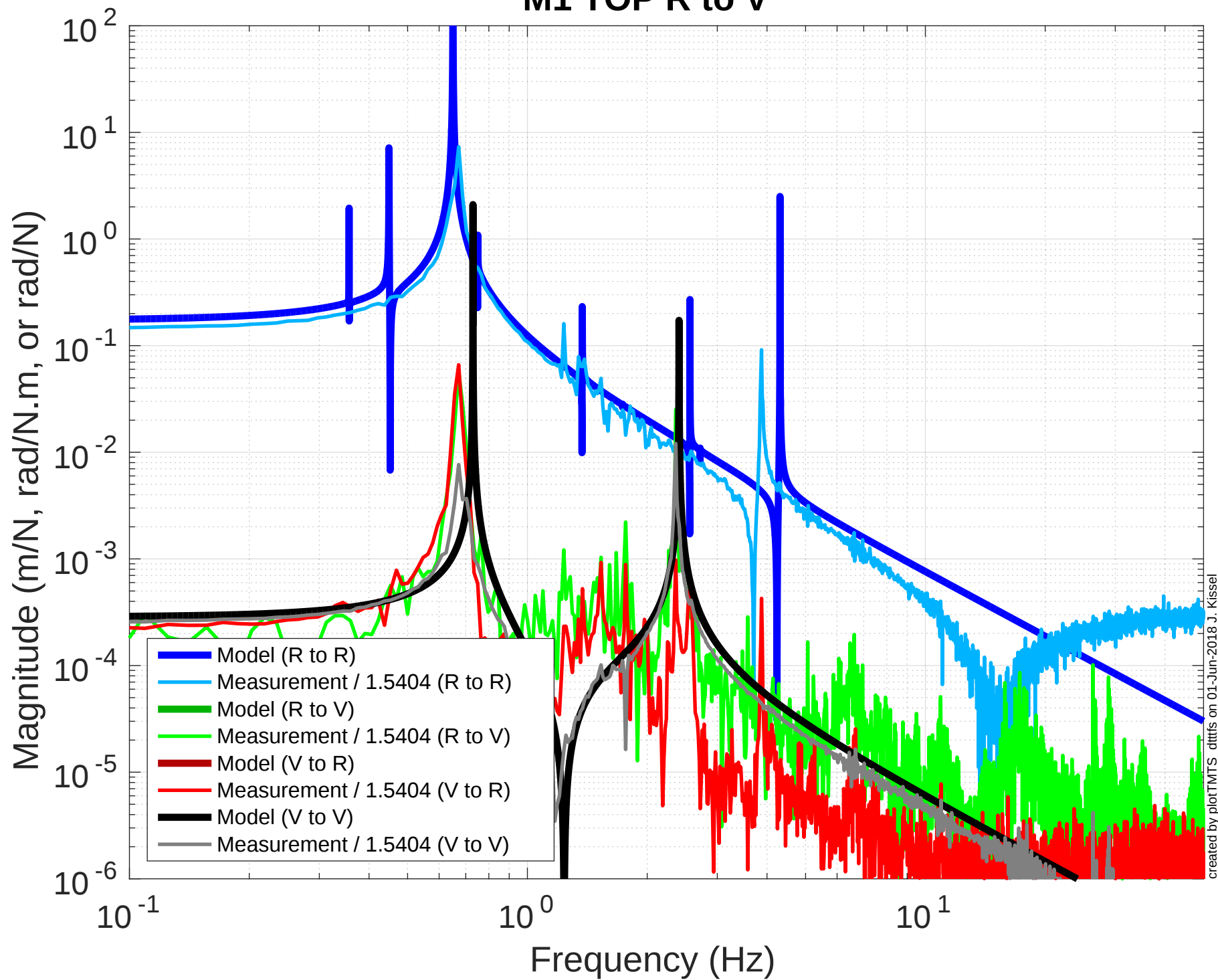
H1SUSTMSX BUILD

M1 TOP R to T



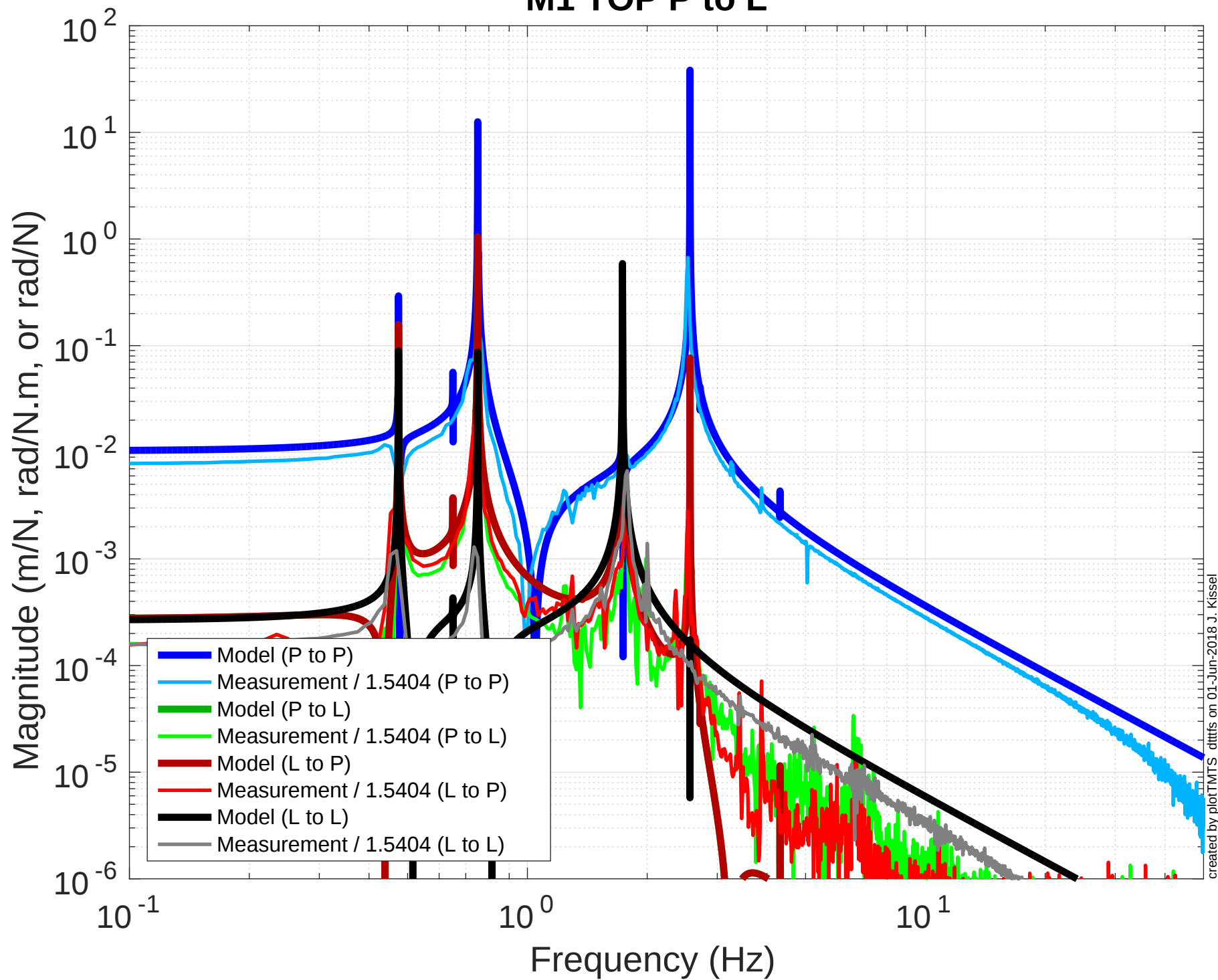
H1SUSTMSX BUILD

M1 TOP R to V



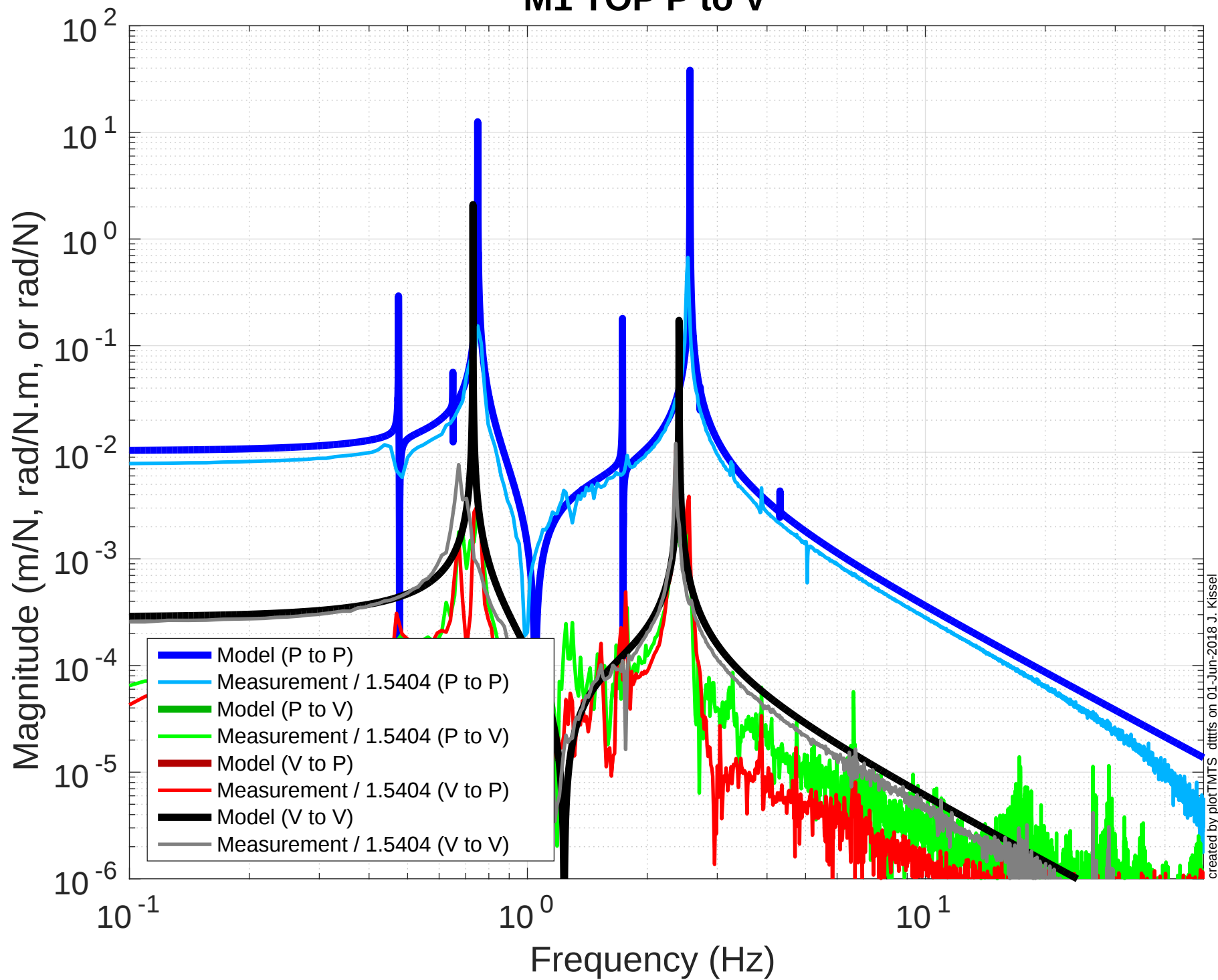
H1SUSTMSX BUILD

M1 TOP P to L



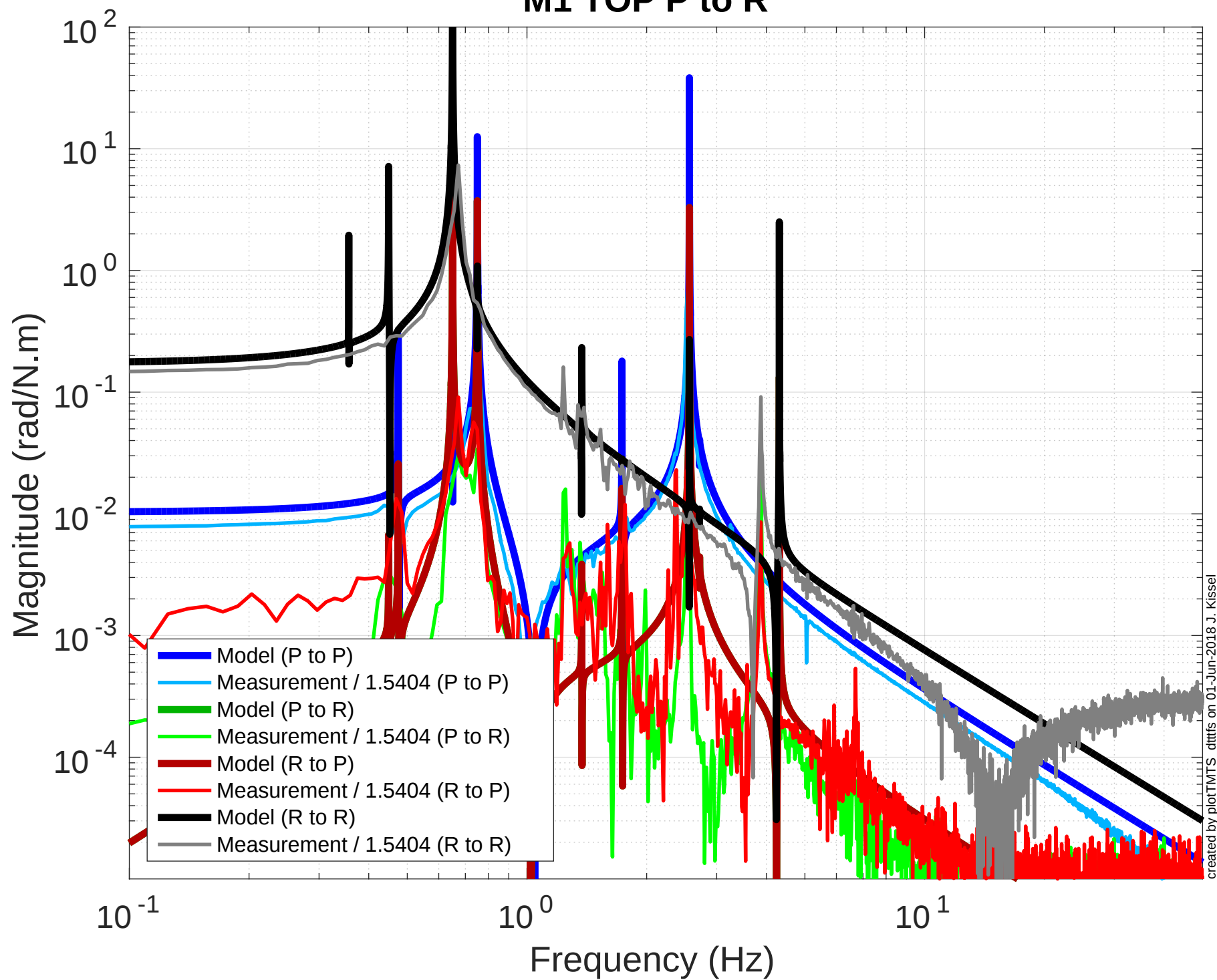
H1SUSTMSX BUILD

M1 TOP P to V



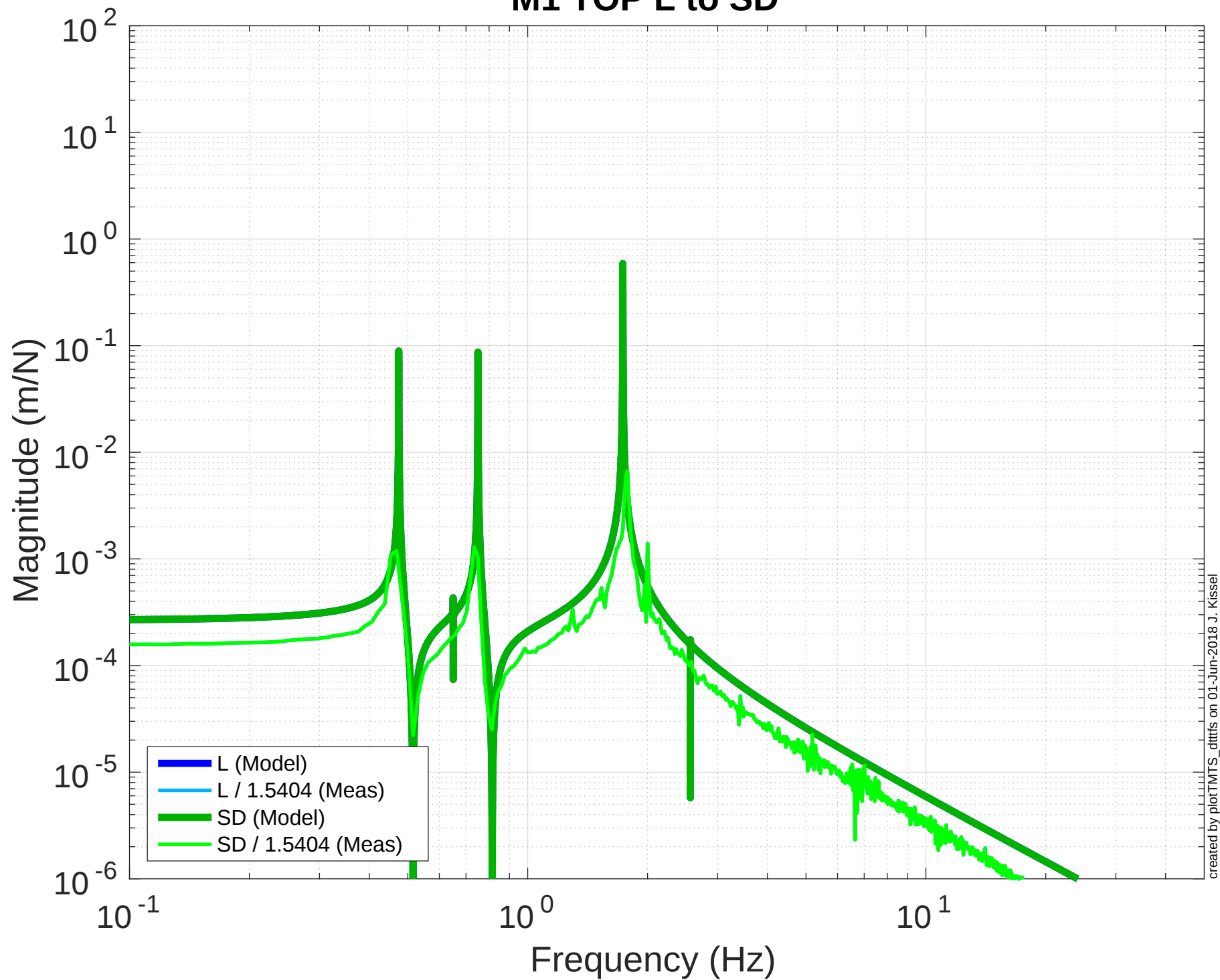
H1SUSTMSX BUILD

M1 TOP P to R



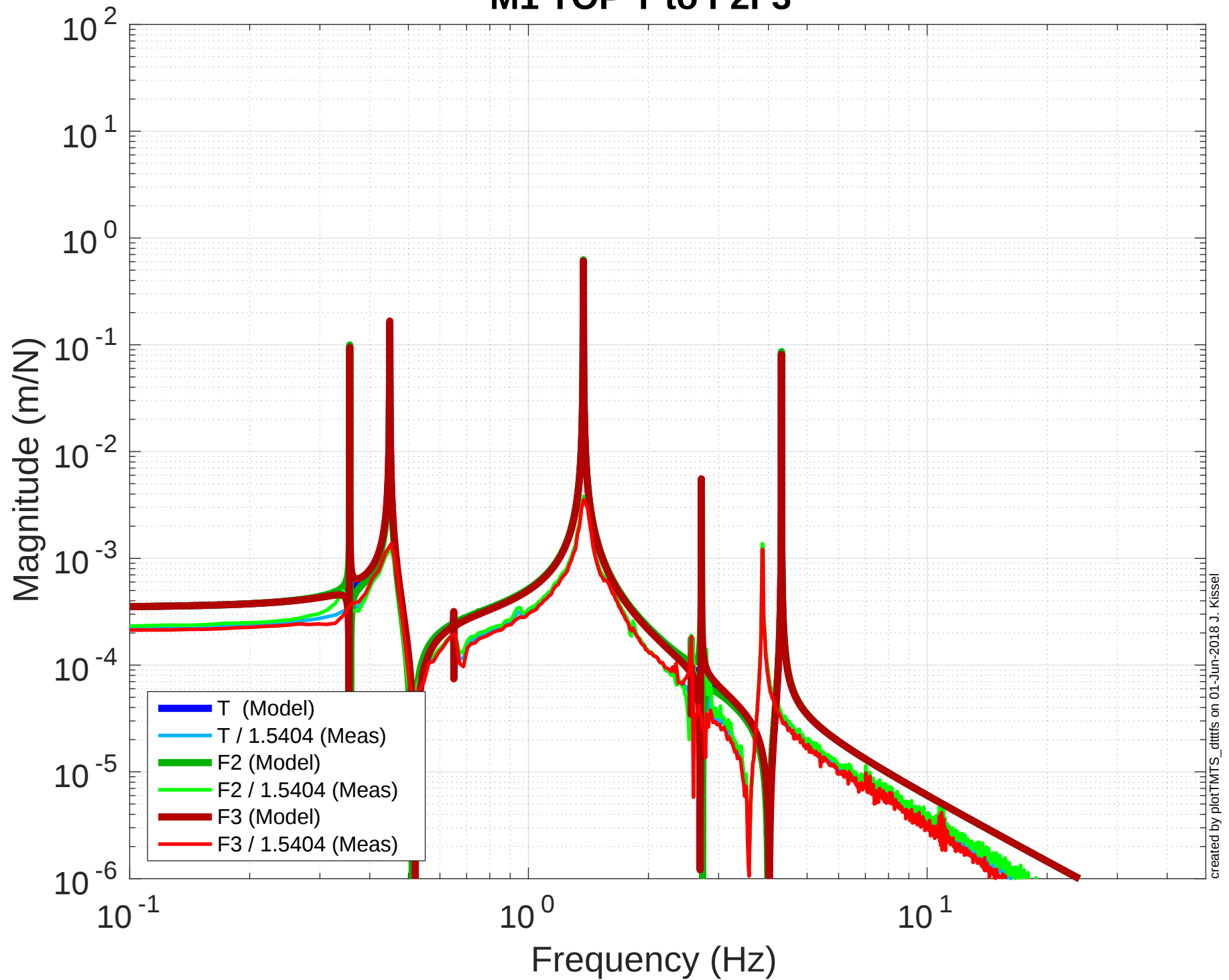
H1SUSTMSX BUILD

M1 TOP L to SD



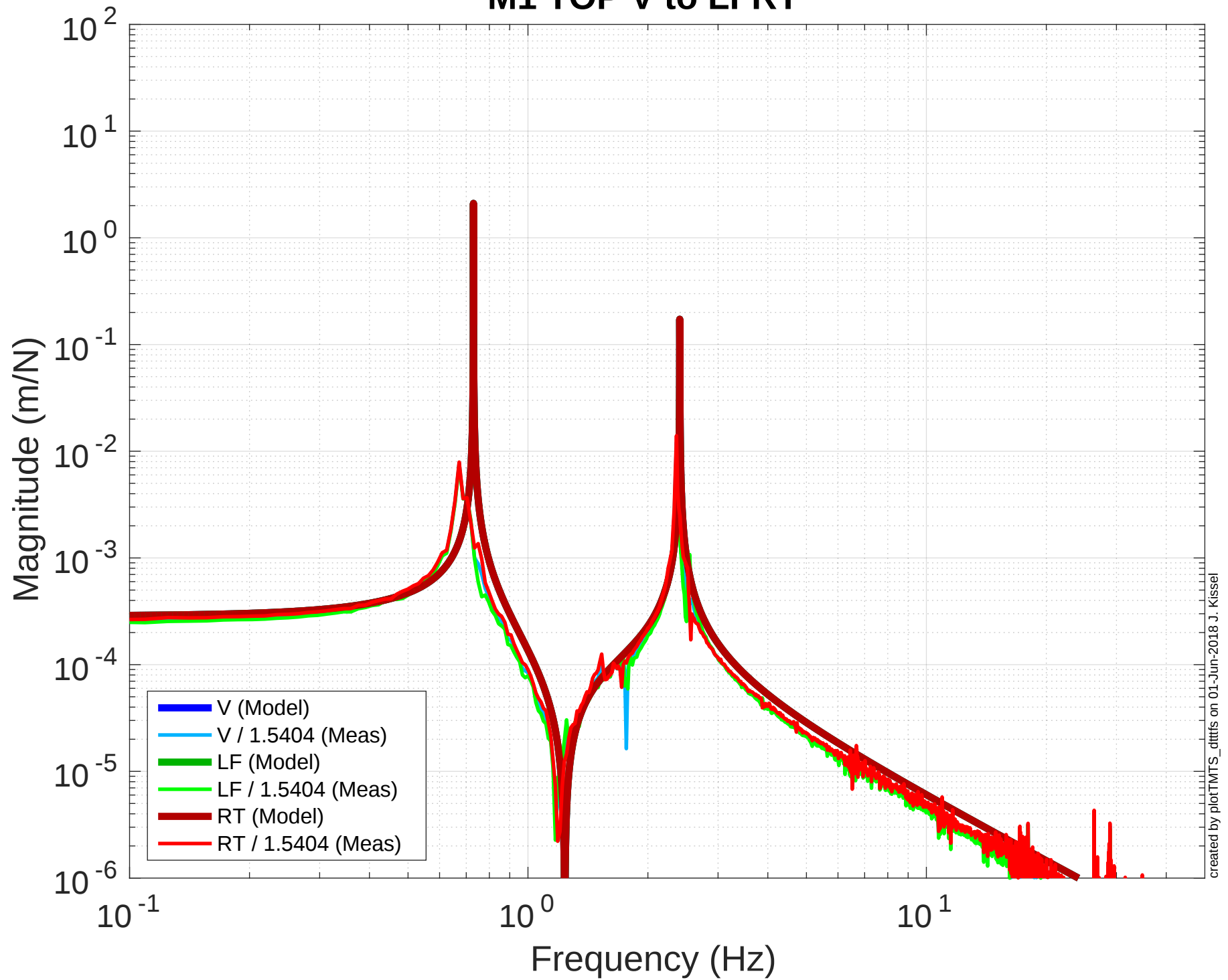
H1SUSTMSX BUILD

M1 TOP T to F2F3



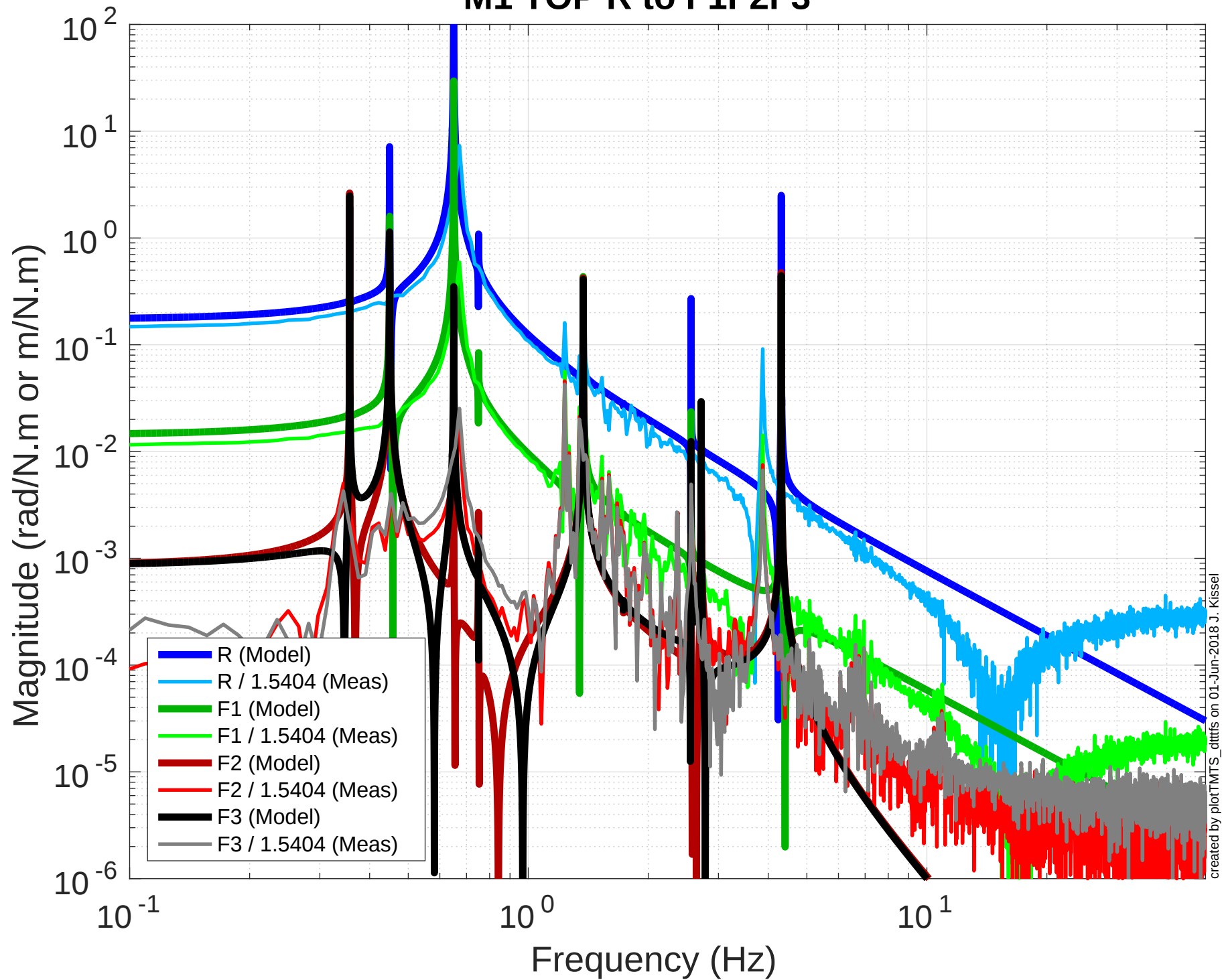
H1SUSTMSX BUILD

M1 TOP V to LFRT



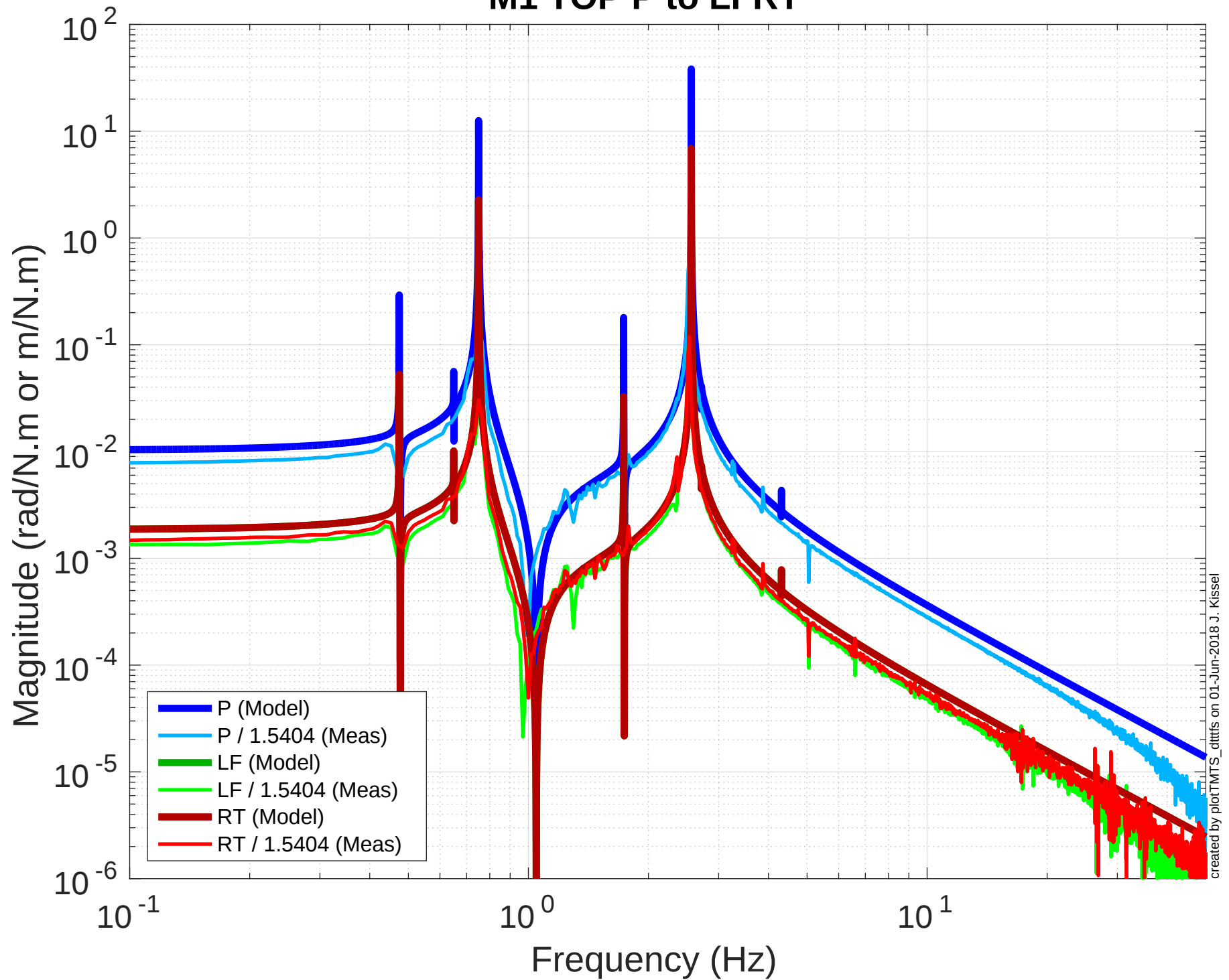
H1SUSTMSX BUILD

M1 TOP R to F1F2F3



H1SUSTMSX BUILD

M1 TOP P to LFRT



H1SUSTMSX BUILD

M1 TOP Y to F2F3

