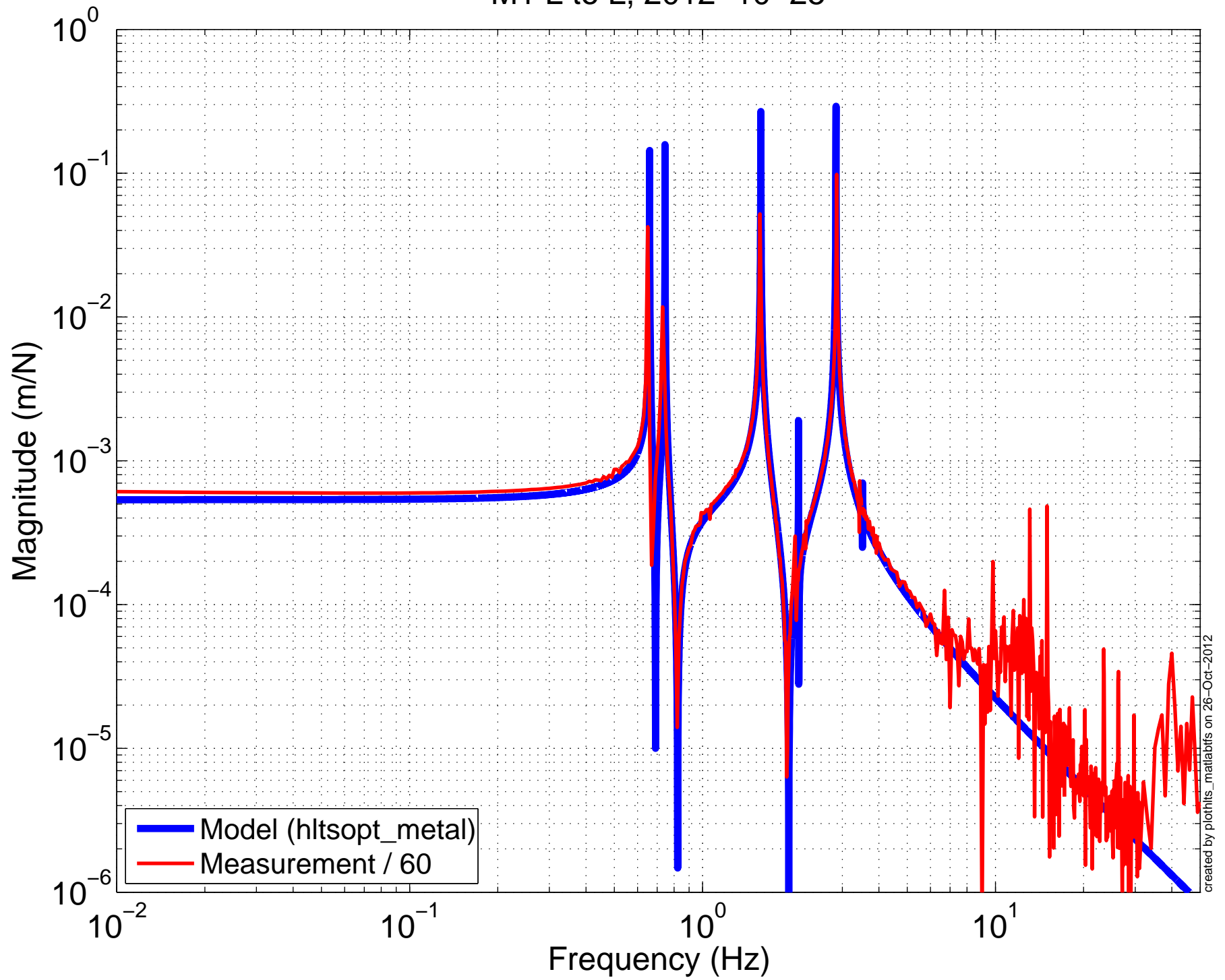
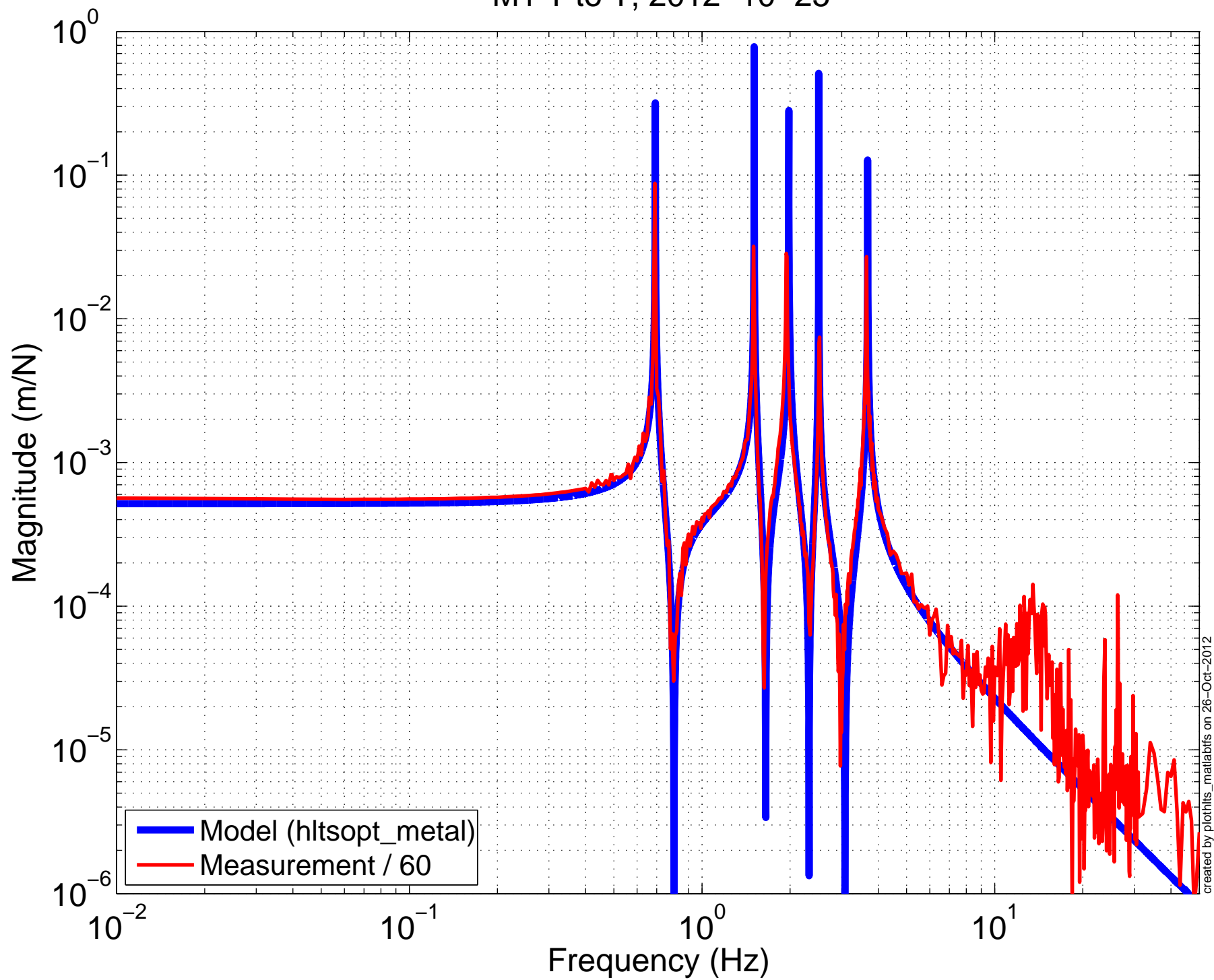


X1SUSSR3
M1 L to L, 2012-10-25

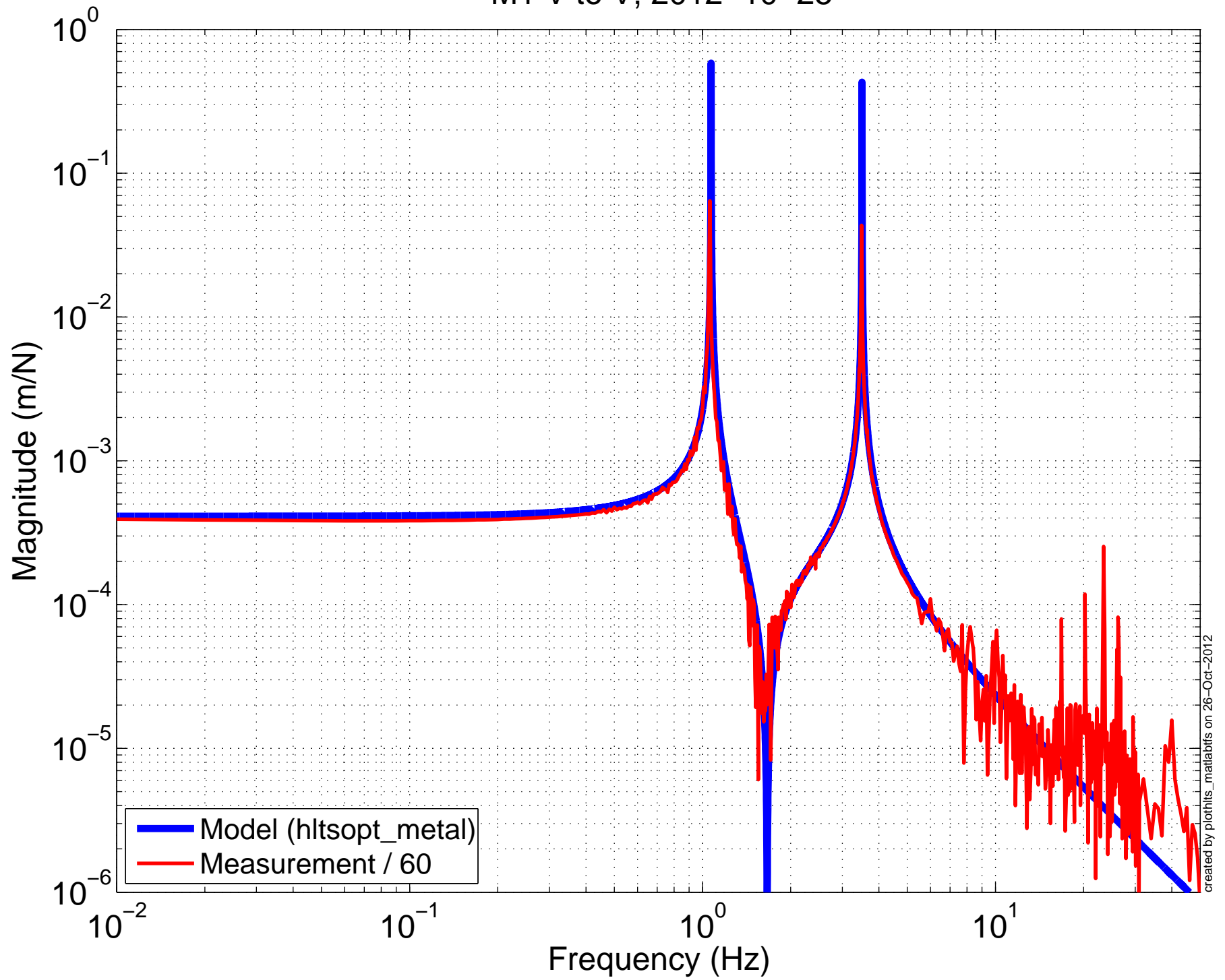


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X1SUSSR3
M1 T to T, 2012-10-25

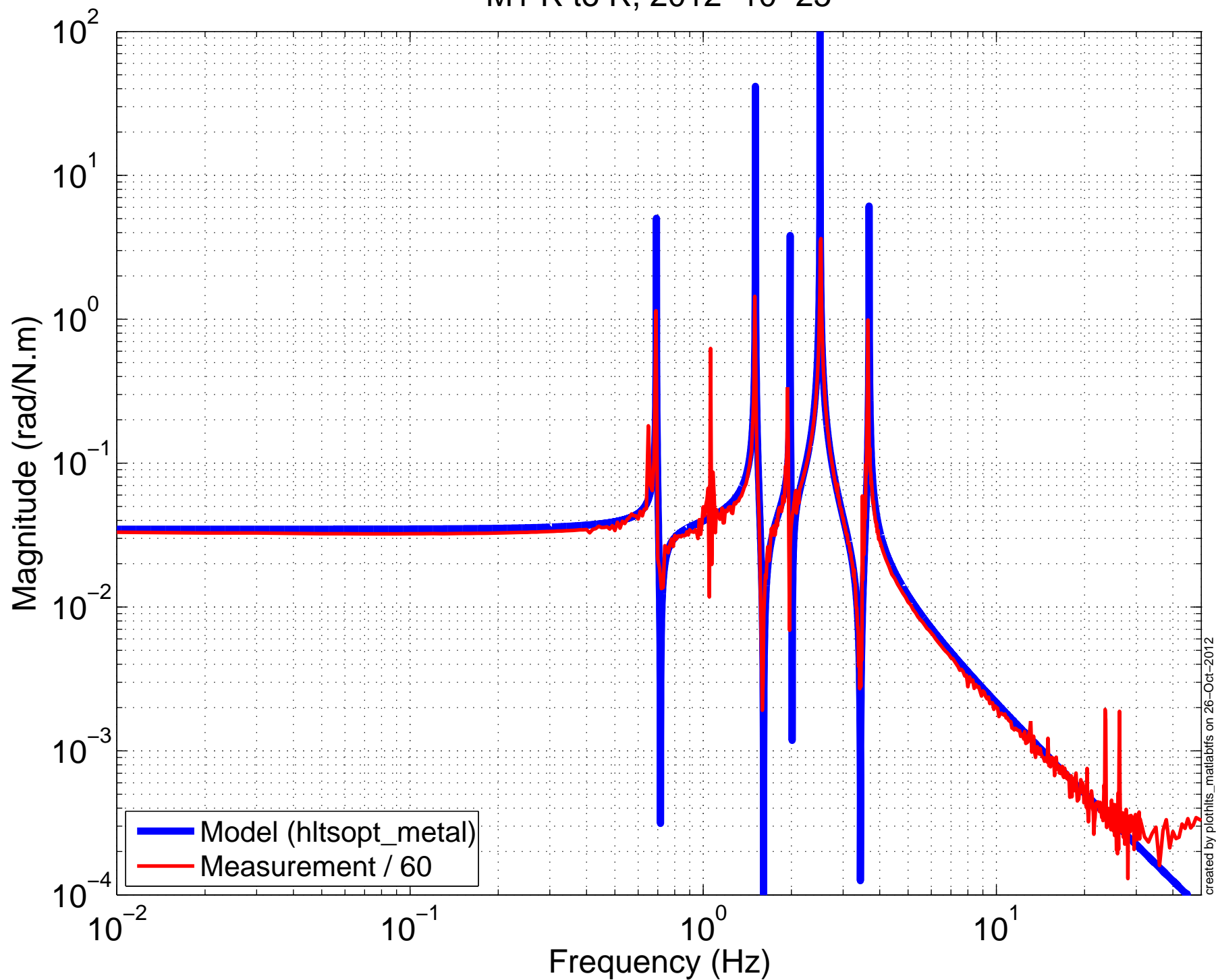


X1SUSSR3
M1 V to V, 2012-10-25

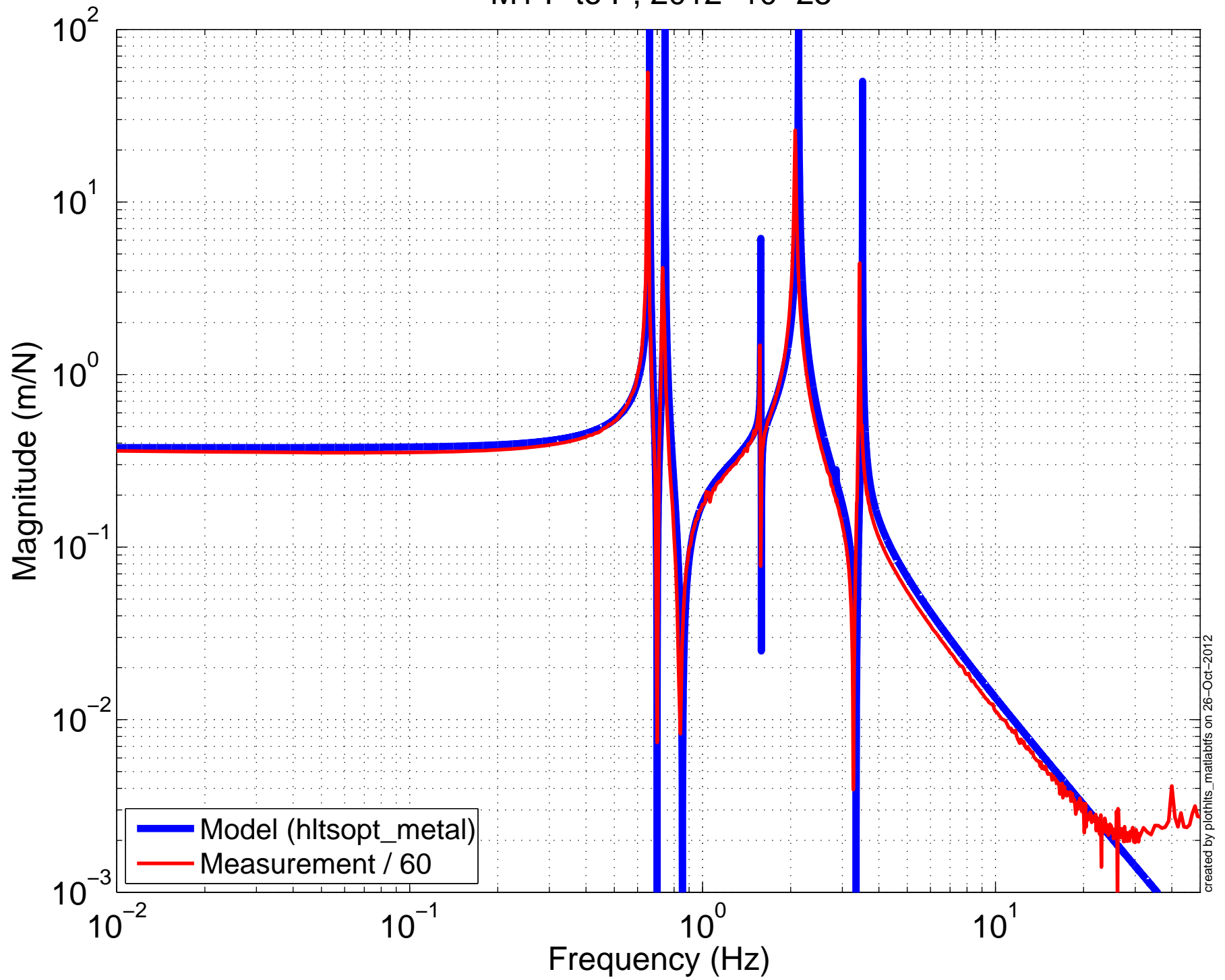


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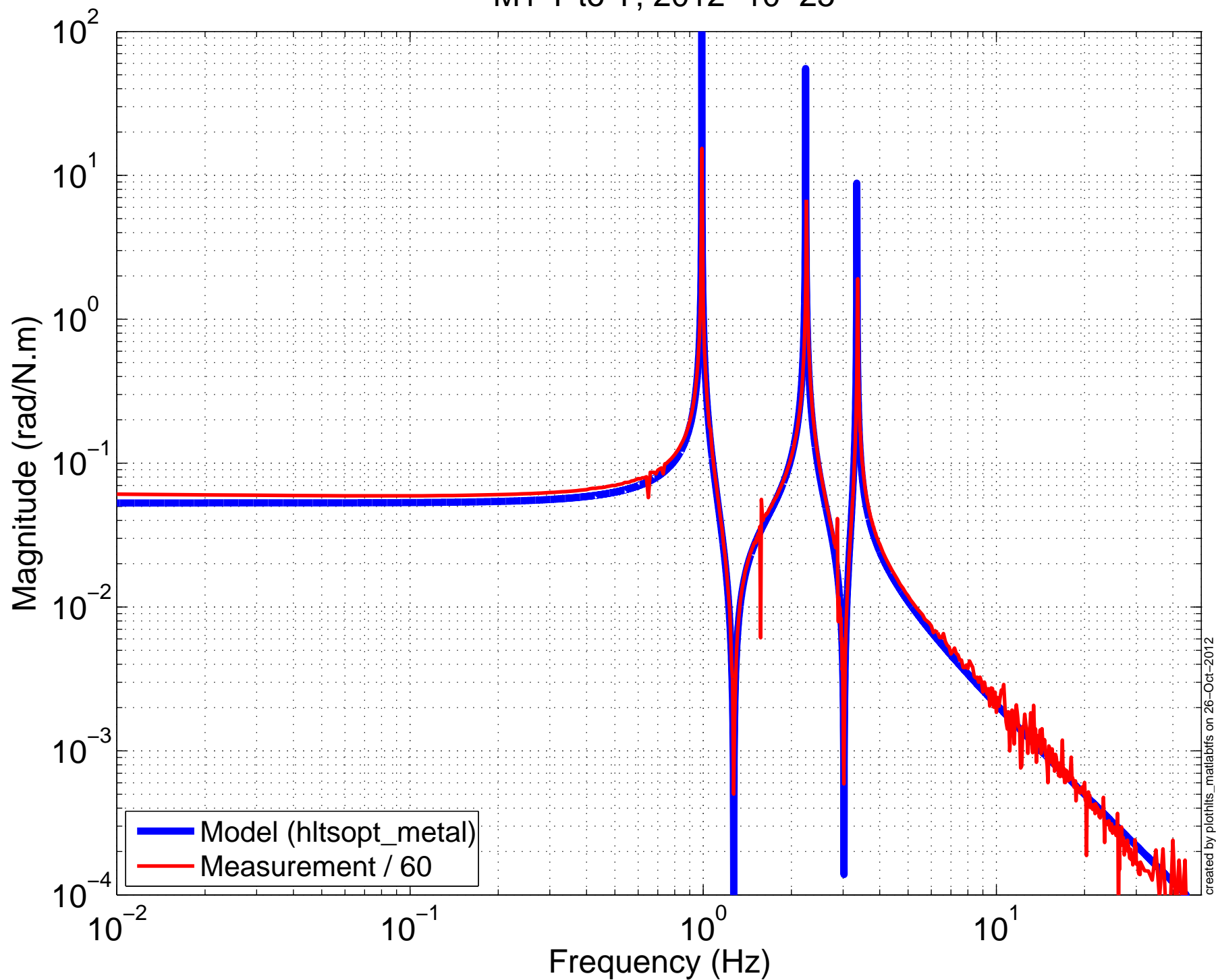
X1SUSSR3
M1 R to R, 2012-10-25



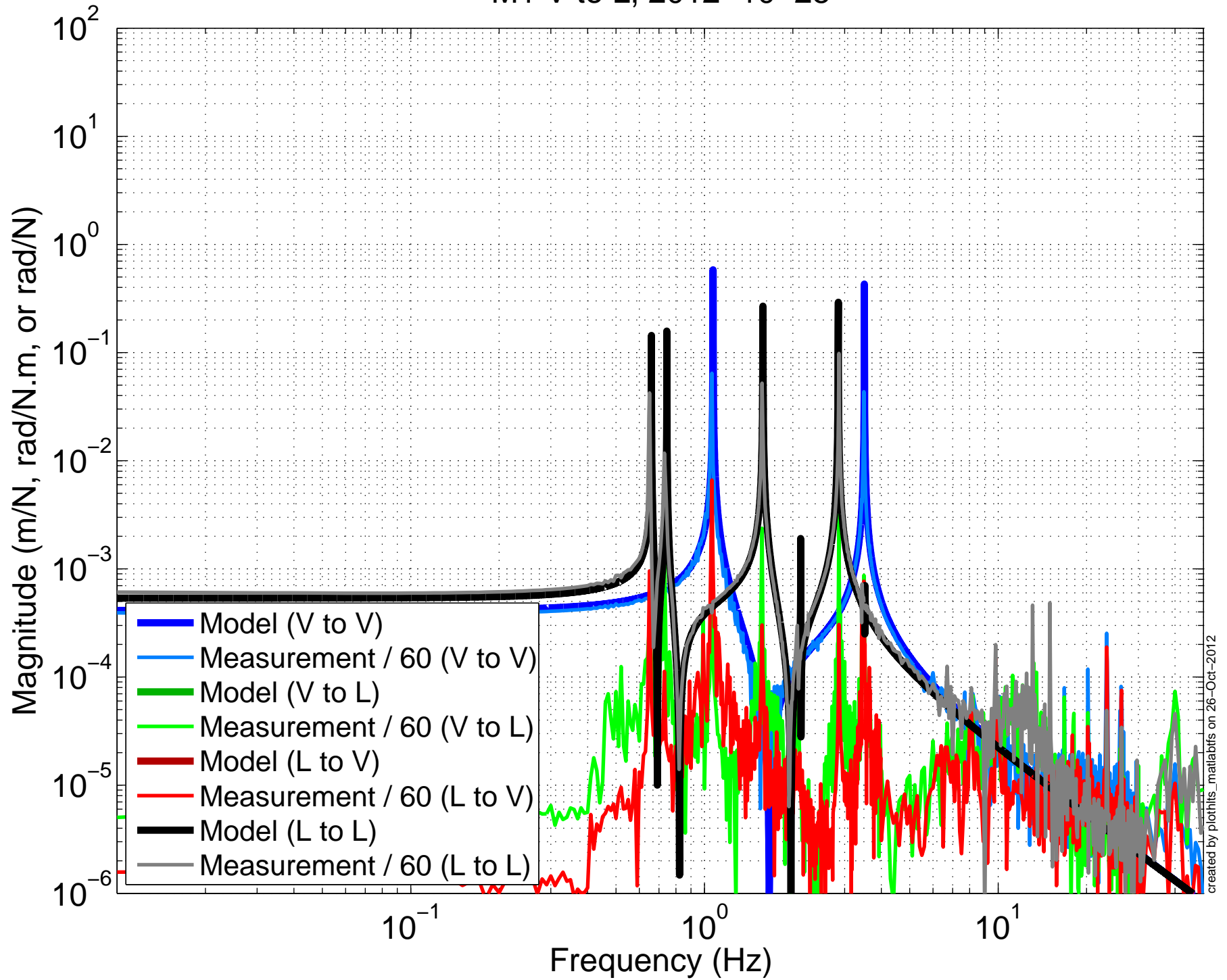
X1SUSSR3
M1 P to P, 2012-10-25



X1SUSSR3
M1 Y to Y, 2012-10-25

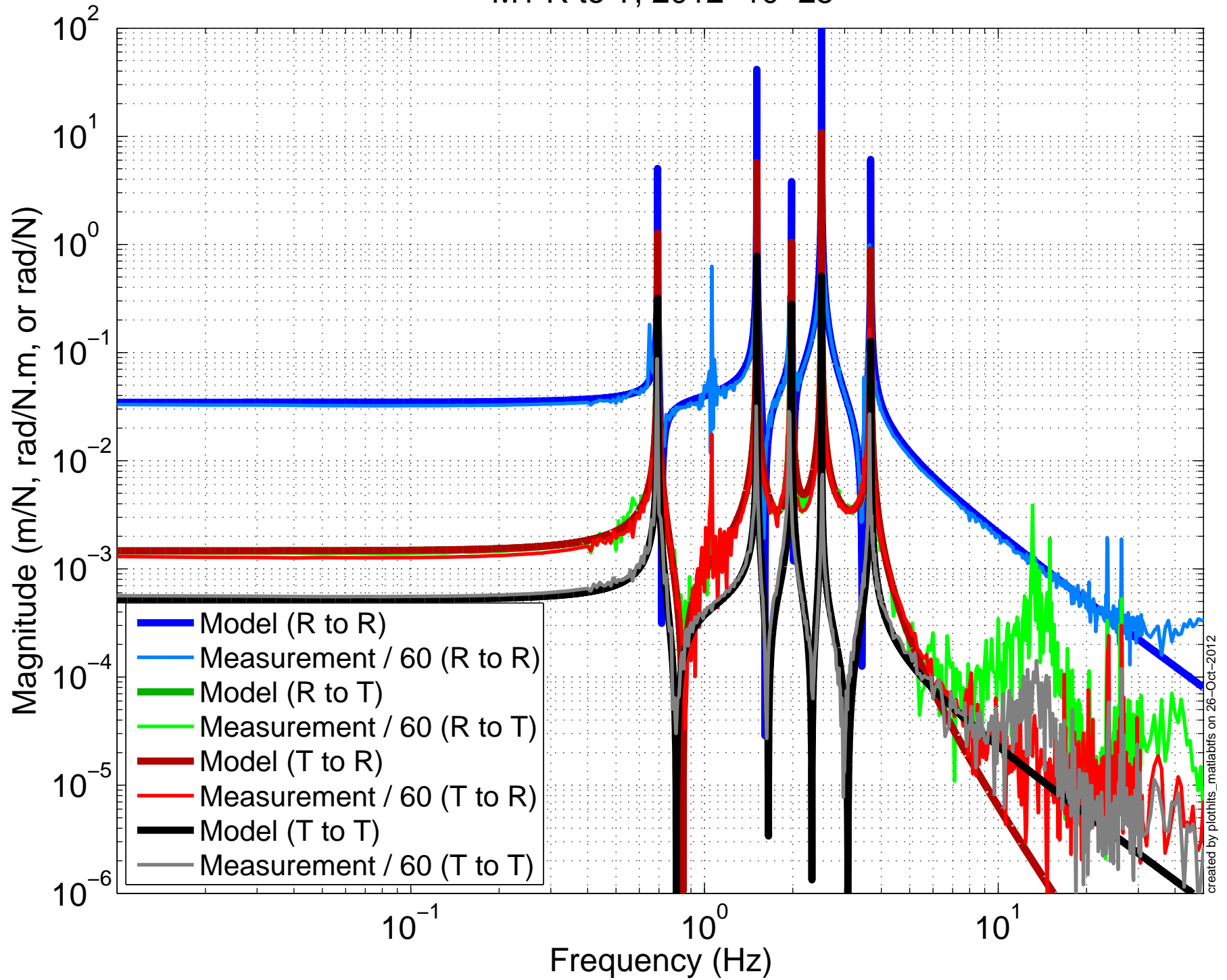


X1SUSSR3
M1 V to L, 2012-10-25

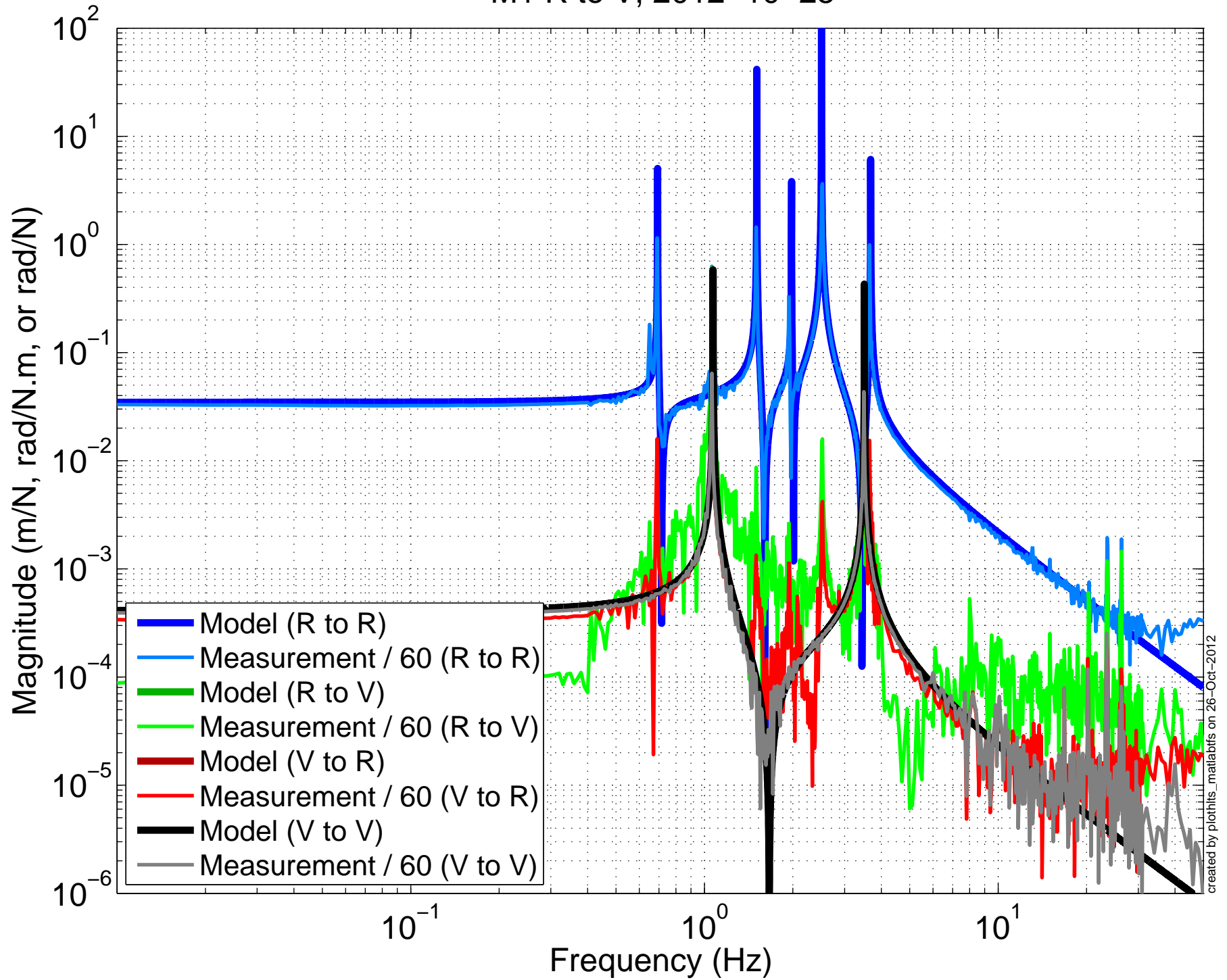


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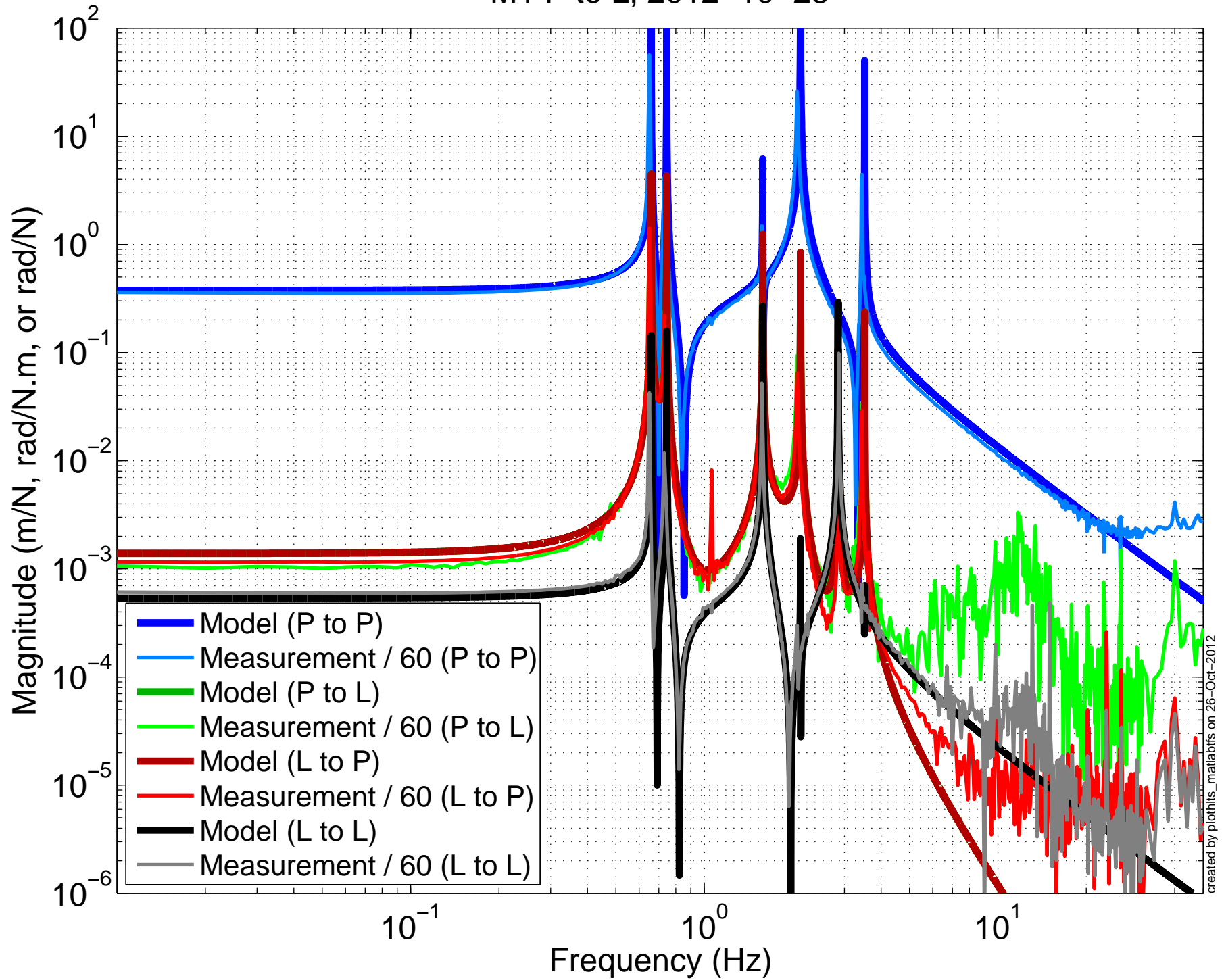
M1 R to T, 2012-10-25



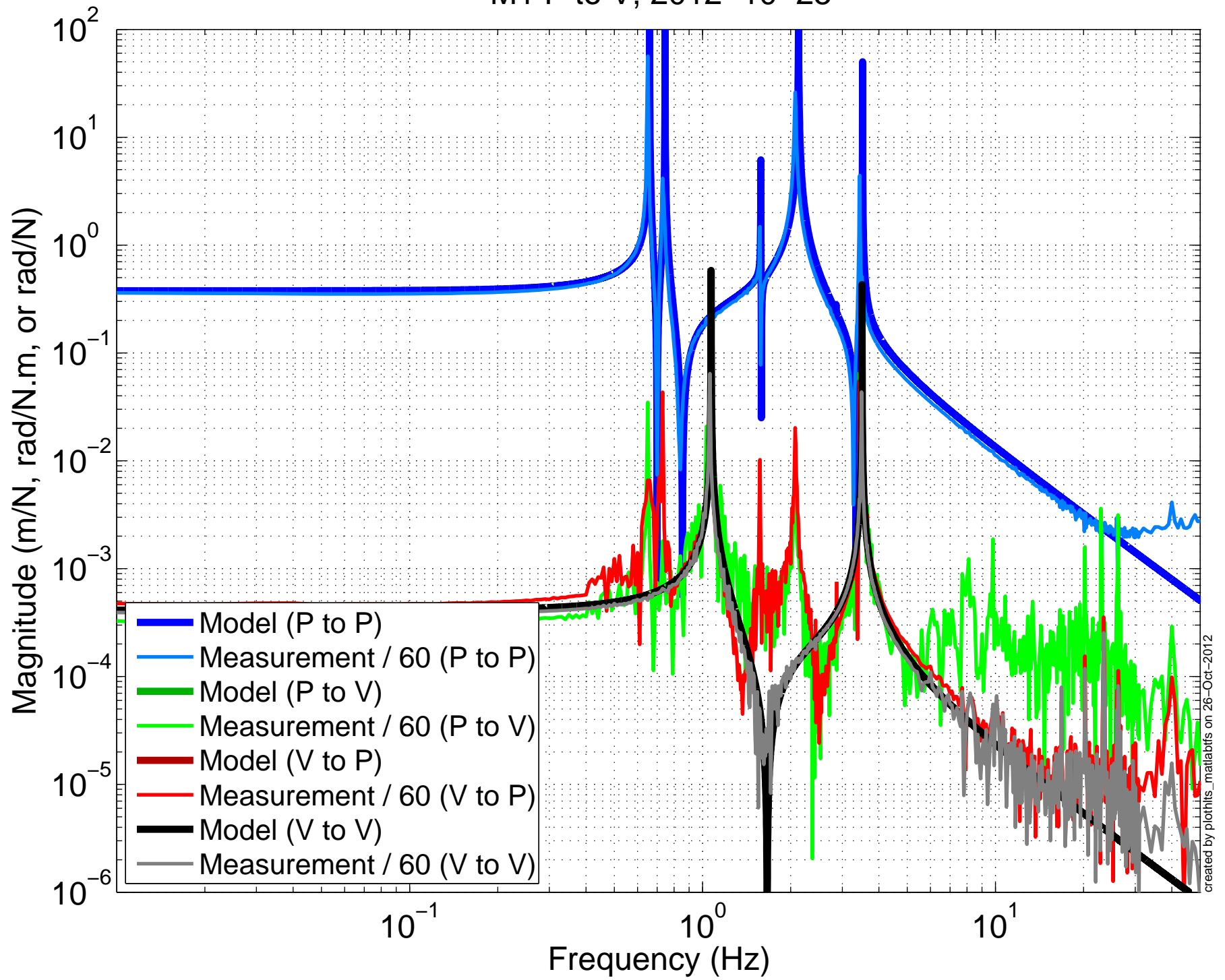
X1SUSSR3
M1 R to V, 2012-10-25



X1SUSSR3
M1 P to L, 2012-10-25

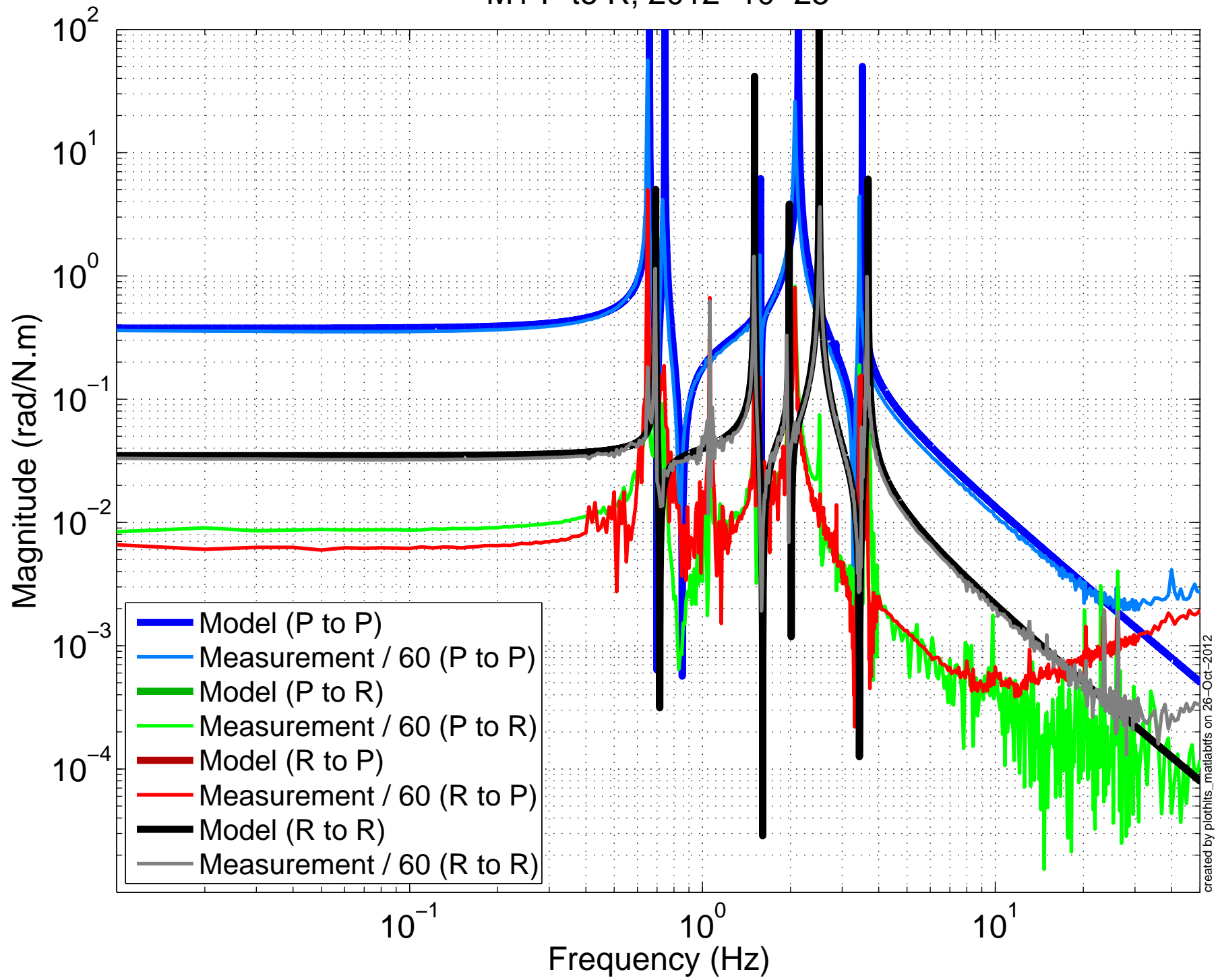


X1SUSSR3
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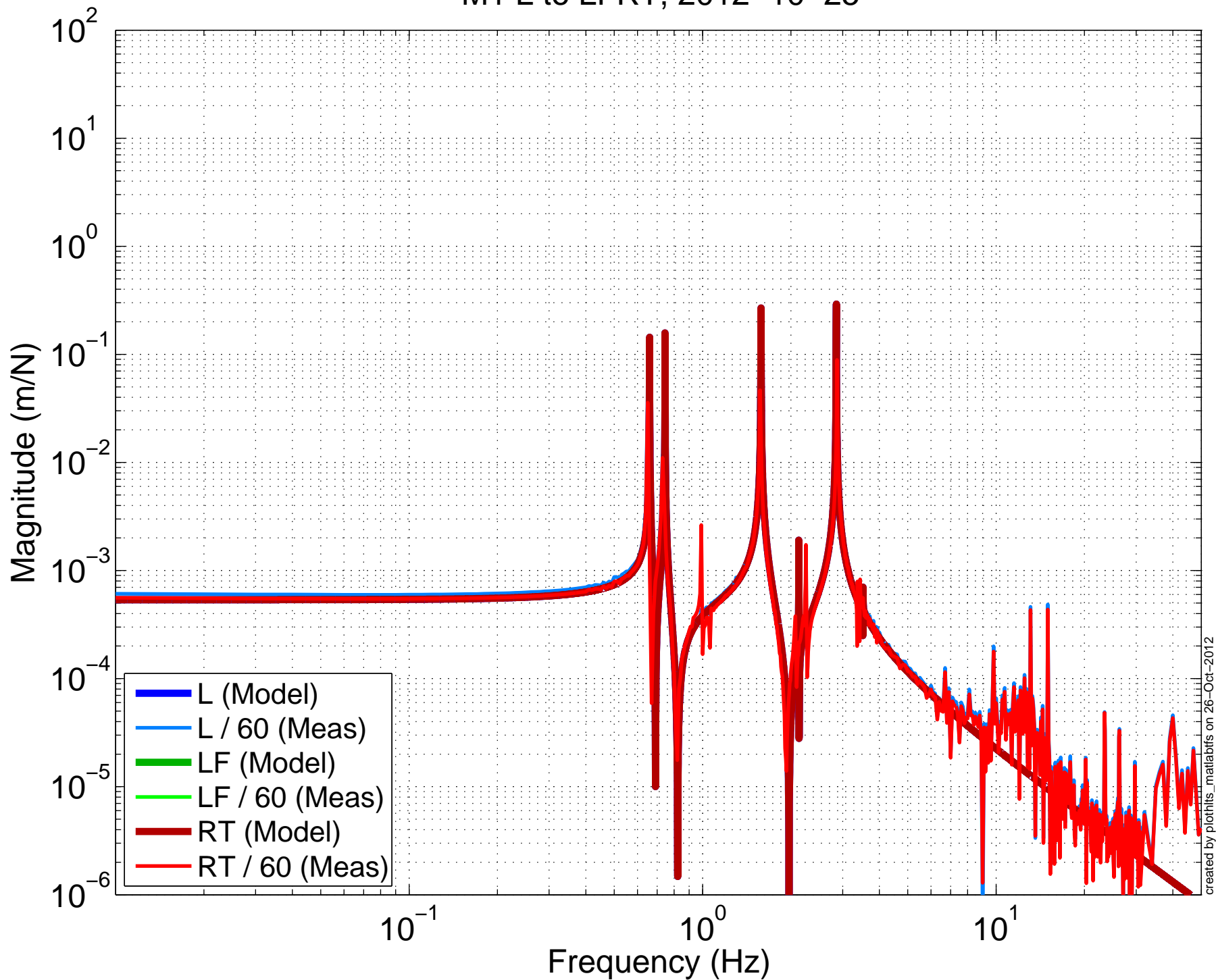
X1SUSSR3

M1 P to R, 2012-10-25



X1SUSSR3

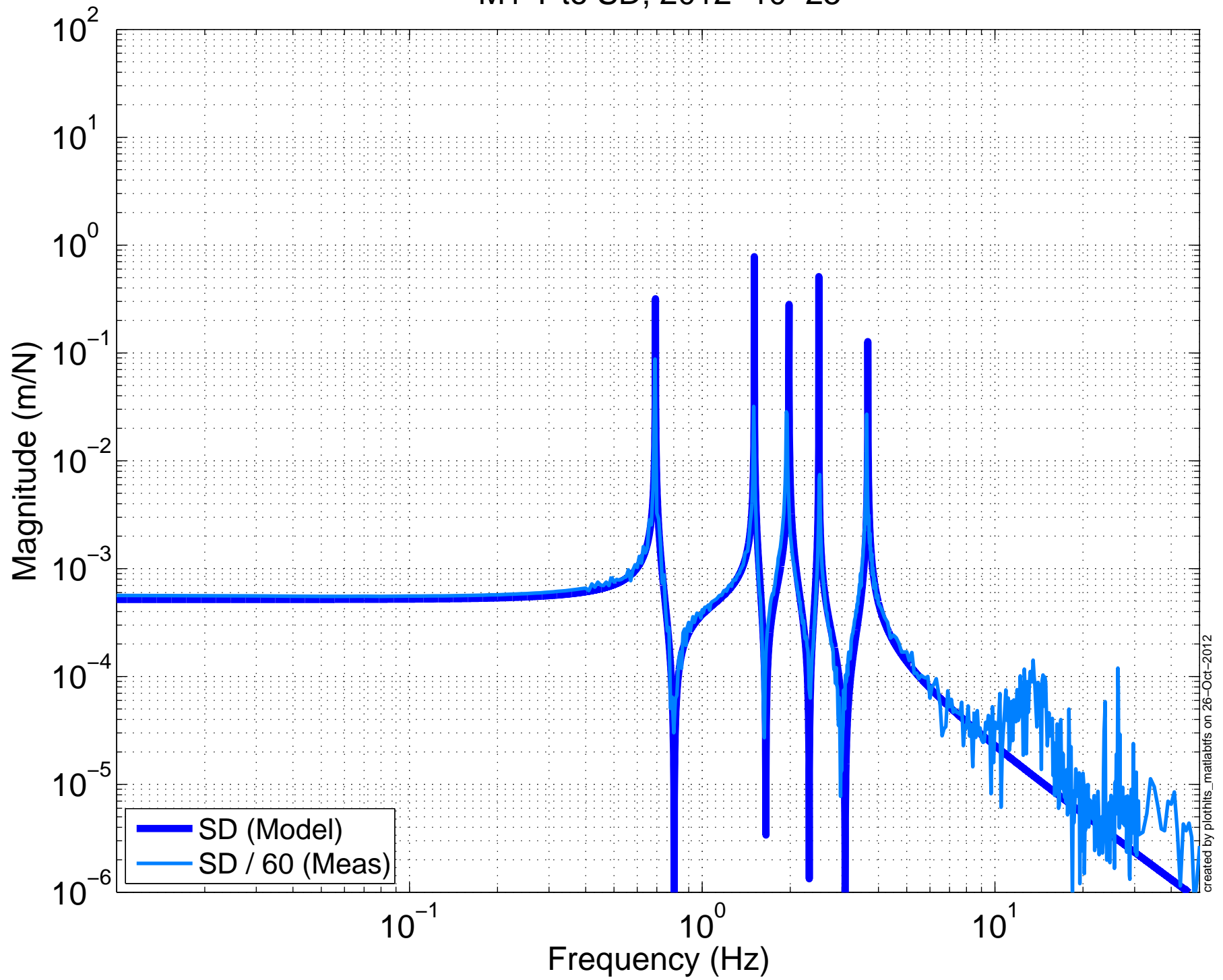
M1 L to LFRT, 2012-10-25



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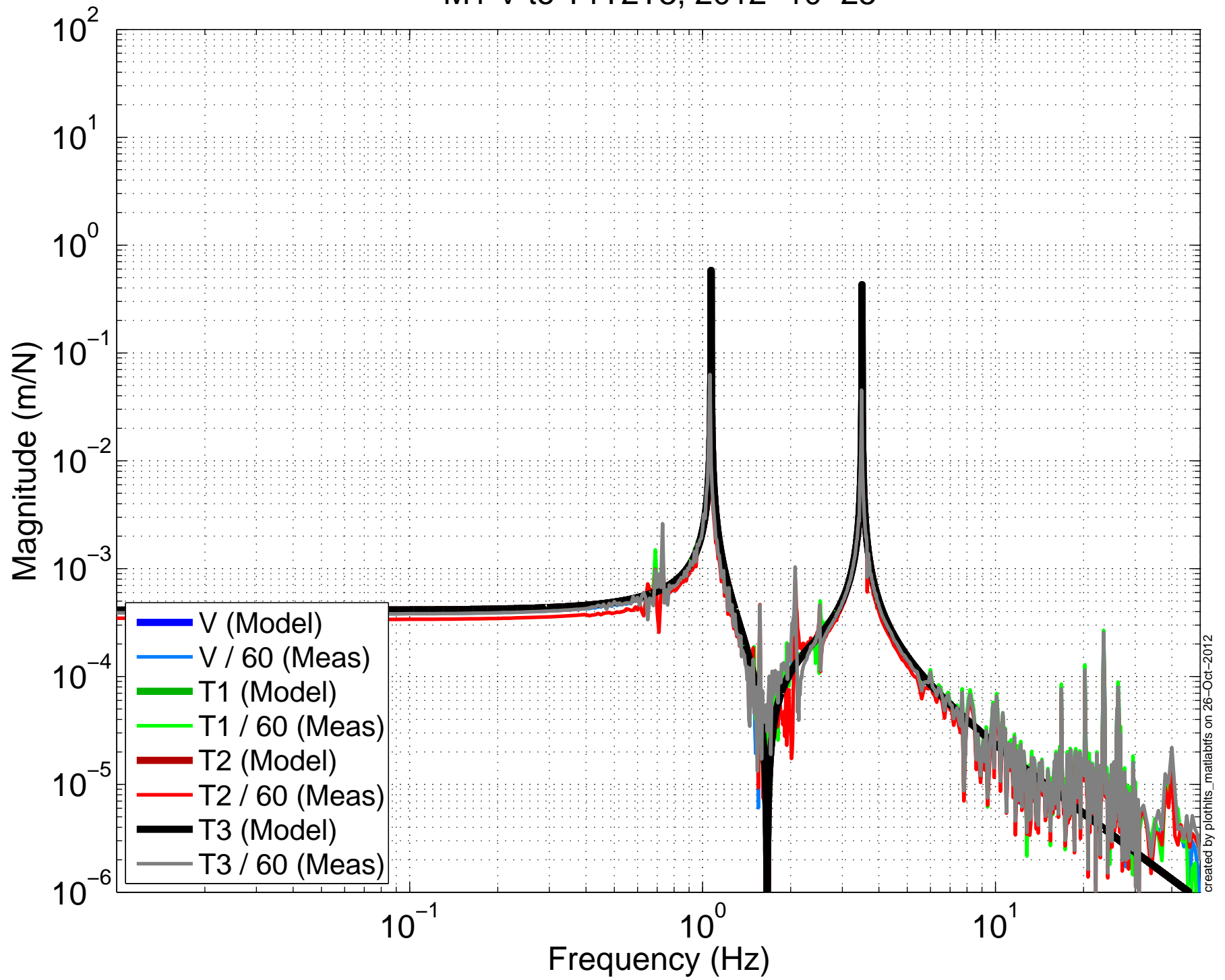
X1SUSSR3

M1 T to SD, 2012-10-25



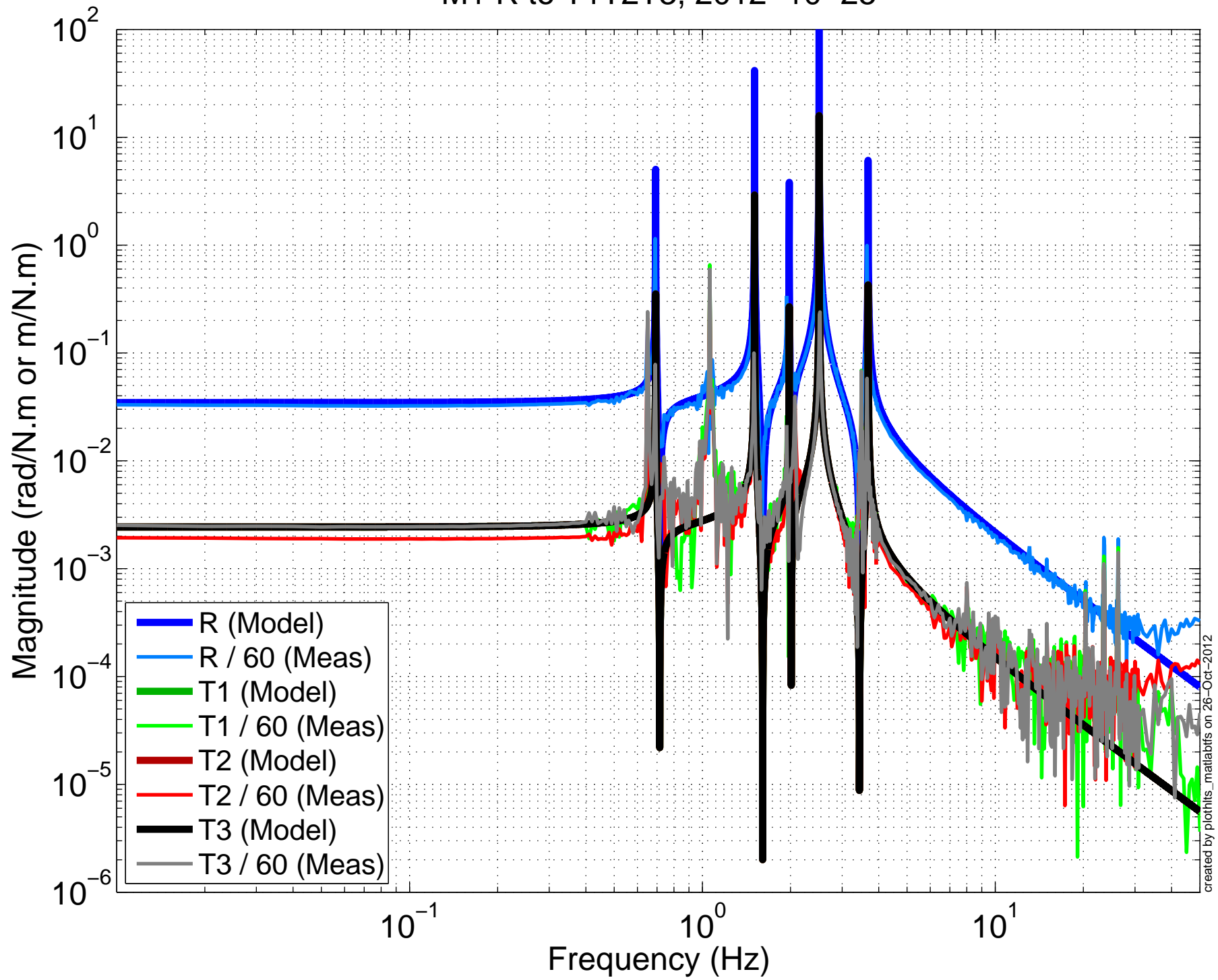
X1SUSSR3

M1 V to T1T2T3, 2012-10-25

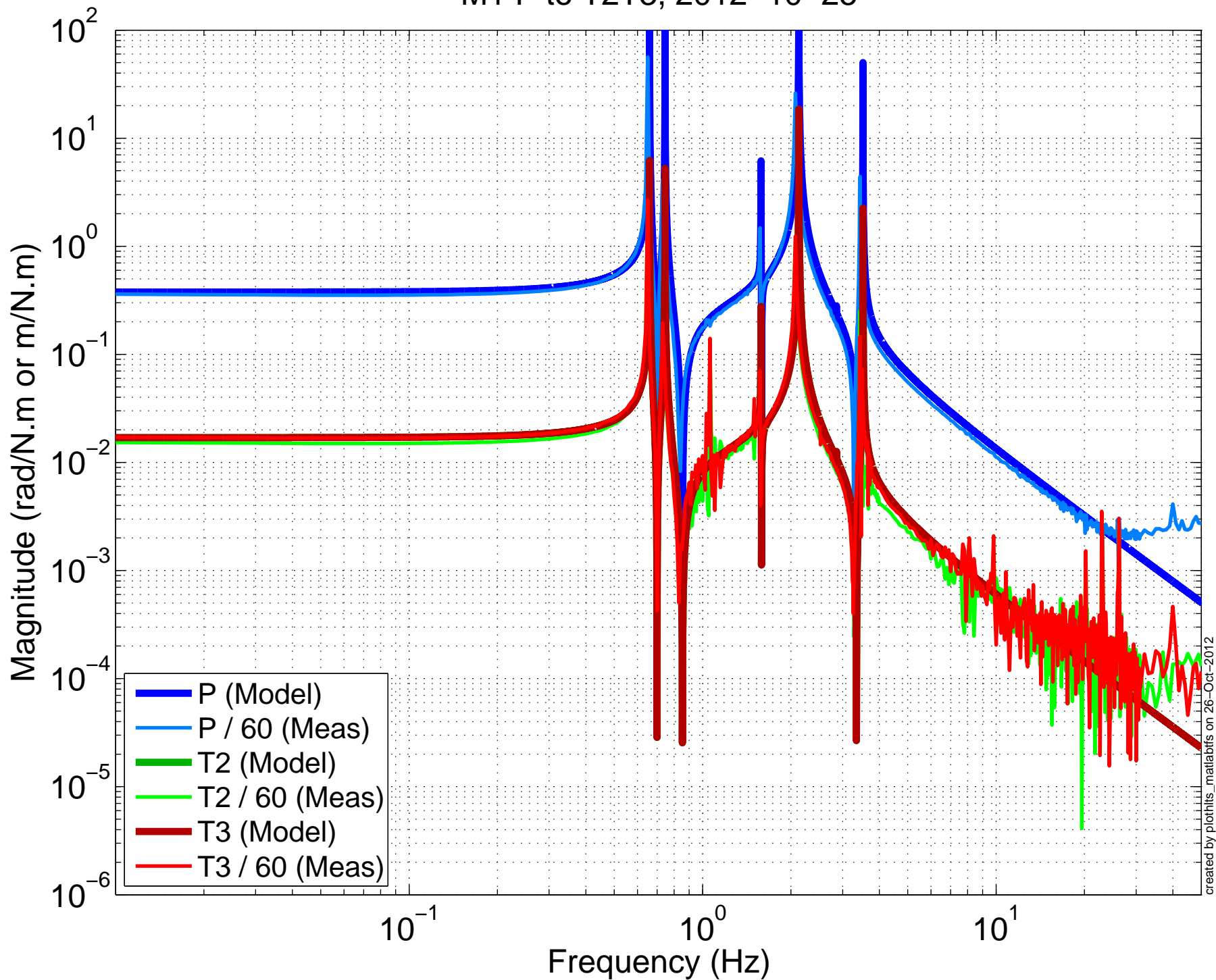


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X1SUSSR3
M1 R to T1T2T3, 2012-10-25



X1SUSSR3
M1 P to T2T3, 2012-10-25



X1SUSSR3

M1 Y to LFRT, 2012-10-25

