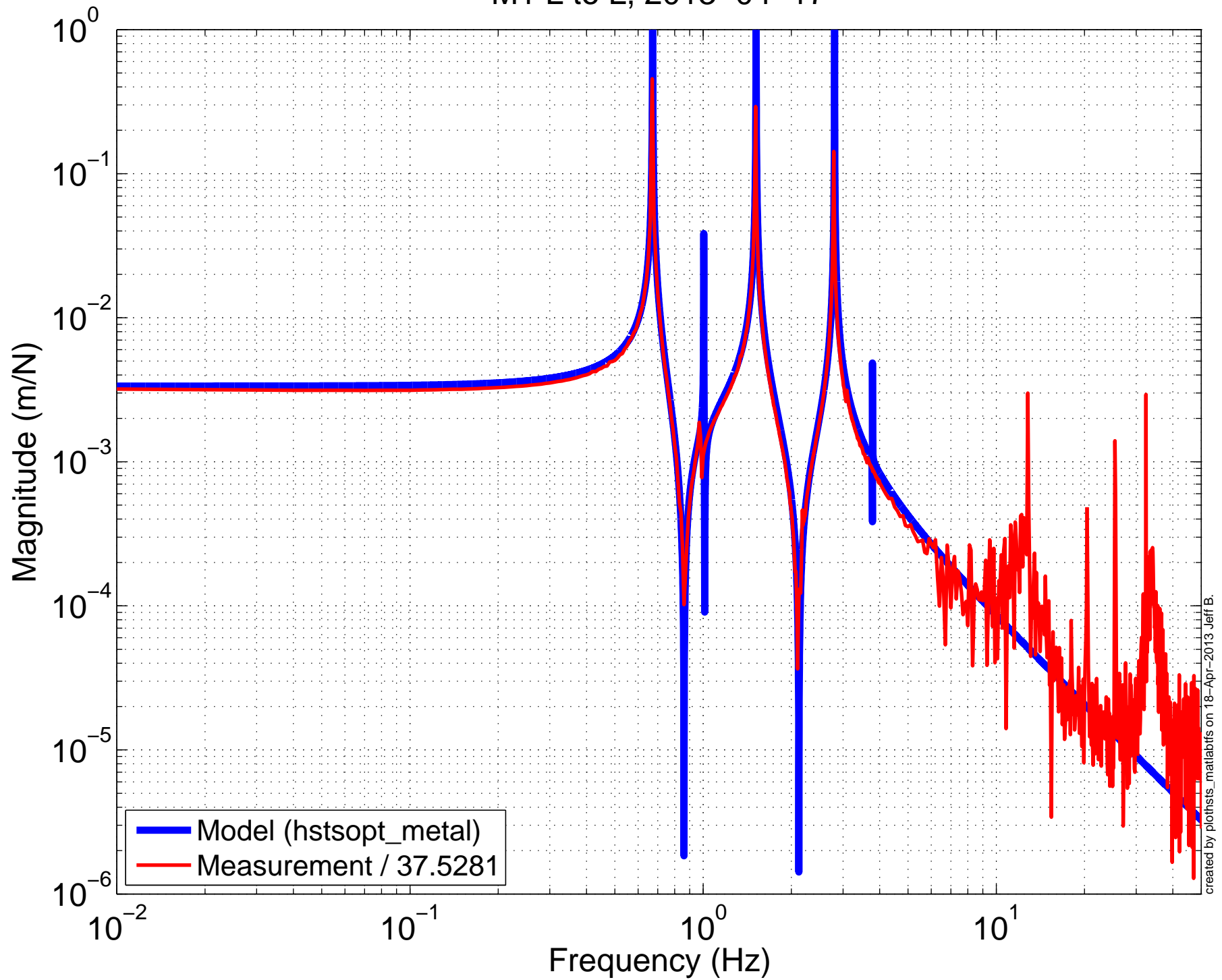
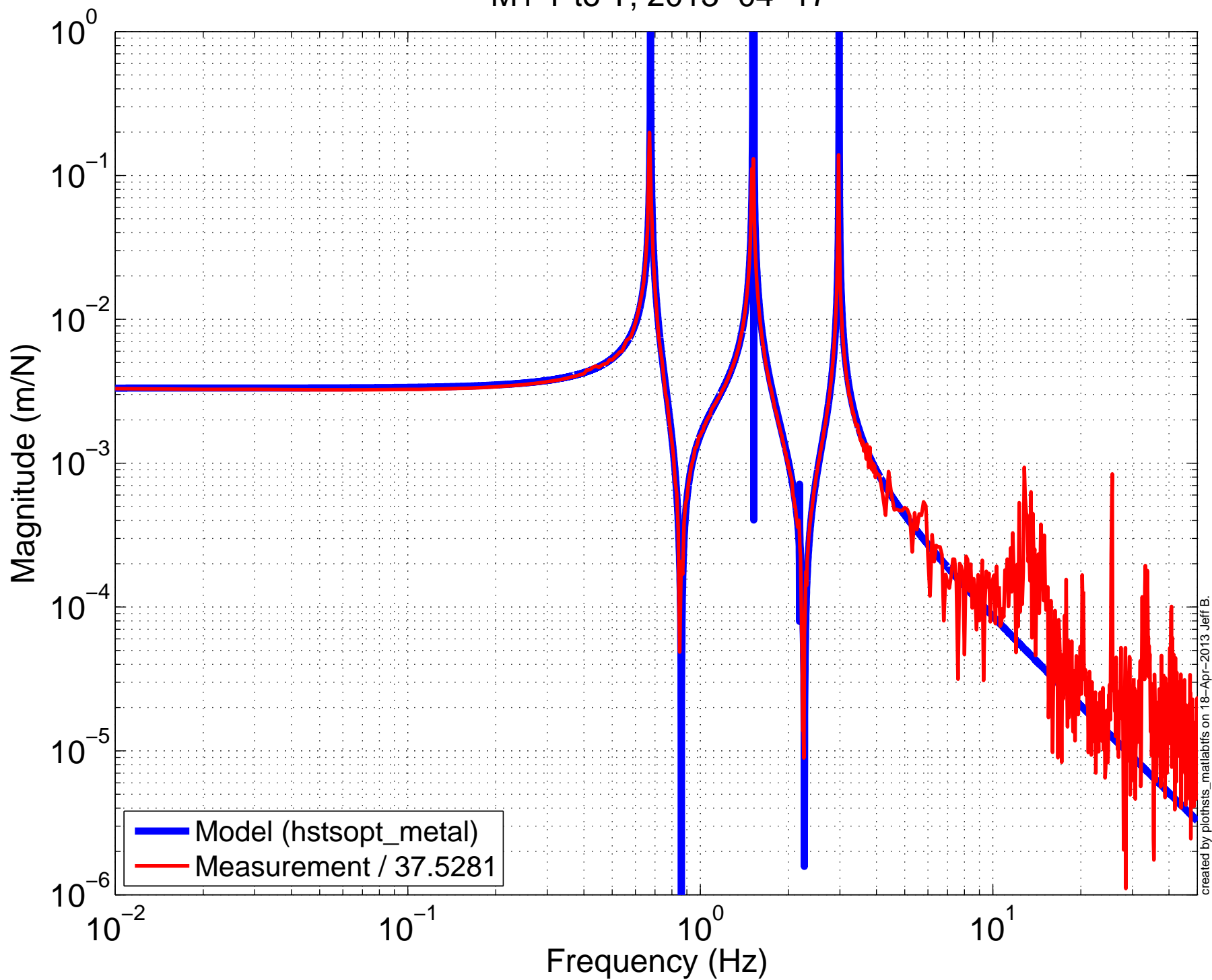


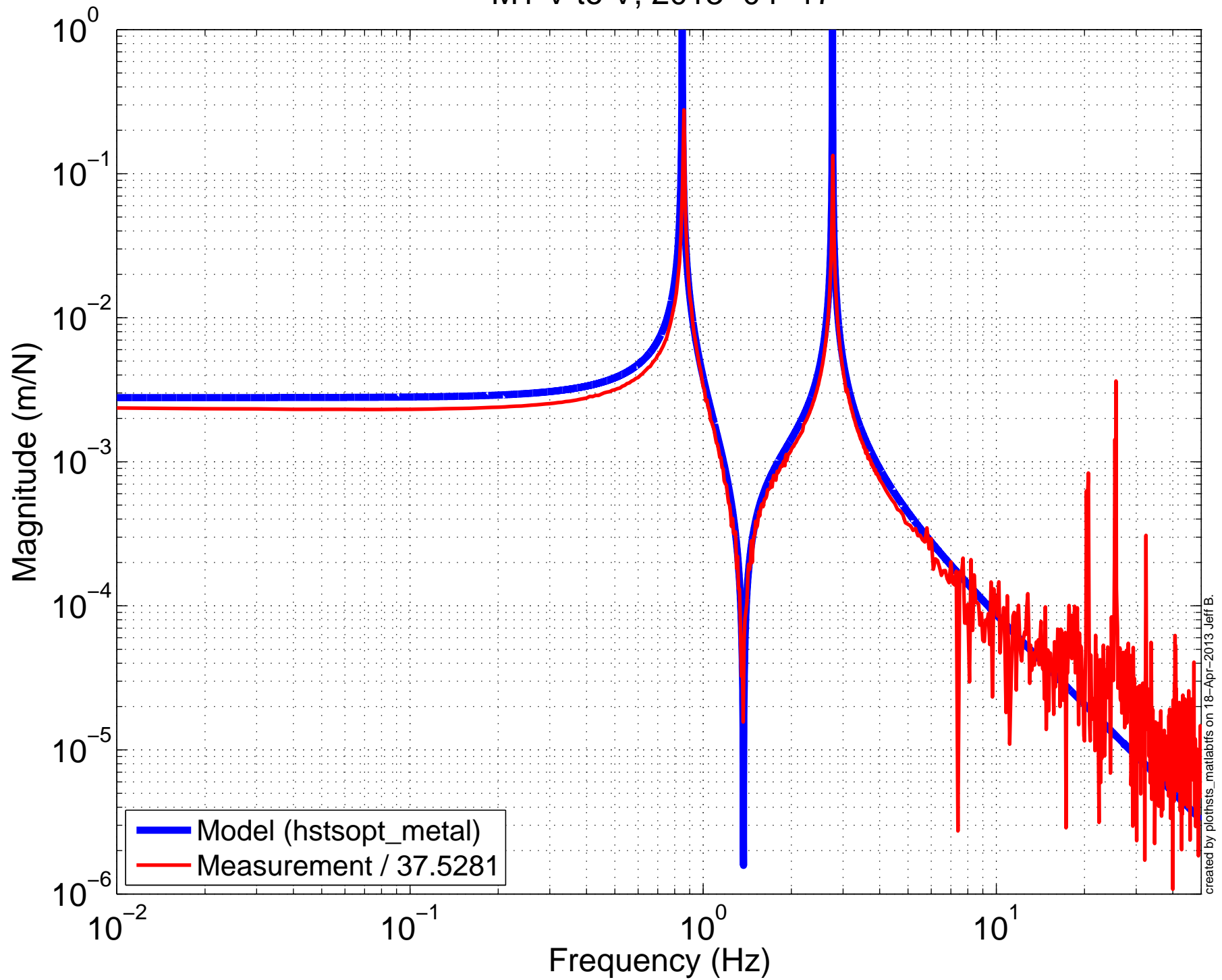
X1SUSPRM  
M1 L to L, 2013-04-17



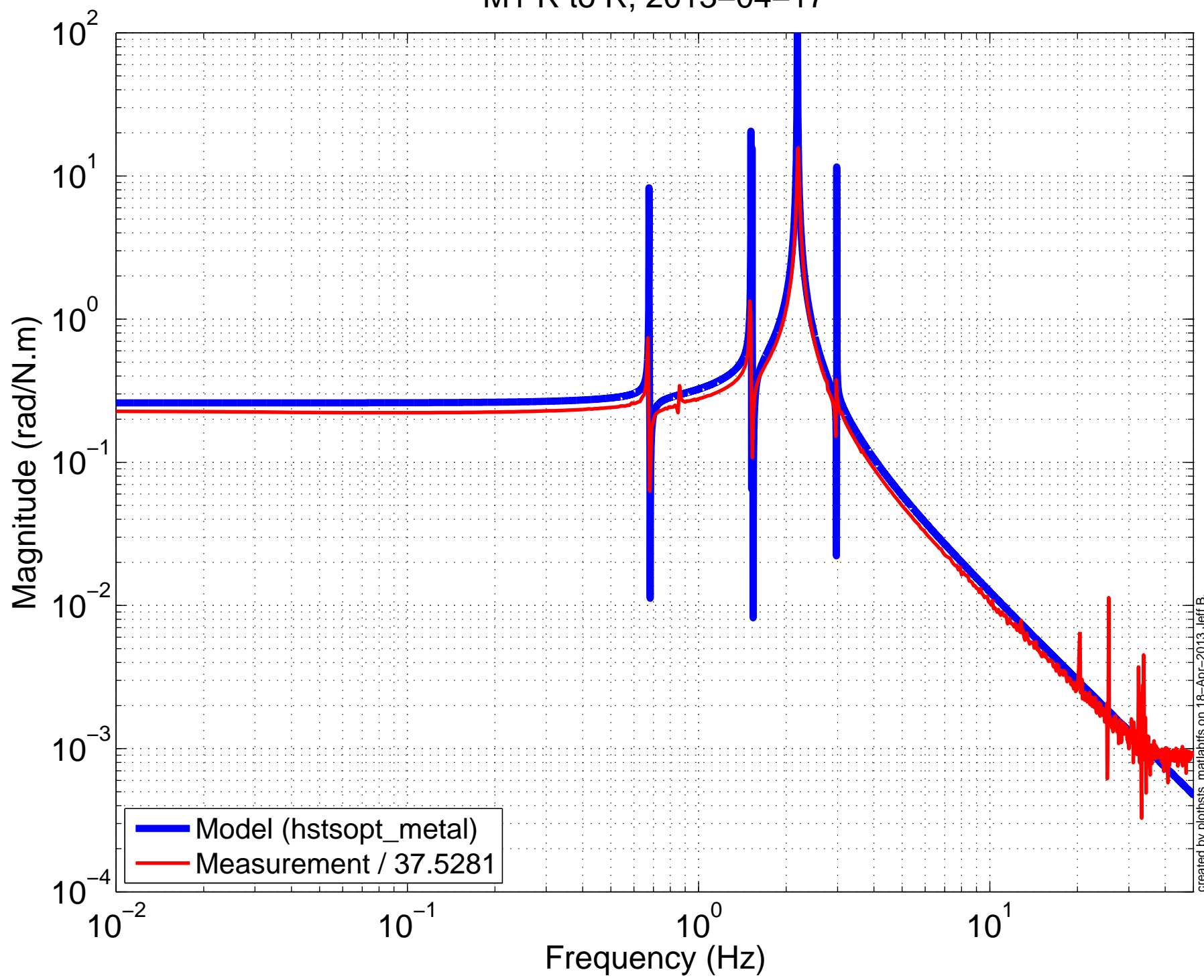
X1SUSPRM  
M1 T to T, 2013-04-17



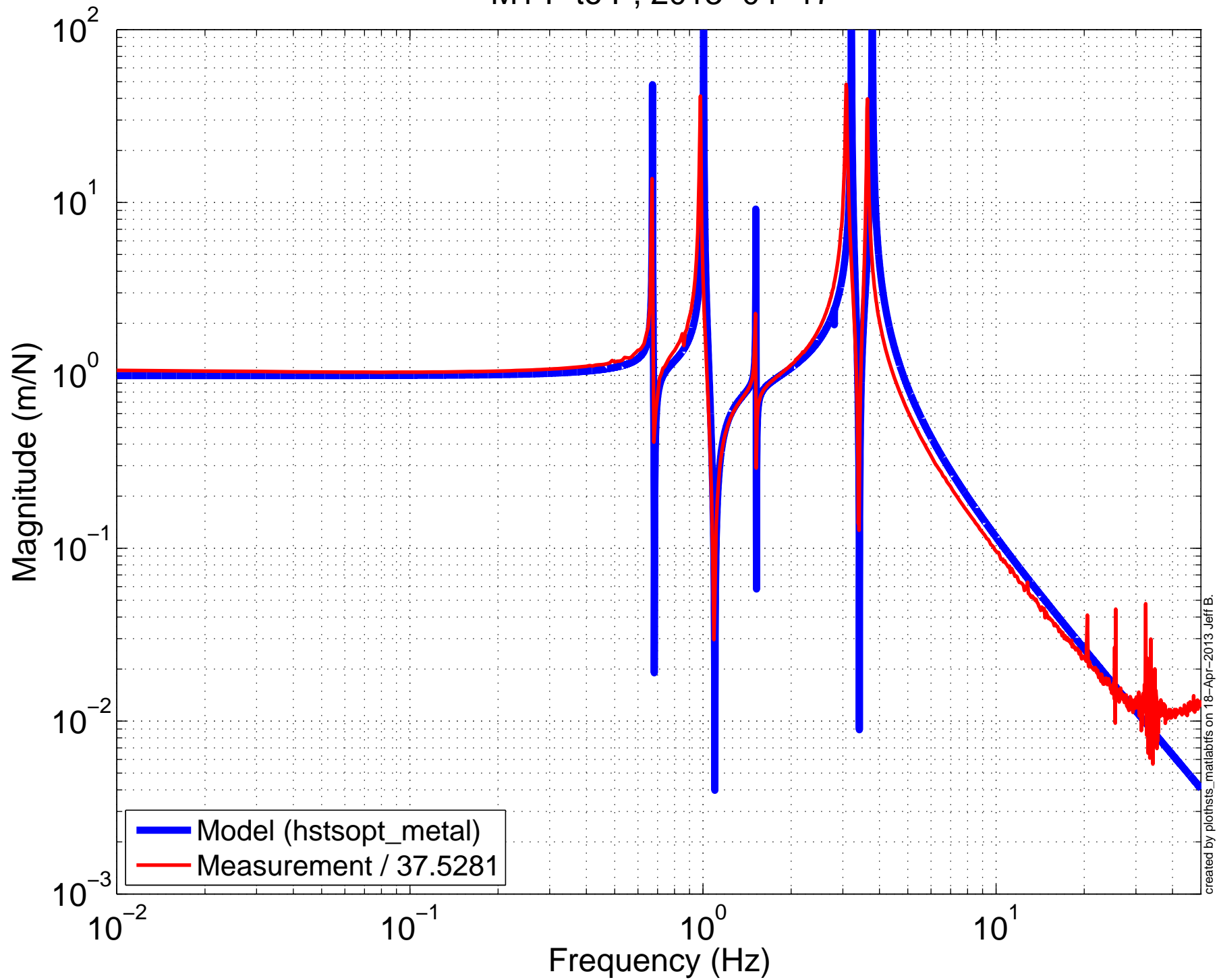
X1SUSPRM  
M1 V to V, 2013-04-17



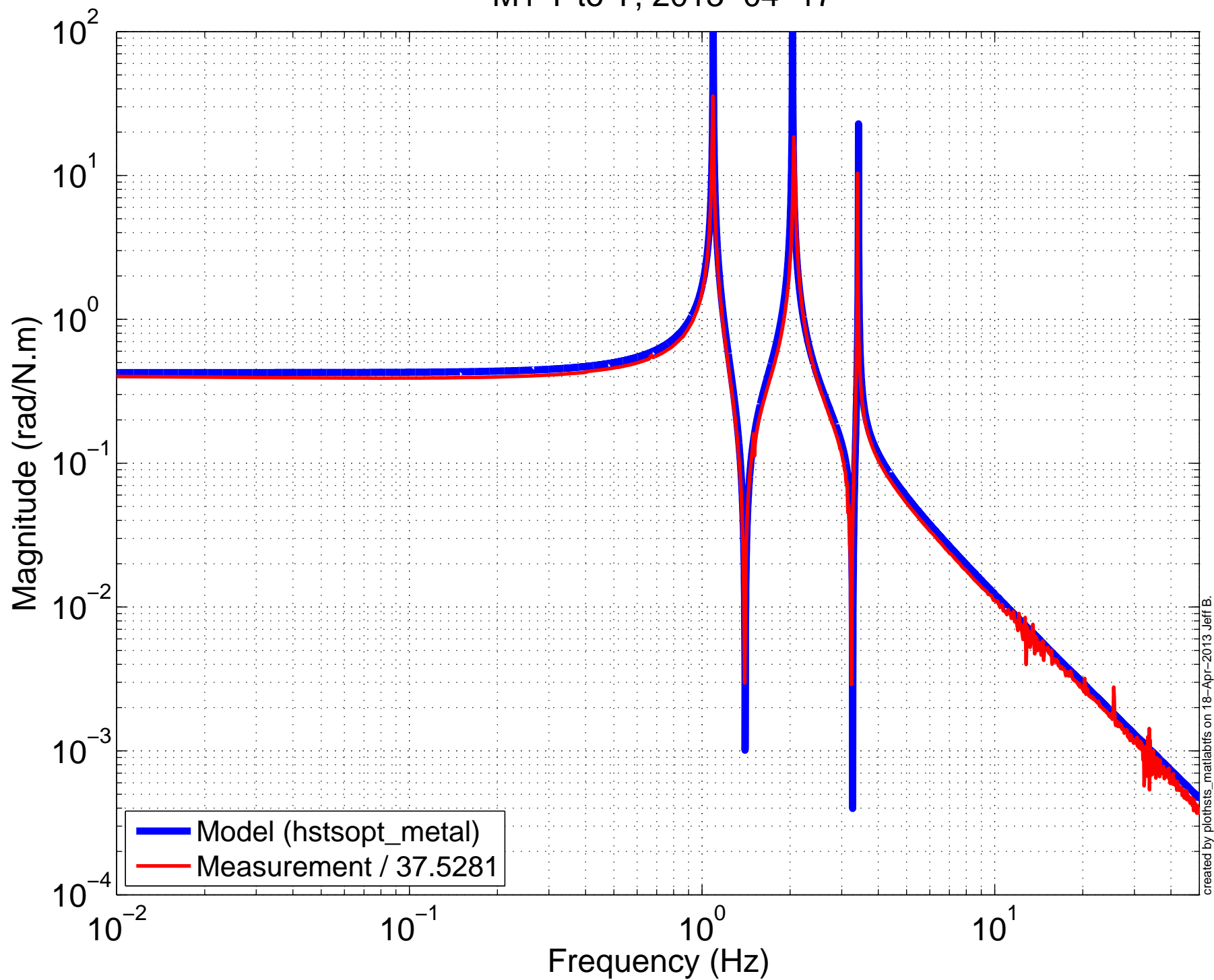
X1SUSPRM  
M1 R to R, 2013-04-17



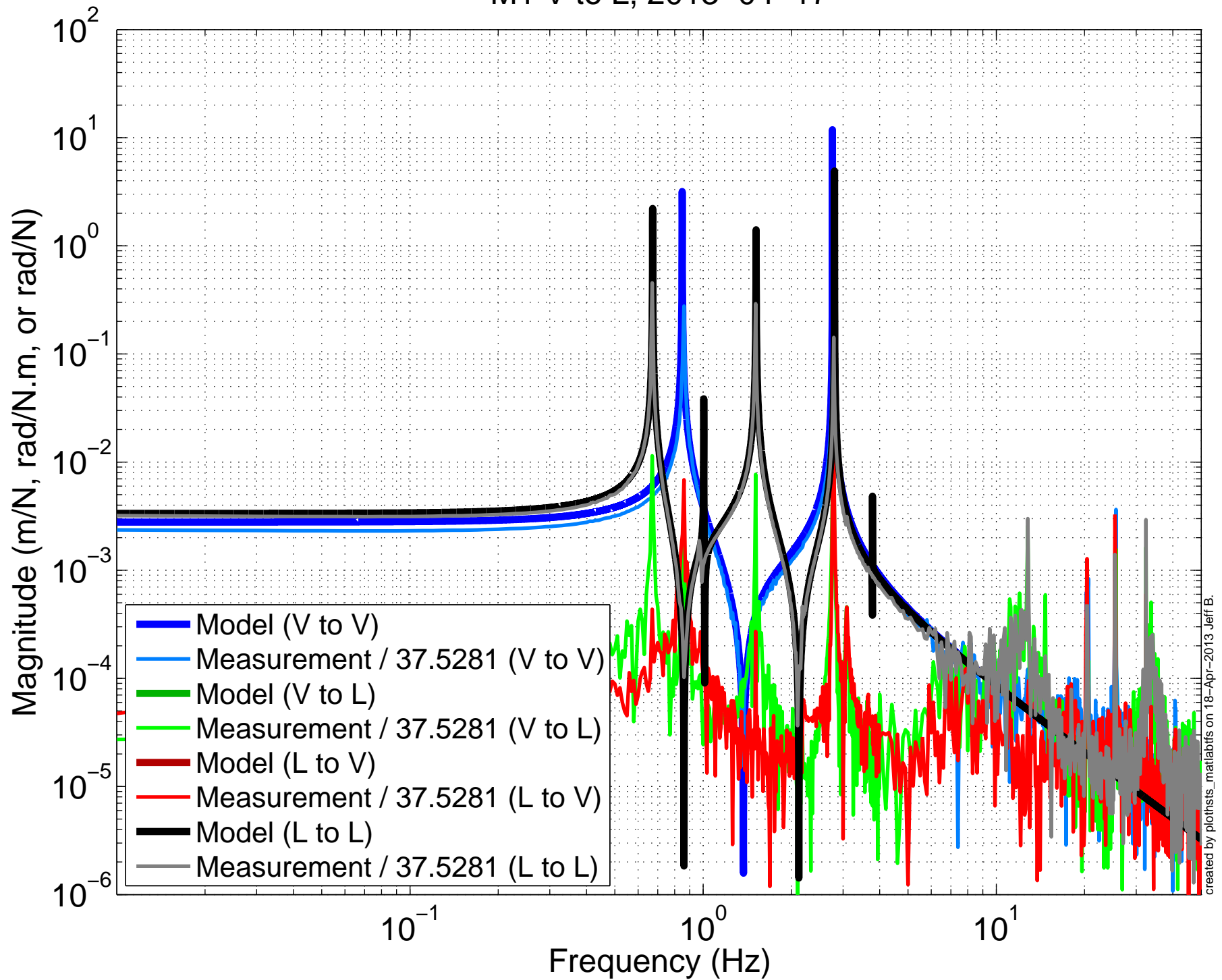
X1SUSPRM  
M1 P to P, 2013-04-17



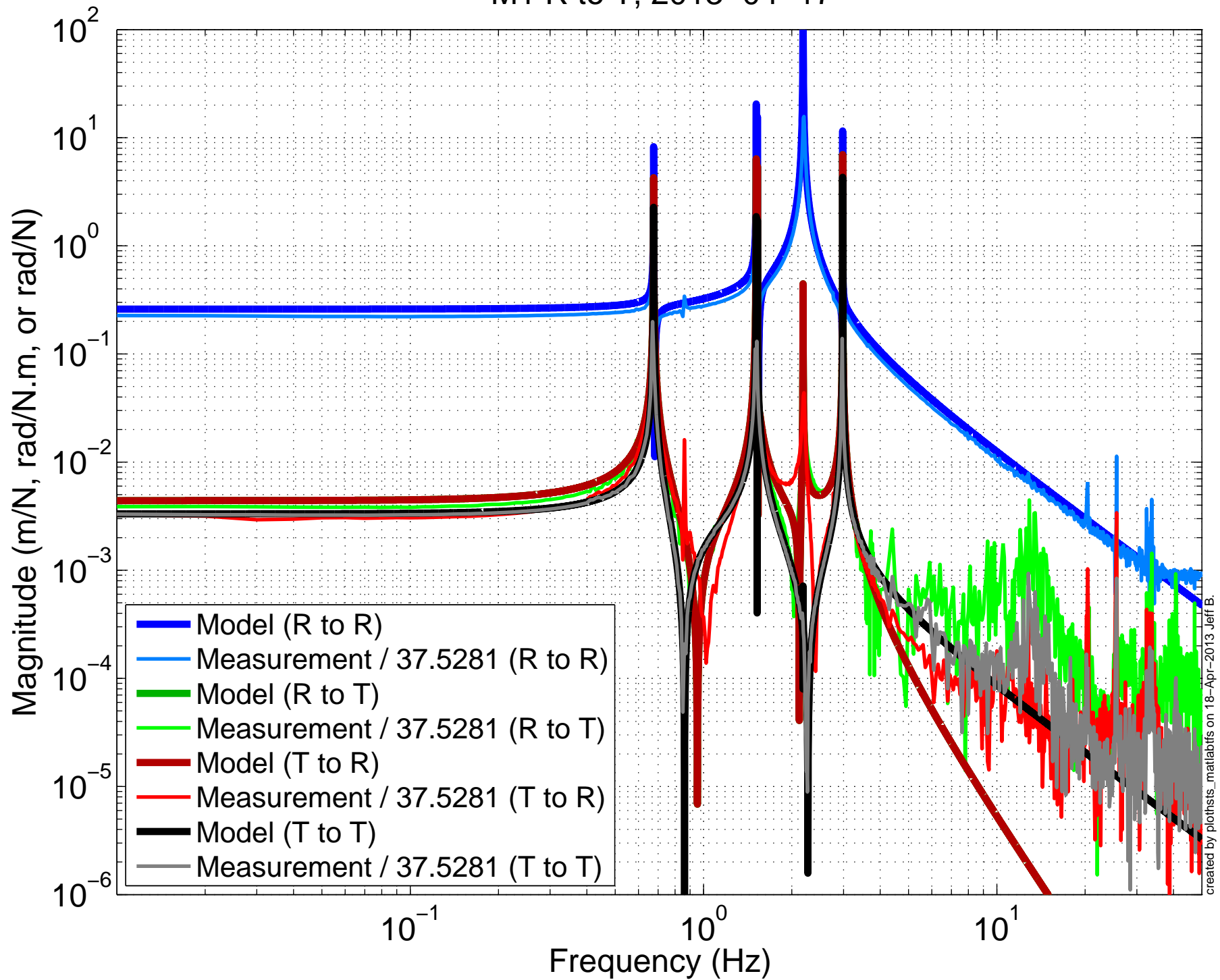
X1SUSPRM  
M1 Y to Y, 2013-04-17



X1SUSPRM  
M1 V to L, 2013-04-17

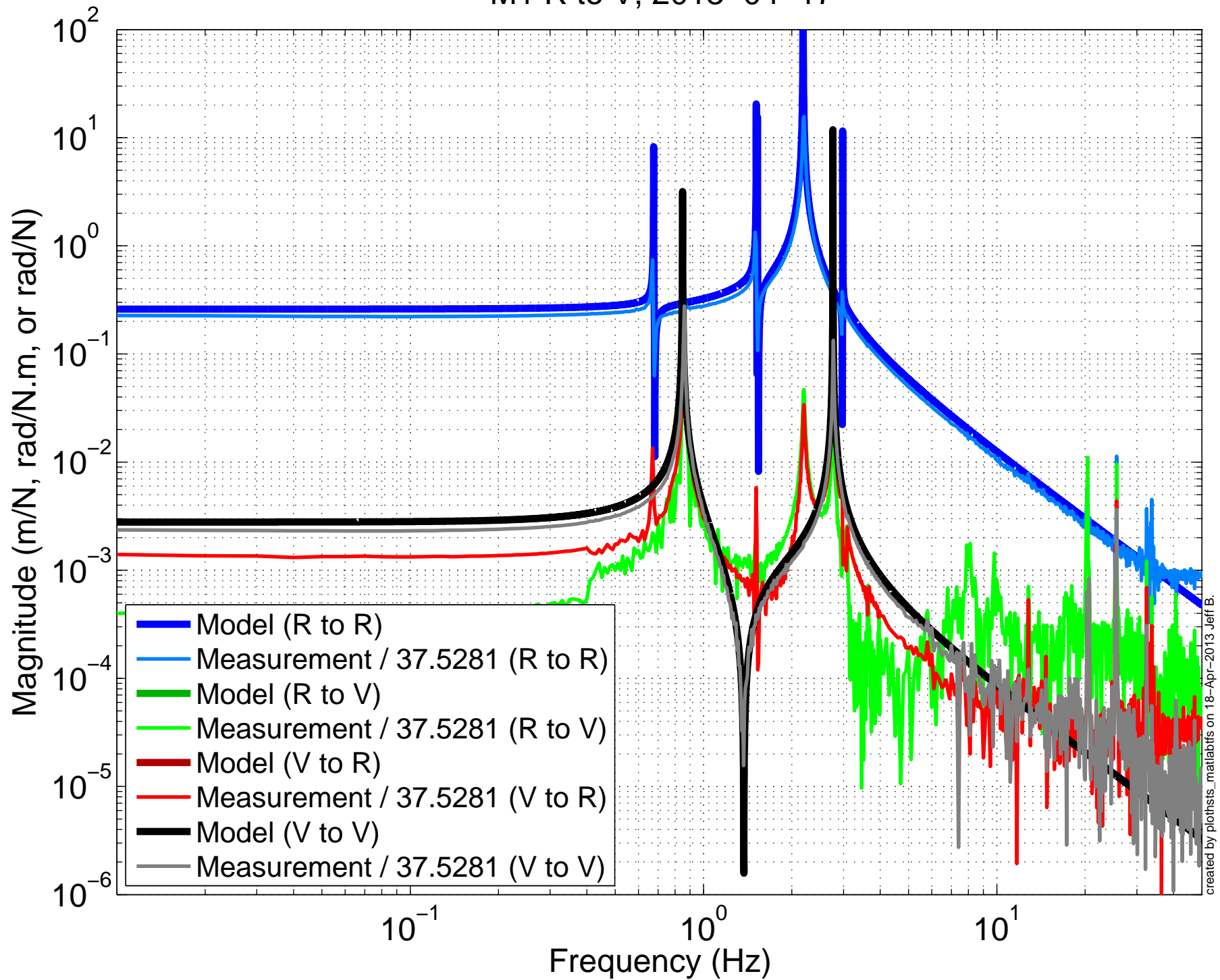


X1SUSPRM  
M1 R to T, 2013-04-17



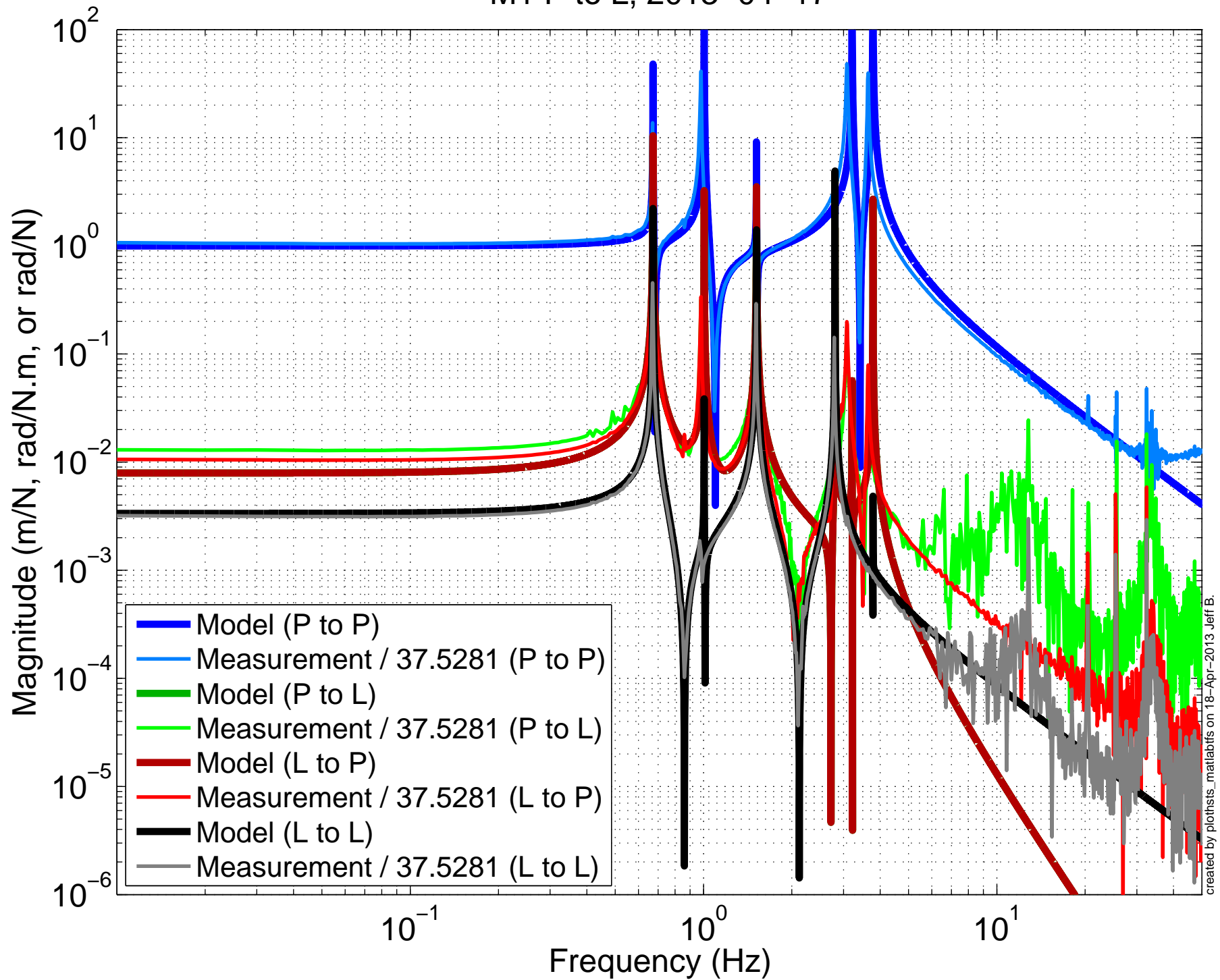


X1SUSPRM  
M1 R to V, 2013-04-17

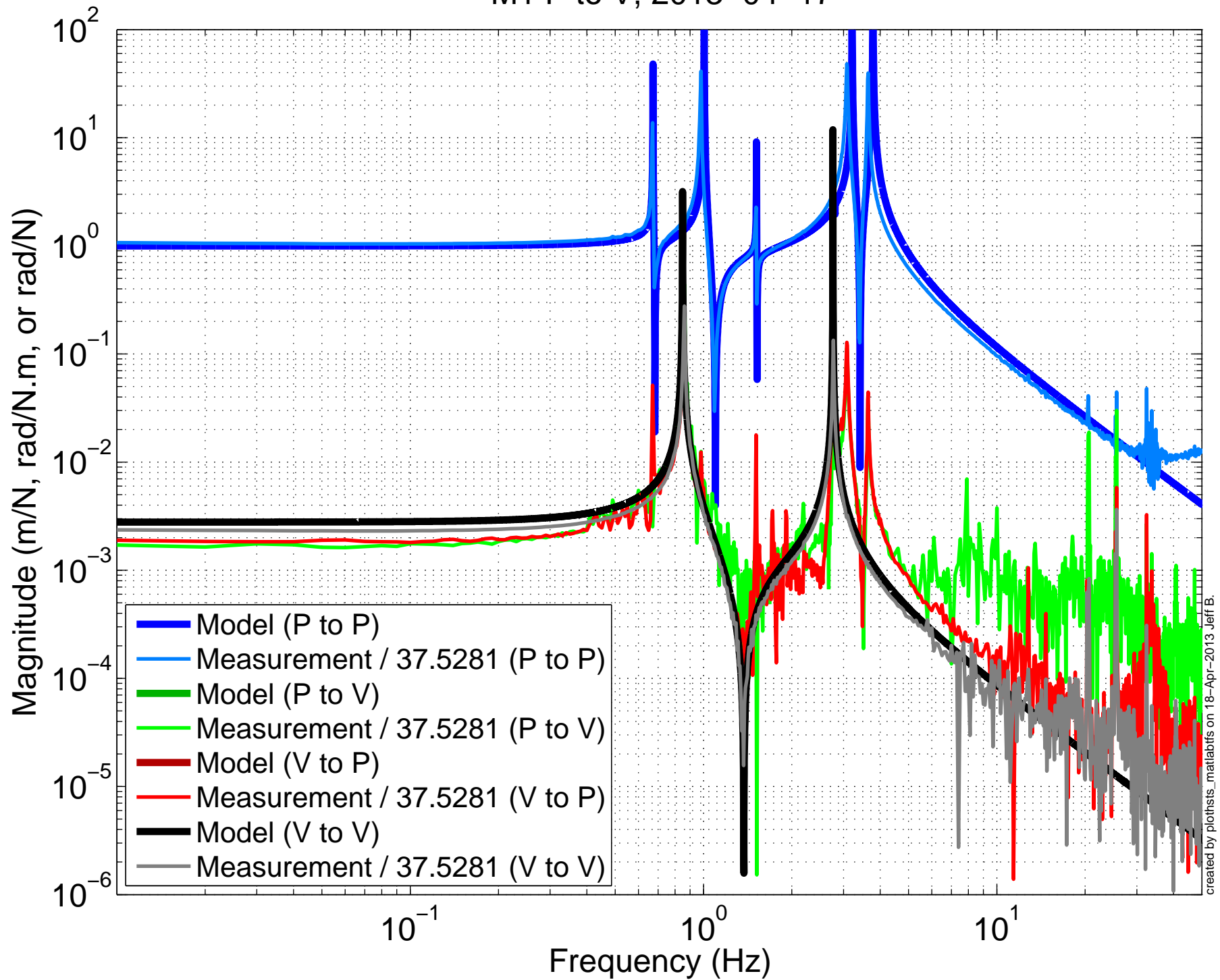


# X1SUSPRM

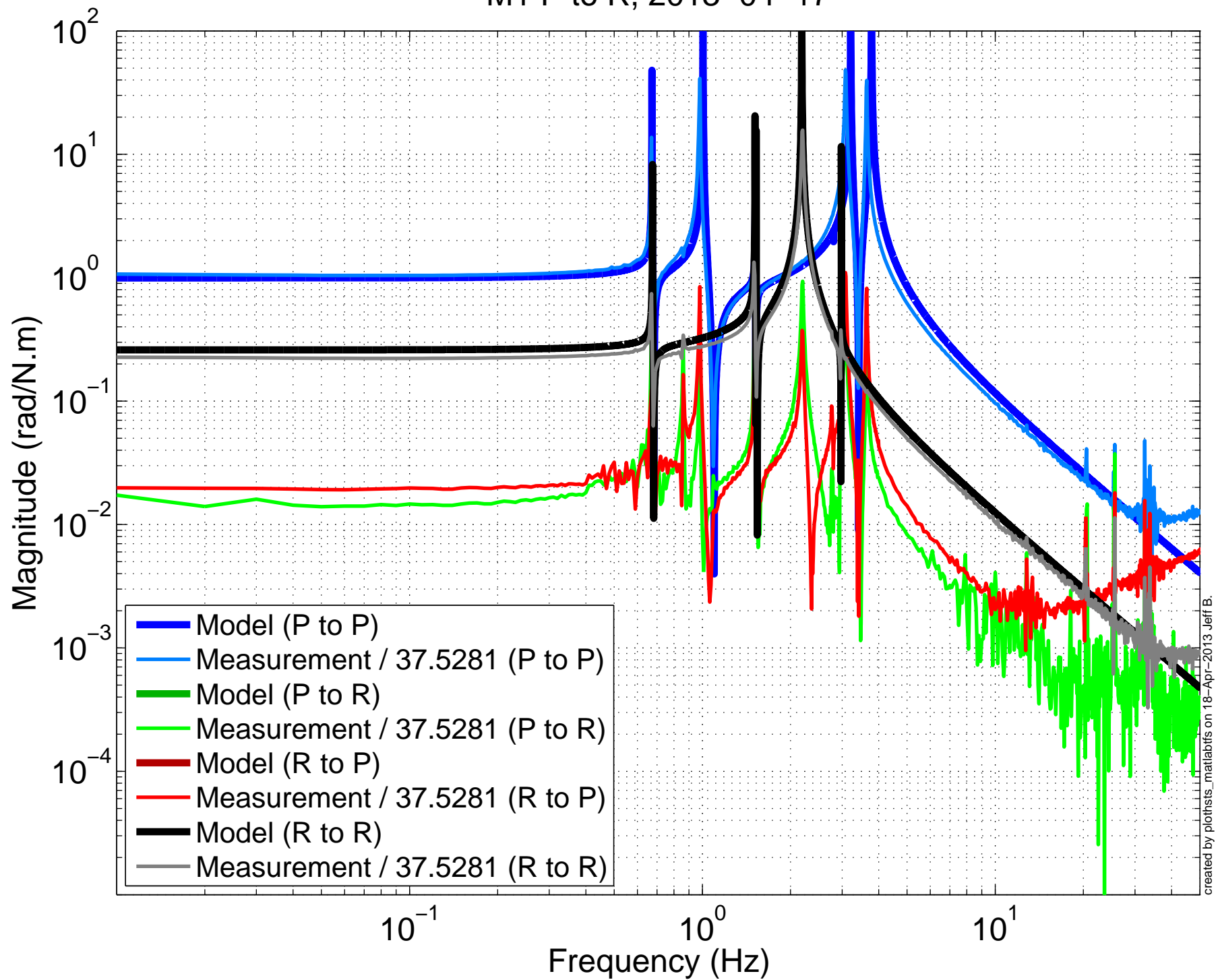
## M1 P to L, 2013-04-17



X1SUSPRM  
M1 P to V, 2013-04-17

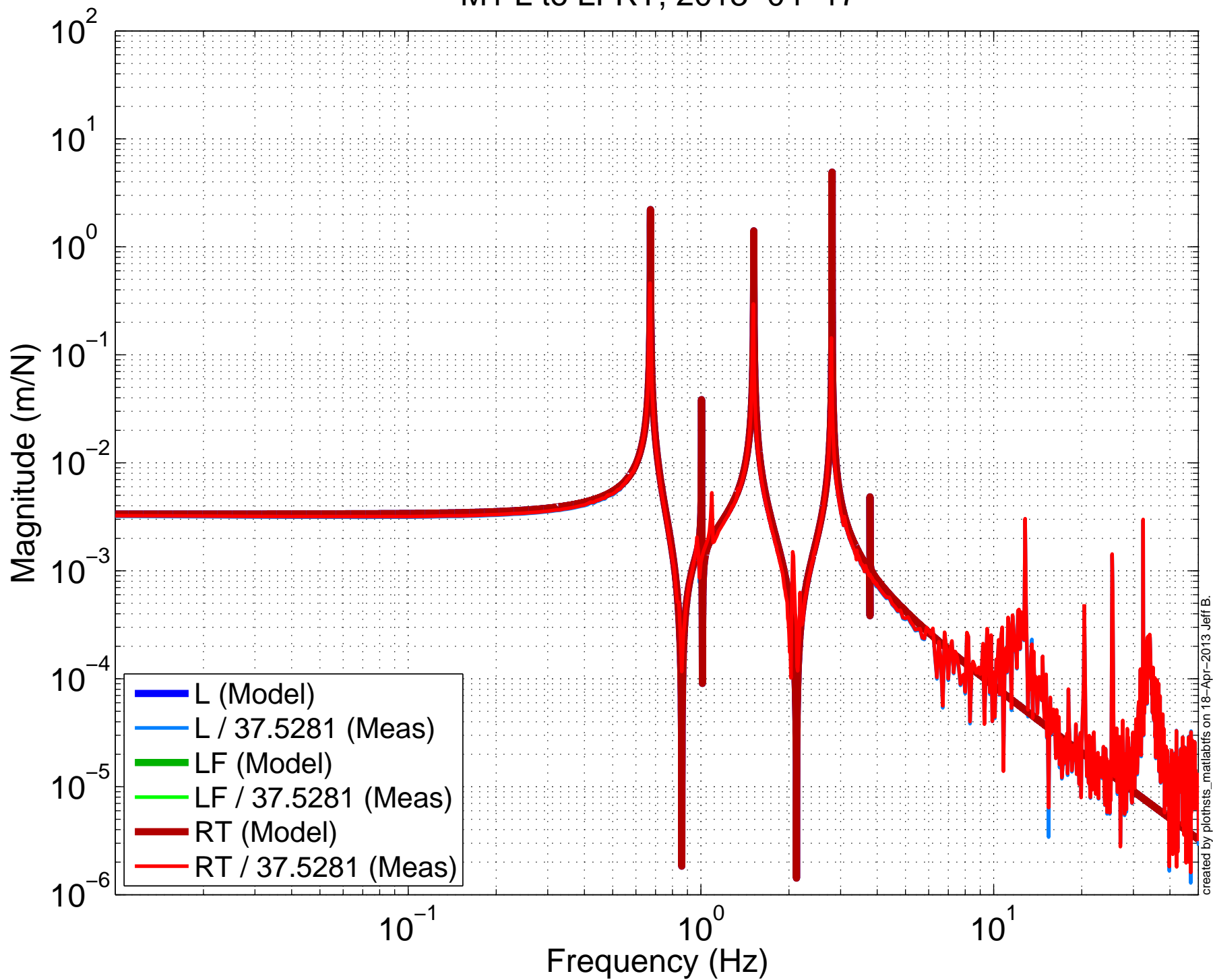


X1SUSPRM  
M1 P to R, 2013-04-17



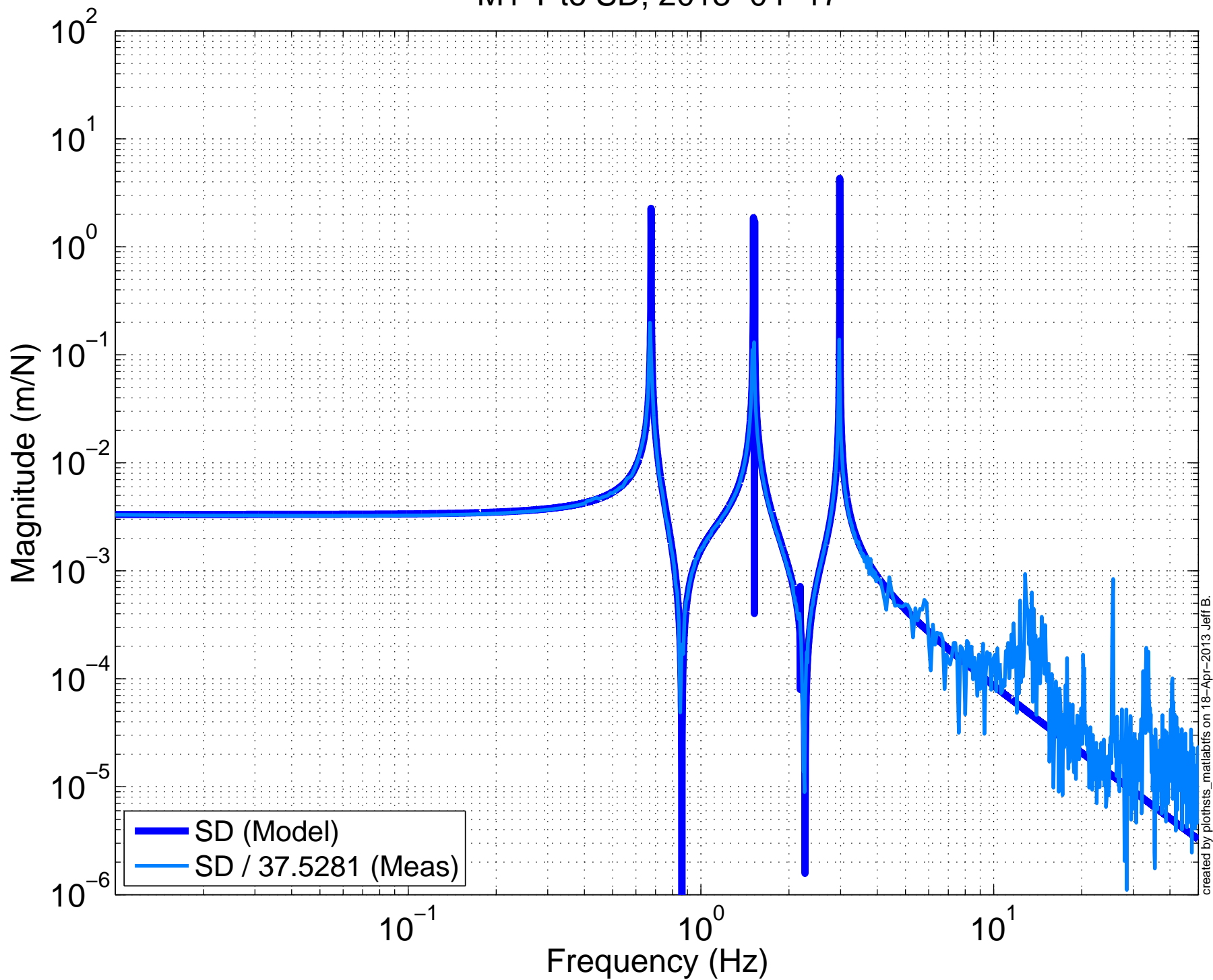
# X1SUSPRM

## M1 L to LFRT, 2013-04-17



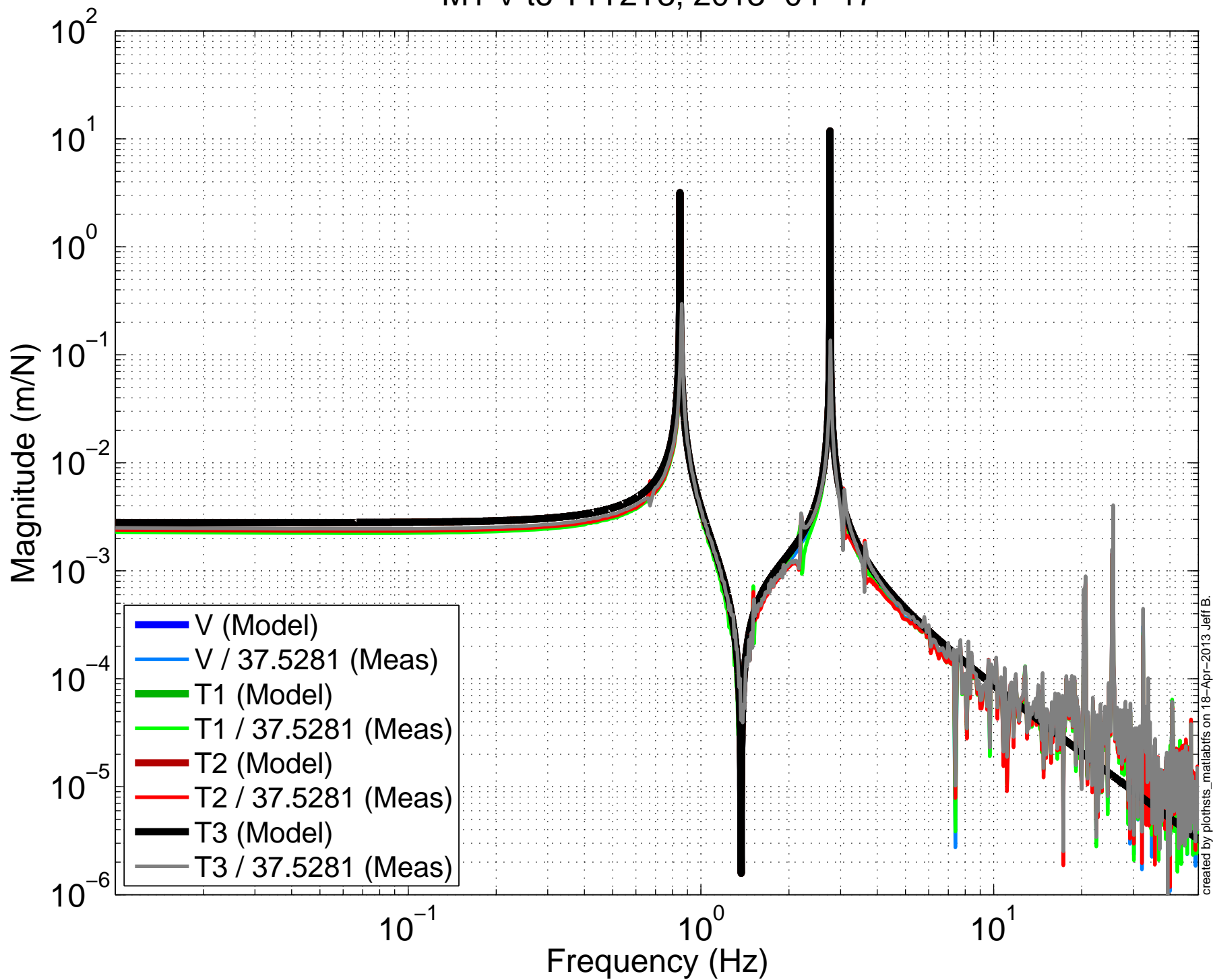
# X1SUSPRM

## M1 T to SD, 2013-04-17



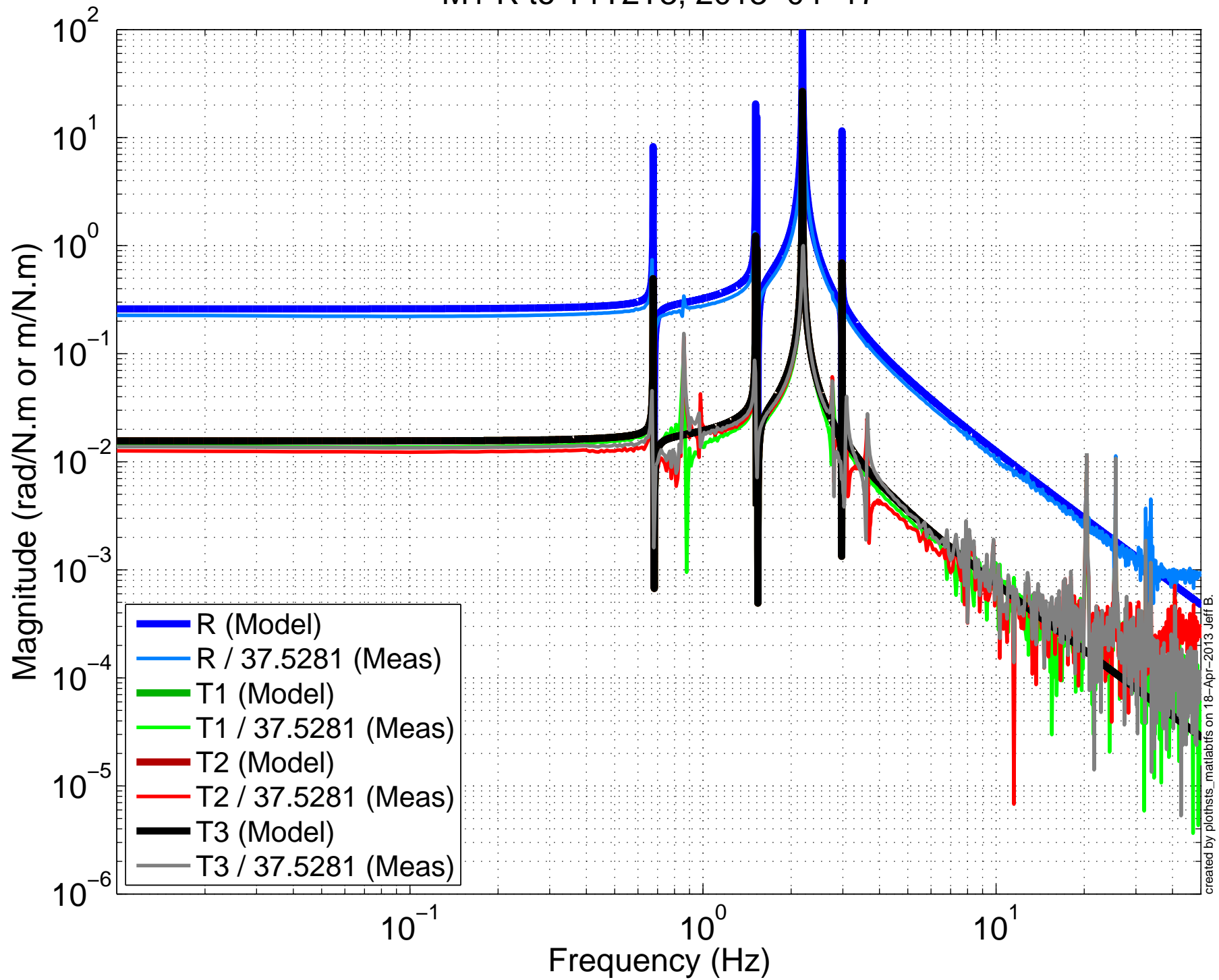
# X1SUSPRM

## M1 V to T1T2T3, 2013-04-17



# X1SUSPRM

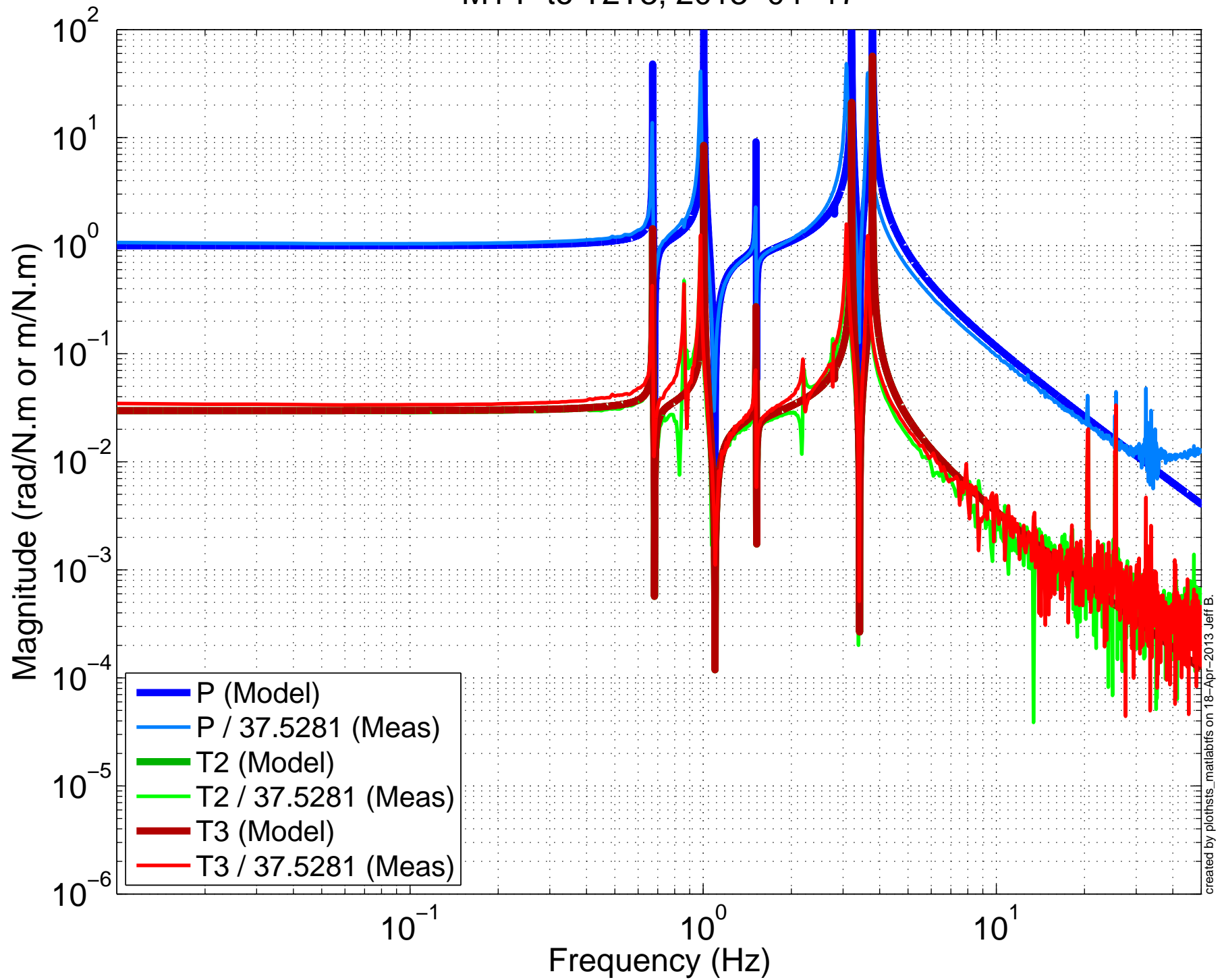
## M1 R to T1T2T3, 2013-04-17





# X1SUSPRM

## M1 P to T2T3, 2013-04-17



# X1SUSPRM

## M1 Y to LFRT, 2013-04-17

