

DB9 Breakout Board connection to Test A Input from the back of the OMC Whitening Chassis

DC On/Off
5V
-15V
Chassis power (switch shown OFF. It should be ON during measurement, of course)

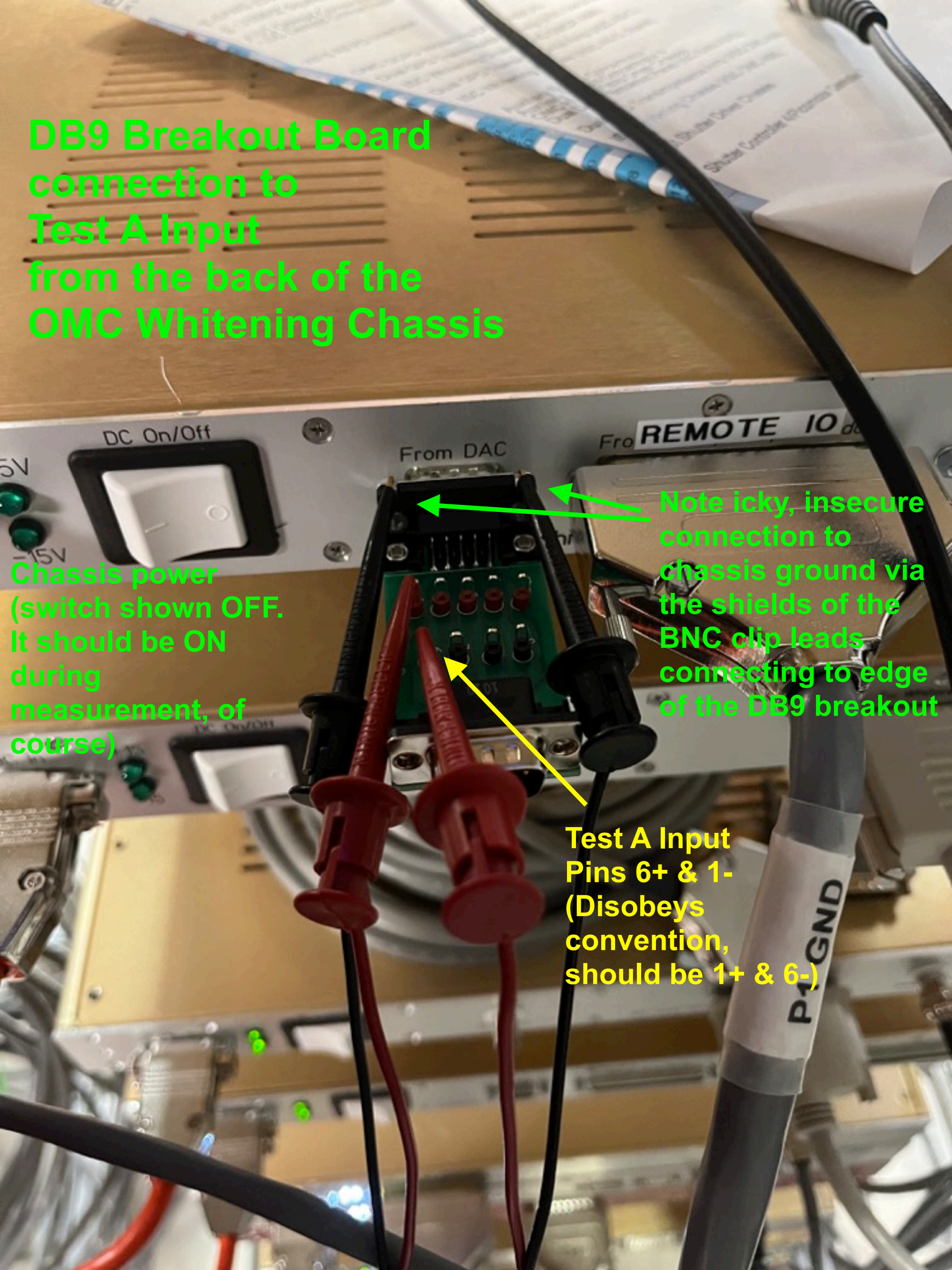
From DAC

Fro REMOTE IO d

Note icky, insecure connection to chassis ground via the shields of the BNC clip leads connecting to edge of the DB9 breakout

Test A Input Pins 6+ & 1- (Disobeys convention, should be 1+ & 6-)

P1 GND



Pin 13
OMC
Chassis
0V

Test Path
to DCPD B
"Cal Rly 2"
is OFF

Whitening
gain switch is
(irrelevant,
but) OFF

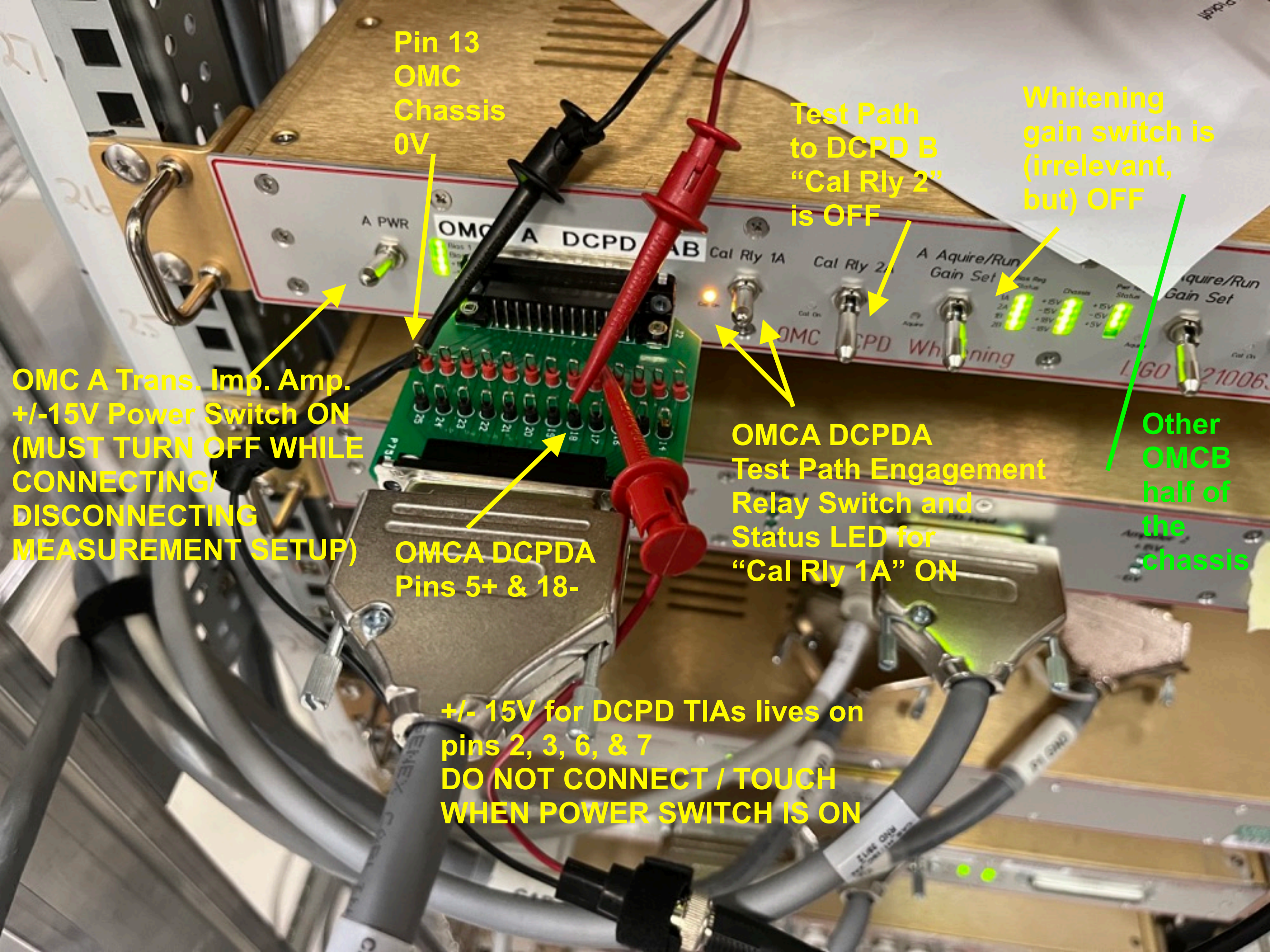
OMC A Trans. Imp. Amp.
+/-15V Power Switch ON
(MUST TURN OFF WHILE
CONNECTING/
DISCONNECTING
MEASUREMENT SETUP)

OMCA DCPDA
Pins 5+ & 18-

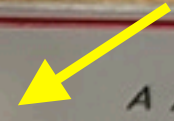
OMCA DCPDA
Test Path Engagement
Relay Switch and
Status LED for
"Cal Rly 1A" ON

Other
OMCB
half of
the
chassis

+/- 15V for DCPD TIAs lives on
pins 2, 3, 6, & 7
DO NOT CONNECT / TOUCH
WHEN POWER SWITCH IS ON



OMCA DCPDB
Test Path Engagement
Relay Switch and
Status LED for
"Cal Rly 2A" ON



A PWR OMC DCPD AB Cal Rly 2A A Aquire/Run Gain Set B Aquire/Run Gain Set
Cal On Cal On Aquire Aquire
OMC DCPD Whipling LIGO

OMCA DCPDB
Pins 1+ & 14-

+/- 15V for DCPD TIAs lives on
pins 2, 3, 6, & 7
DO NOT CONNECT / TOUCH
WHEN POWER SWITCH IS ON

