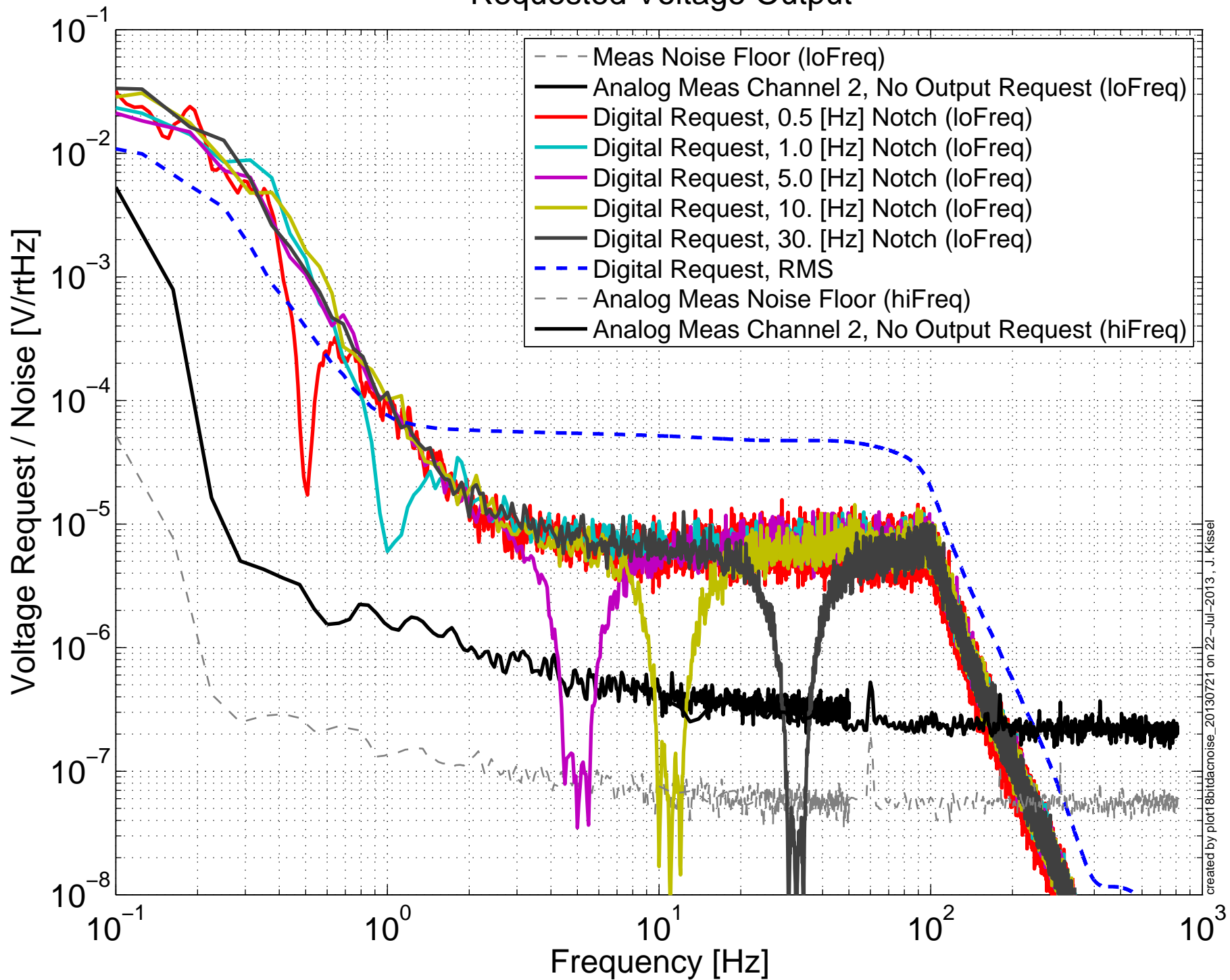


H1 QUAD TST, 2013-07-21

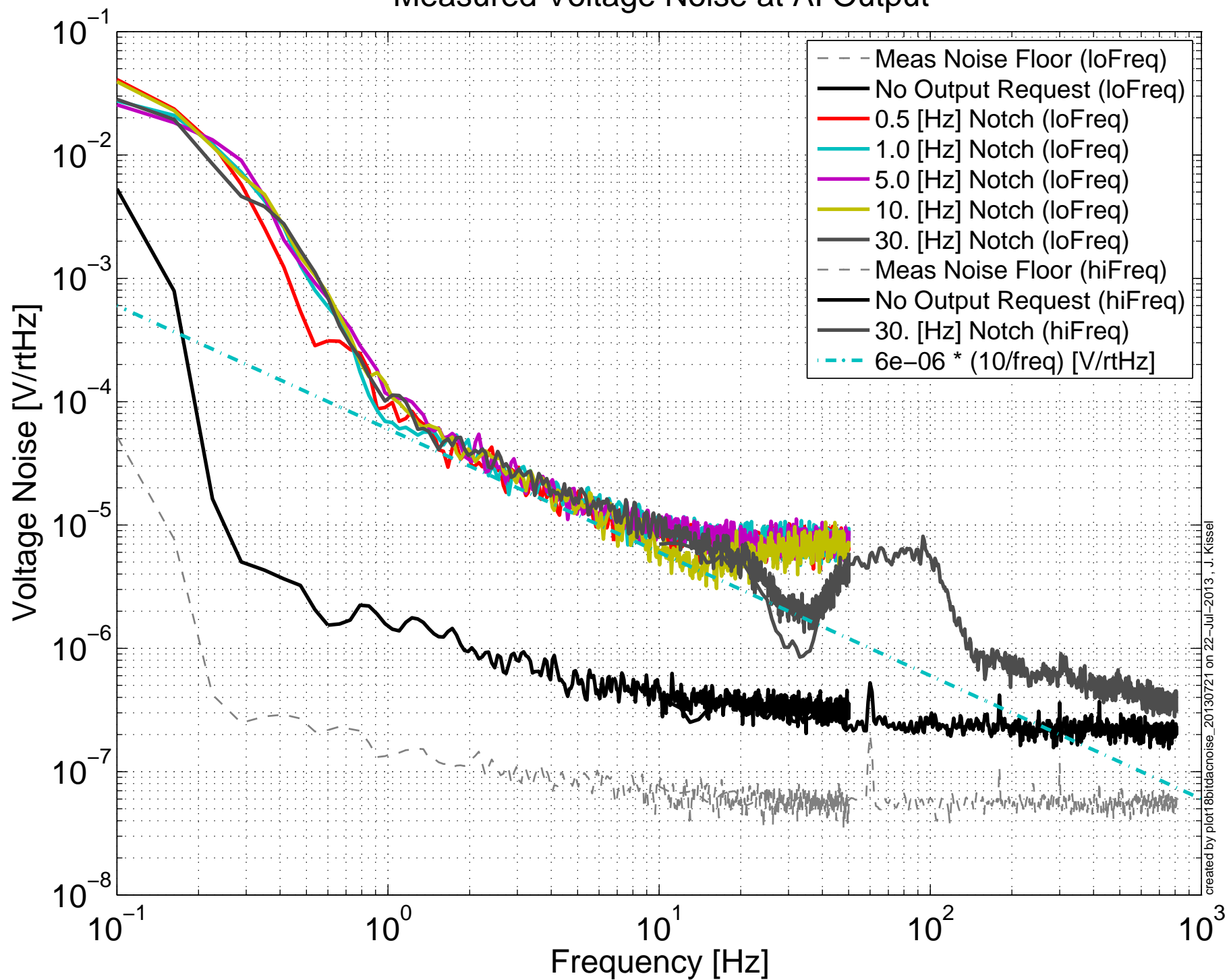
Requested Voltage Output



created by plot8bitdaenise_20130721 on 22-Jul-2013 - J. Kissel

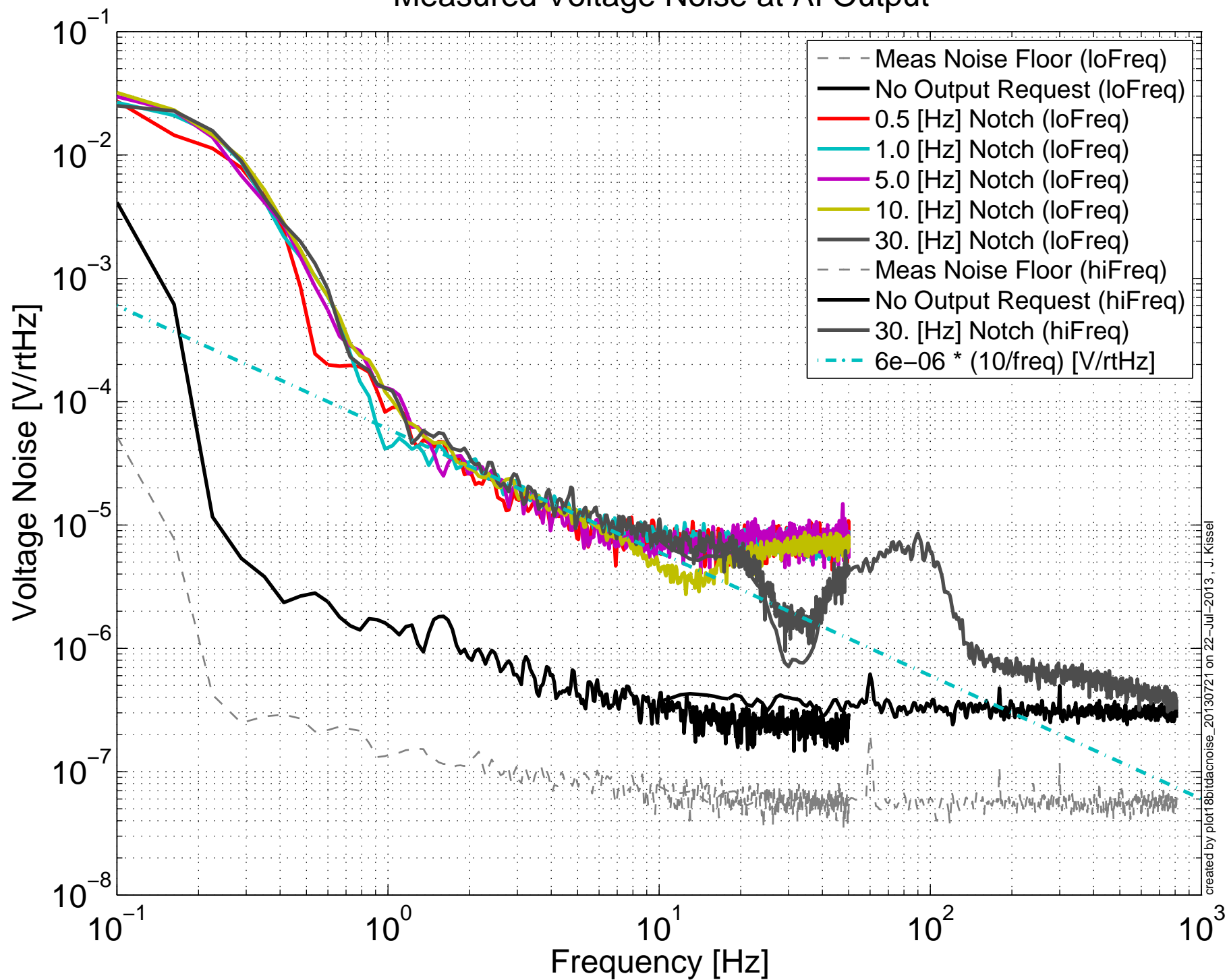
H1 QUAD TST, DAC 1 Channel 5, 2013-07-21

Measured Voltage Noise at AI Output



H1 QUAD TST, DAC 1 Channel 6, 2013-07-21

Measured Voltage Noise at AI Output



H1 QUAD TST, DAC 1 Channel 7, 2013-07-21

Measured Voltage Noise at AI Output

