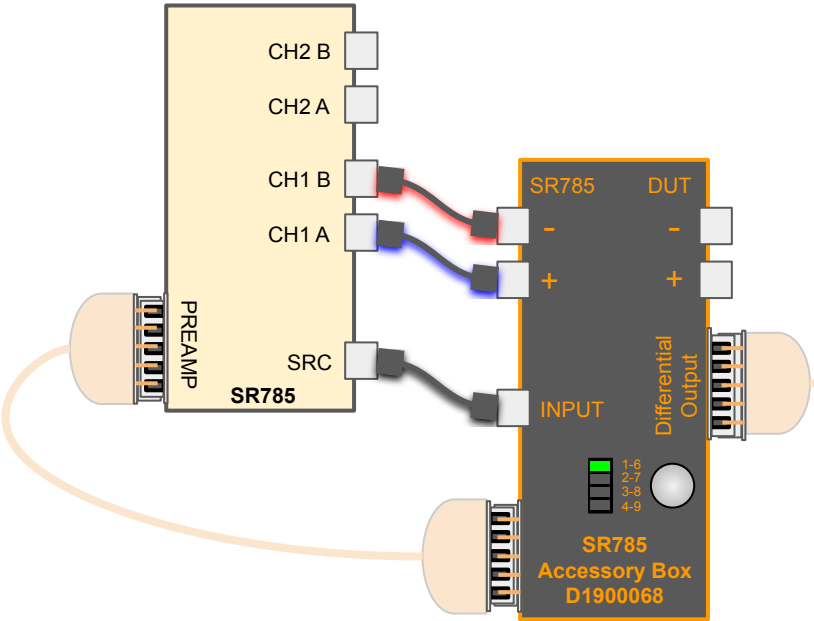


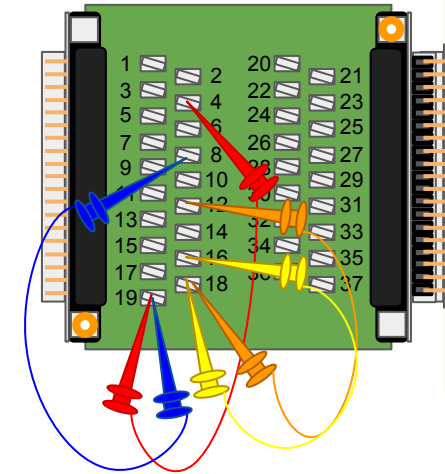
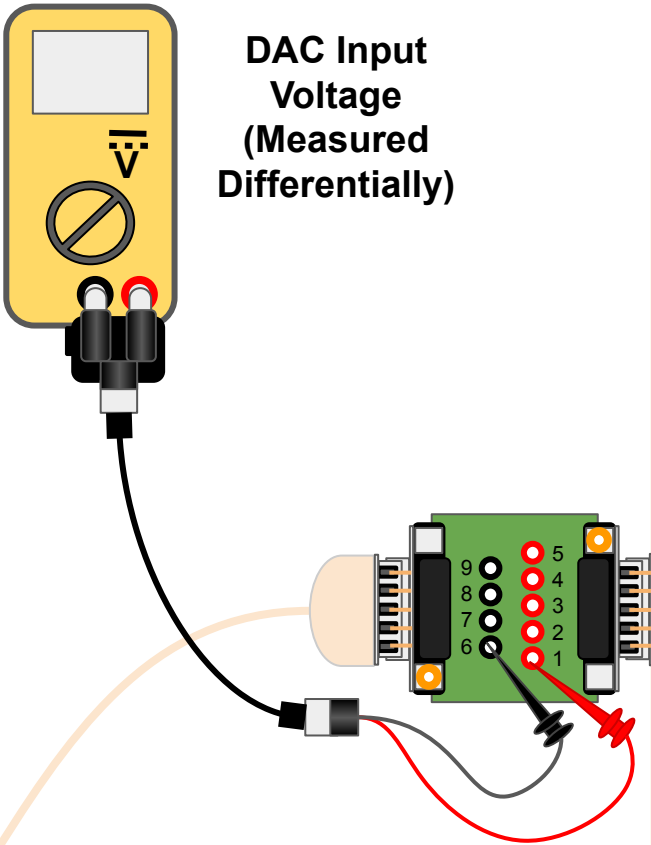
Differential Channel Pins Look-up Table
for Voltage Readback (CH1 Config Shown)

Coil	DAC IN	Coil Out	Slow I Mon	Fast I Mon
CH1	1 & 6	1 & 9	1 (& 14)	2 (& 15)
CH2	2 & 7	3 & 11	4 (& 17)	5 (& 18)
CH3	3 & 8	5 & 13	7 (& 20)	8 (& 21)
CH4	5 & 9	7 & 15	10 (& 23)	11 (& 24)
0V	5	8	Every Other Pin	

SR785 SRC Offset used
to drive DC Voltage

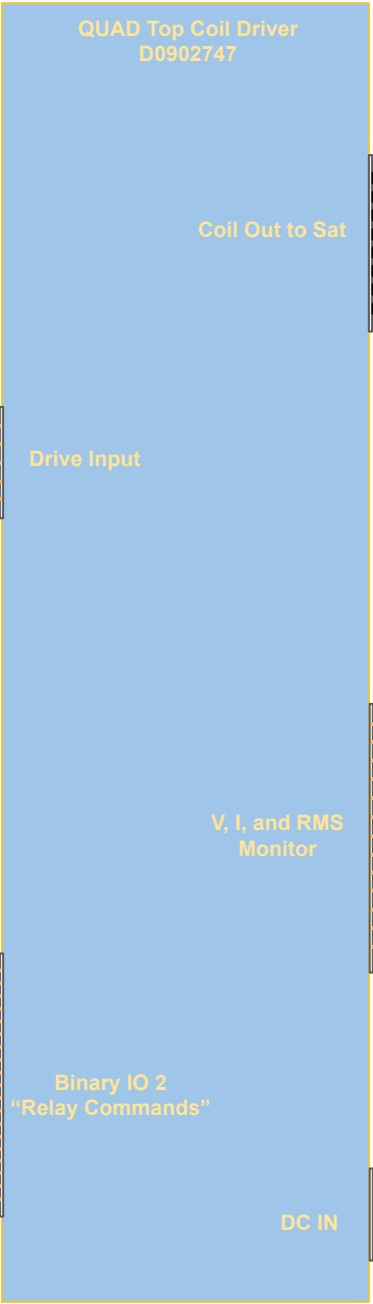


DAC Input
Voltage
(Measured
Differentially)

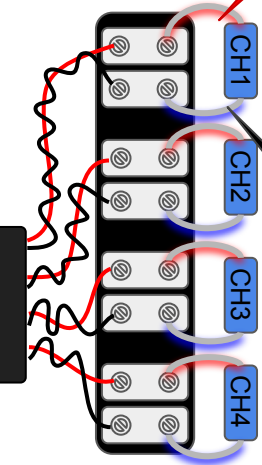


Coil	Coil/Test SW	LP SW
CH1	4	1
CH2	8	5
CH3	12	9
CH4	16	13
0V	Every Other Pin	

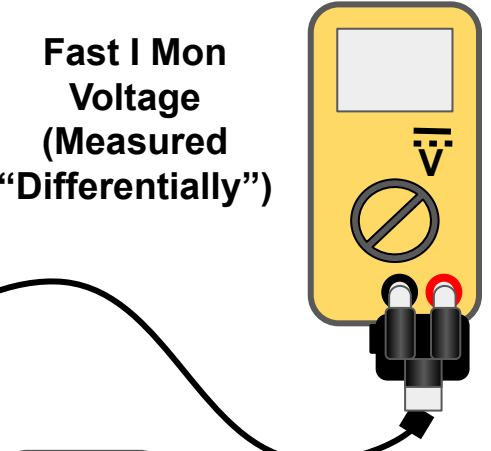
Binary IO Commands Pinout
- Short Coil/Test to 0V to enable Drive Input
- Short LP SW to 0V to turn ON low pass



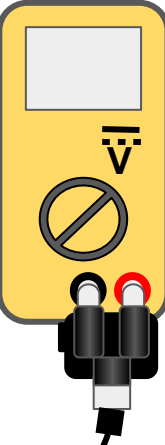
DUMMY
OSEM



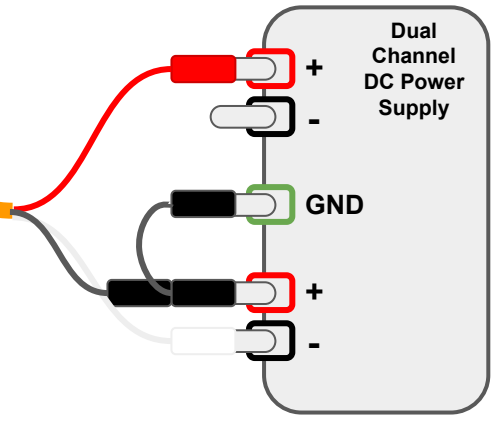
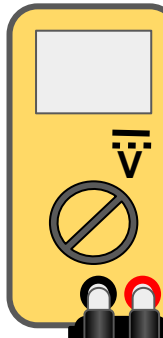
Fast I Mon
Voltage
(Measured
“Differentially”)



Slow RMS I Mon
Voltage
(Measured
“Differentially”)

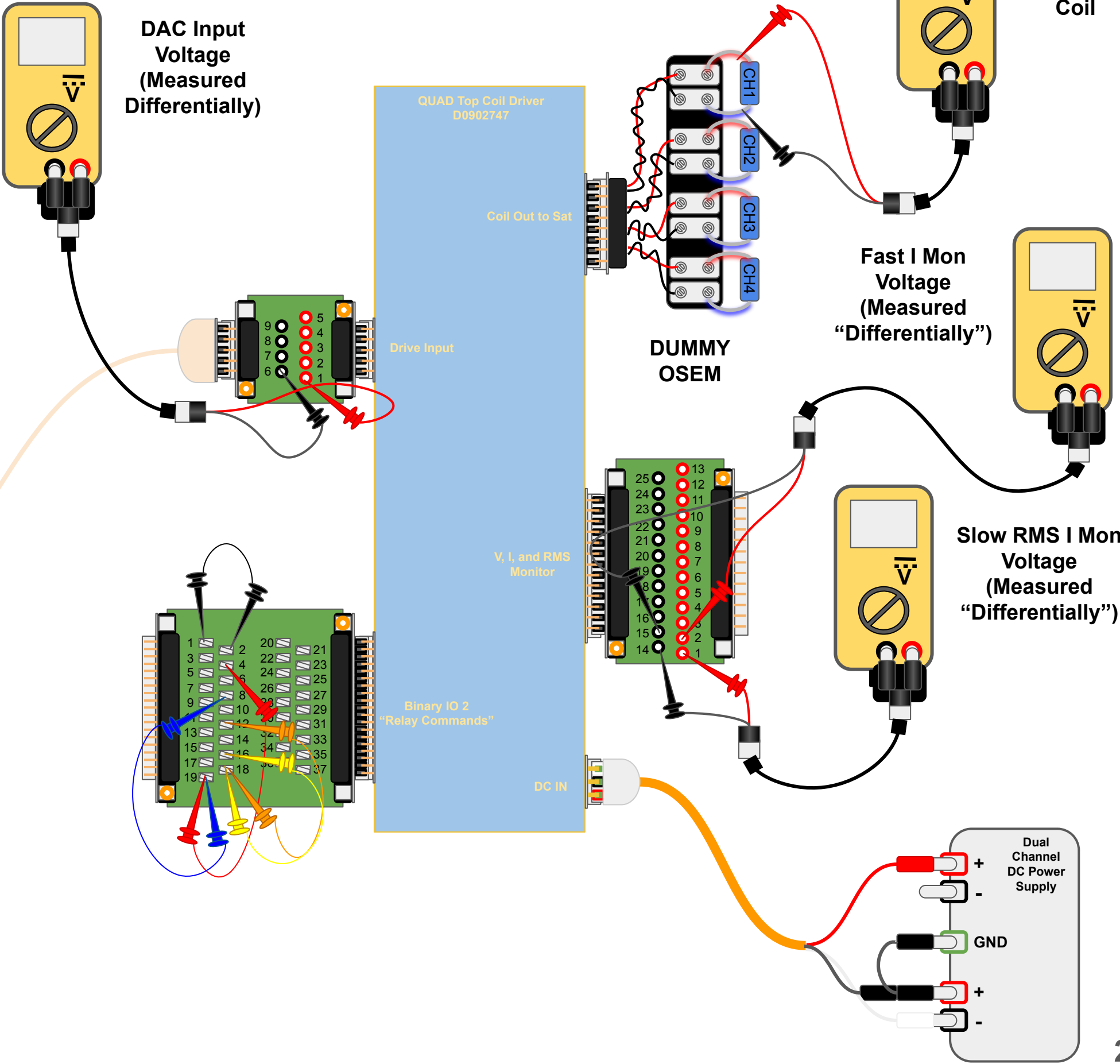
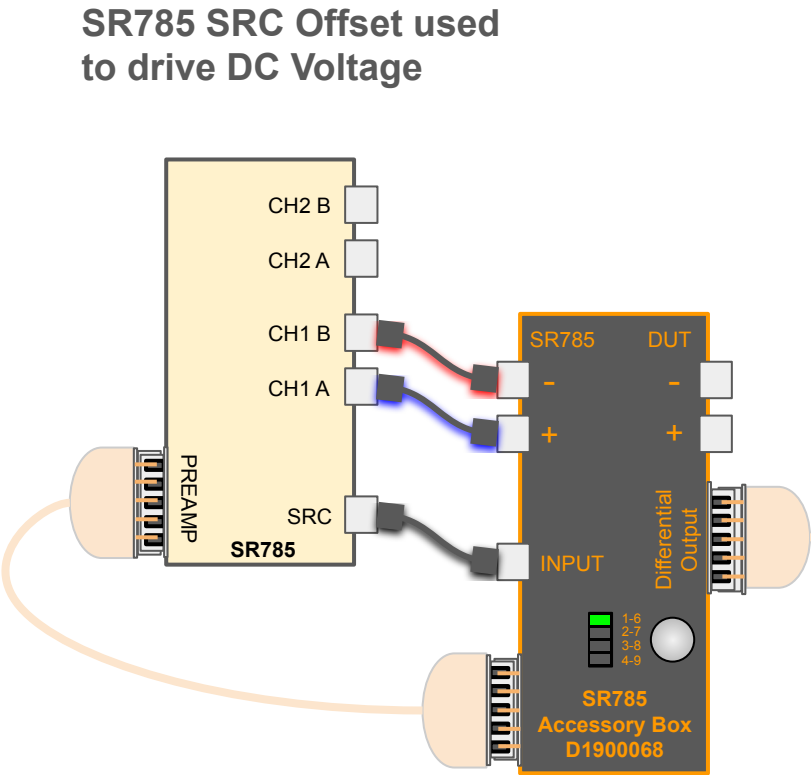


Voltage
across
Dummy
Coil



Differential Channel Pins Look-up Table
for Voltage Readback (CH1 Config Shown)

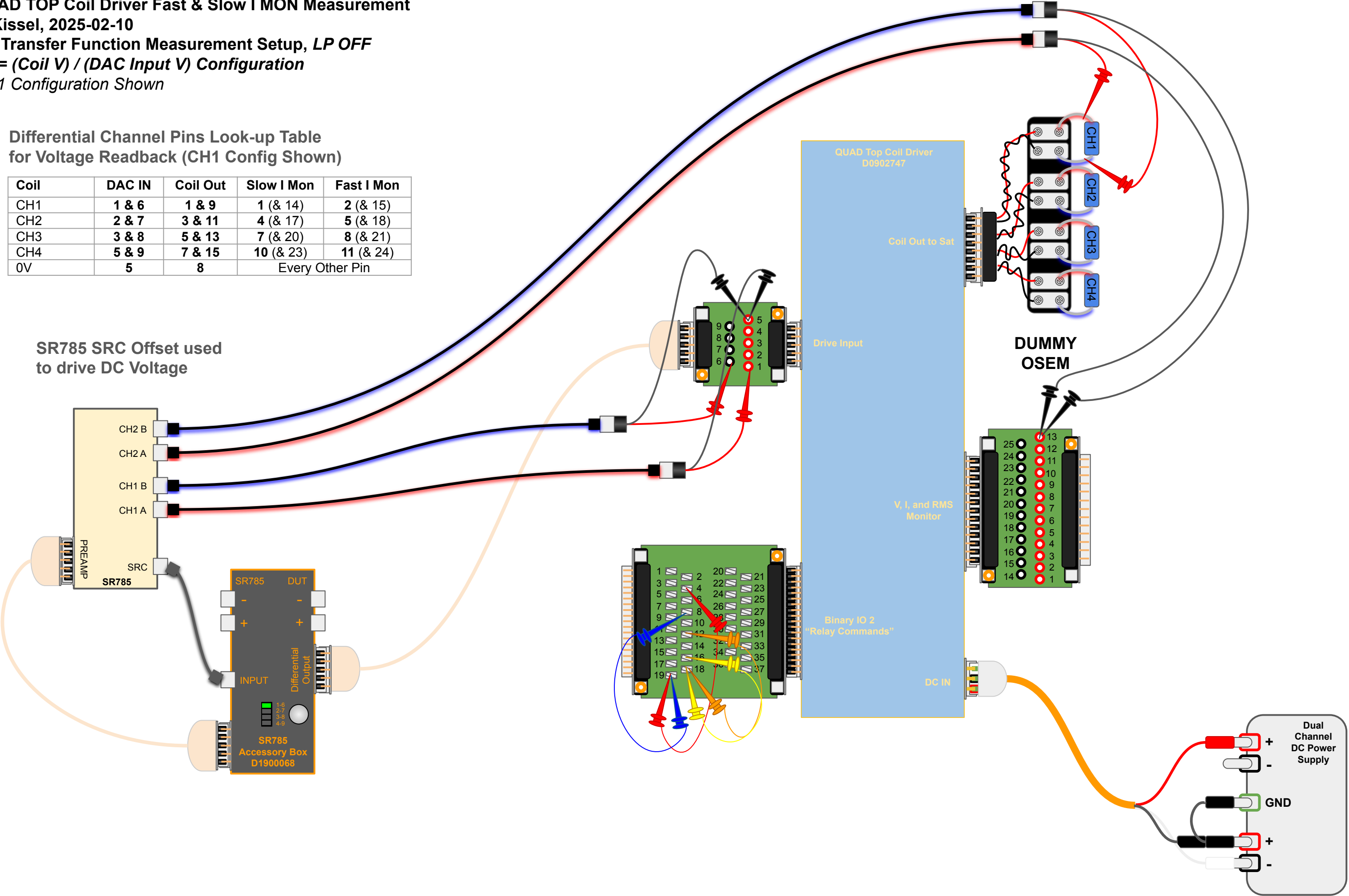
Coil	DAC IN	Coil Out	Slow I Mon	Fast I Mon
CH1	1 & 6	1 & 9	1 (& 14)	2 (& 15)
CH2	2 & 7	3 & 11	4 (& 17)	5 (& 18)
CH3	3 & 8	5 & 13	7 (& 20)	8 (& 21)
CH4	5 & 9	7 & 15	10 (& 23)	11 (& 24)
0V	5	8	Every Other Pin	



QUAD TOP Coil Driver Fast & Slow I MON Measurement
J. Kissel, 2025-02-10
AC Transfer Function Measurement Setup, *LP OFF*
TF = (Coil V) / (DAC Input V) Configuration
CH1 Configuration Shown

Differential Channel Pins Look-up Table
for Voltage Readback (CH1 Config Shown)

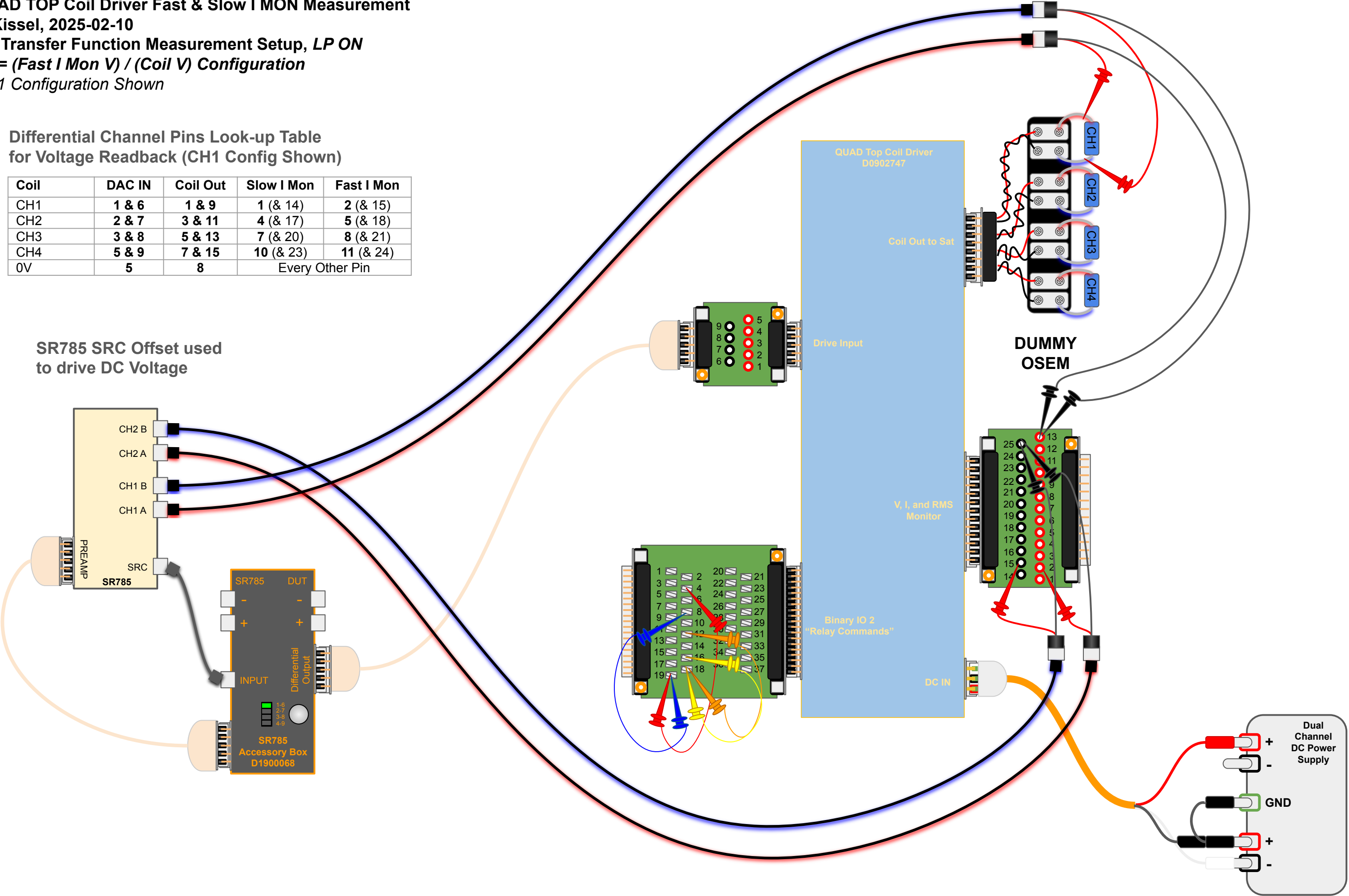
Coil	DAC IN	Coil Out	Slow I Mon	Fast I Mon
CH1	1 & 6	1 & 9	1 (& 14)	2 (& 15)
CH2	2 & 7	3 & 11	4 (& 17)	5 (& 18)
CH3	3 & 8	5 & 13	7 (& 20)	8 (& 21)
CH4	5 & 9	7 & 15	10 (& 23)	11 (& 24)
0V	5	8	Every Other Pin	



QUAD TOP Coil Driver Fast & Slow I MON Measurement
J. Kissel, 2025-02-10
AC Transfer Function Measurement Setup, *LP ON*
TF = (Fast I Mon V) / (Coil V) Configuration
CH1 Configuration Shown

Differential Channel Pins Look-up Table
for Voltage Readback (CH1 Config Shown)

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CH4	5 & 9	7 & 15	10 (& 23)	11 (& 24)
0V	5	8	Every Other Pin	



QUAD TOP Coil Driver Fast & Slow I MON Measurement
J. Kissel, 2025-02-10
AC Transfer Function Measurement Setup, *LP ON*
TF = (Fast I Mon V) / (DAC Input V) Configuration
CH1 Configuration Shown

Differential Channel Pins Look-up Table
for Voltage Readback (CH1 Config Shown)

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CH3	3 & 8	5 & 13	7 (& 20)	8 (& 21)
CH4	5 & 9	7 & 15	10 (& 23)	11 (& 24)
0V	5	8	Every Other Pin	

