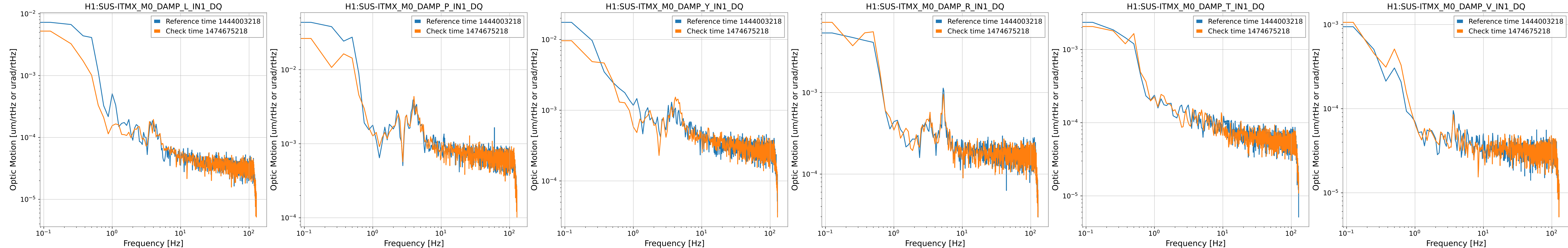
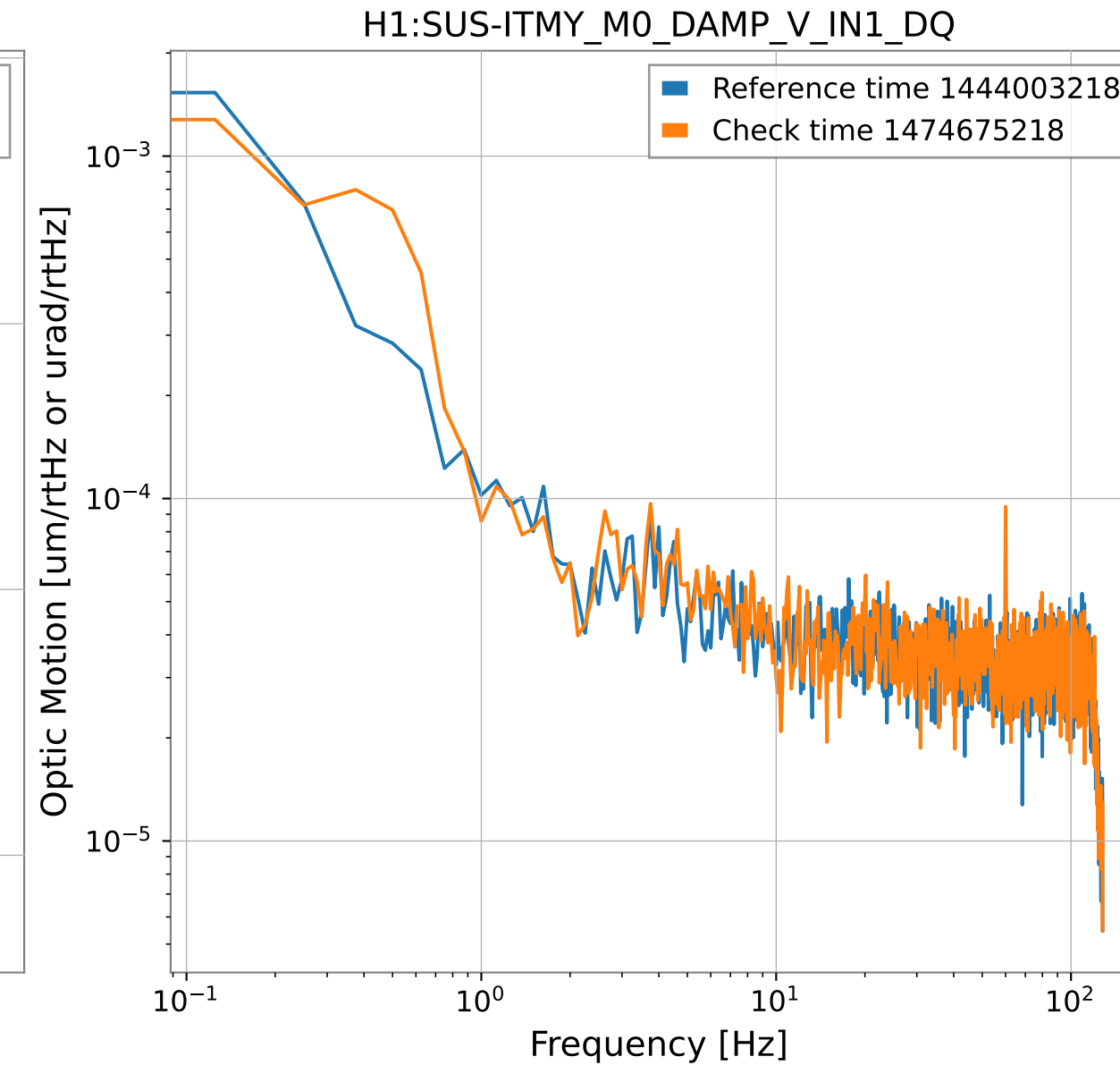
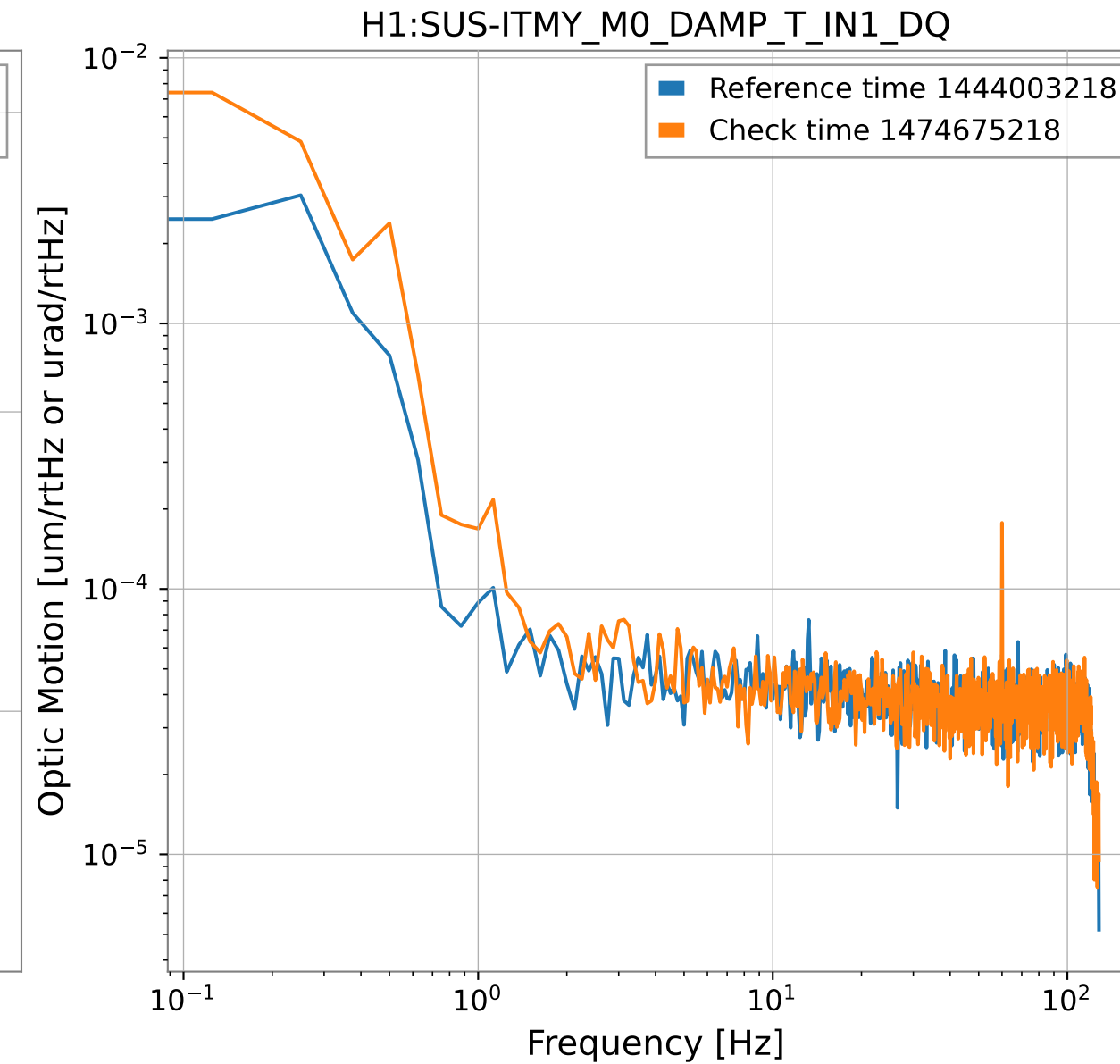
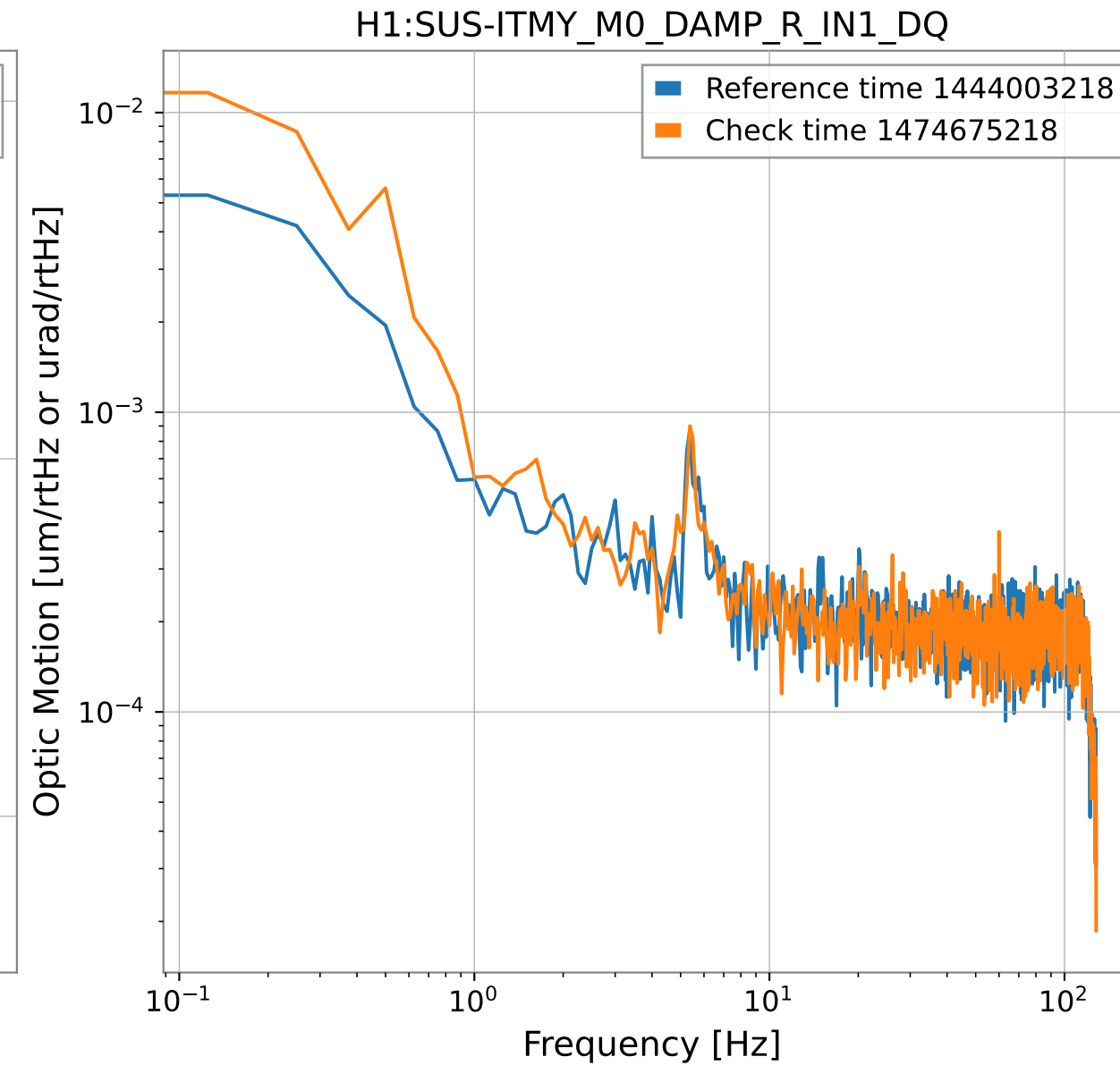
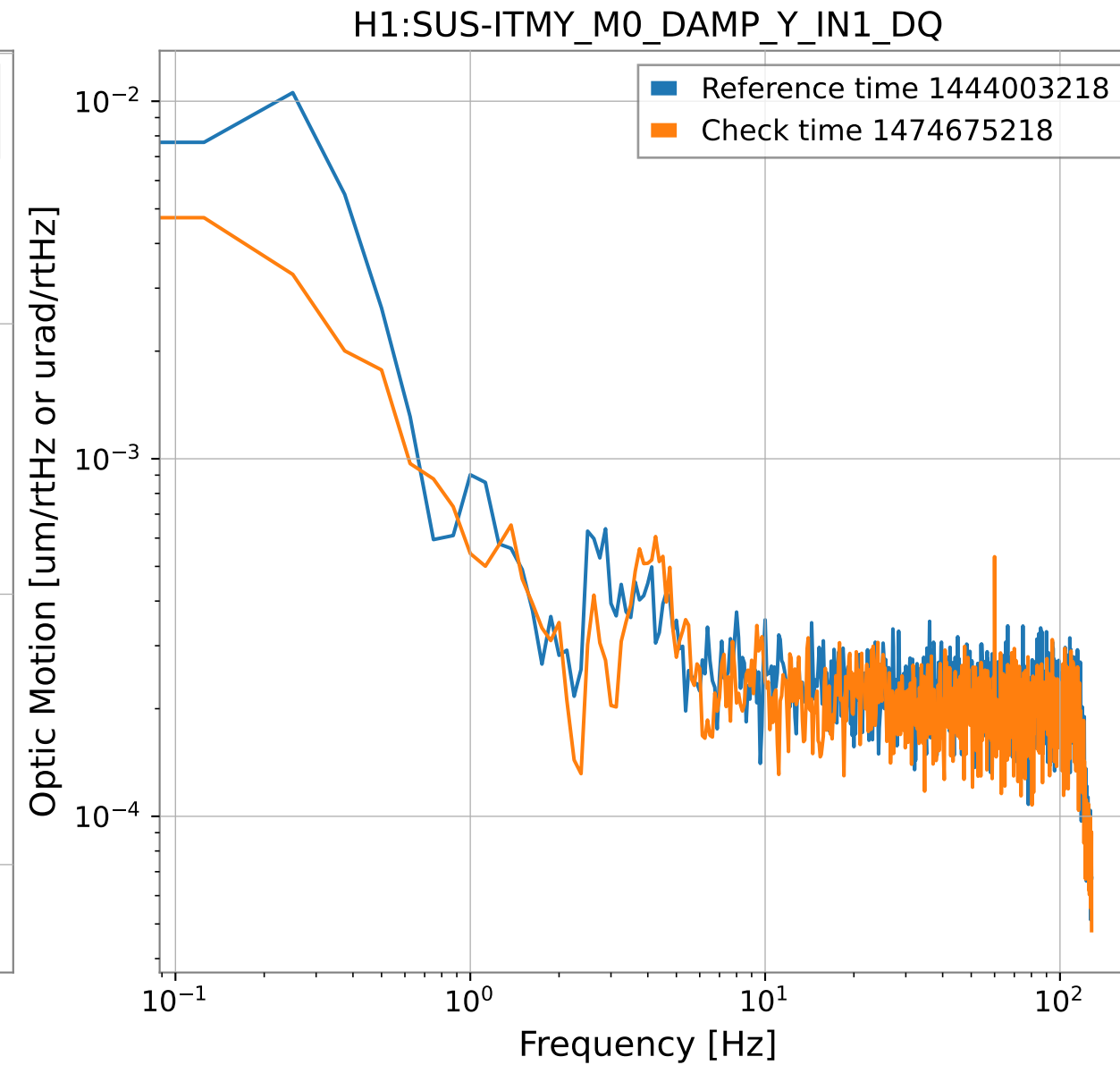
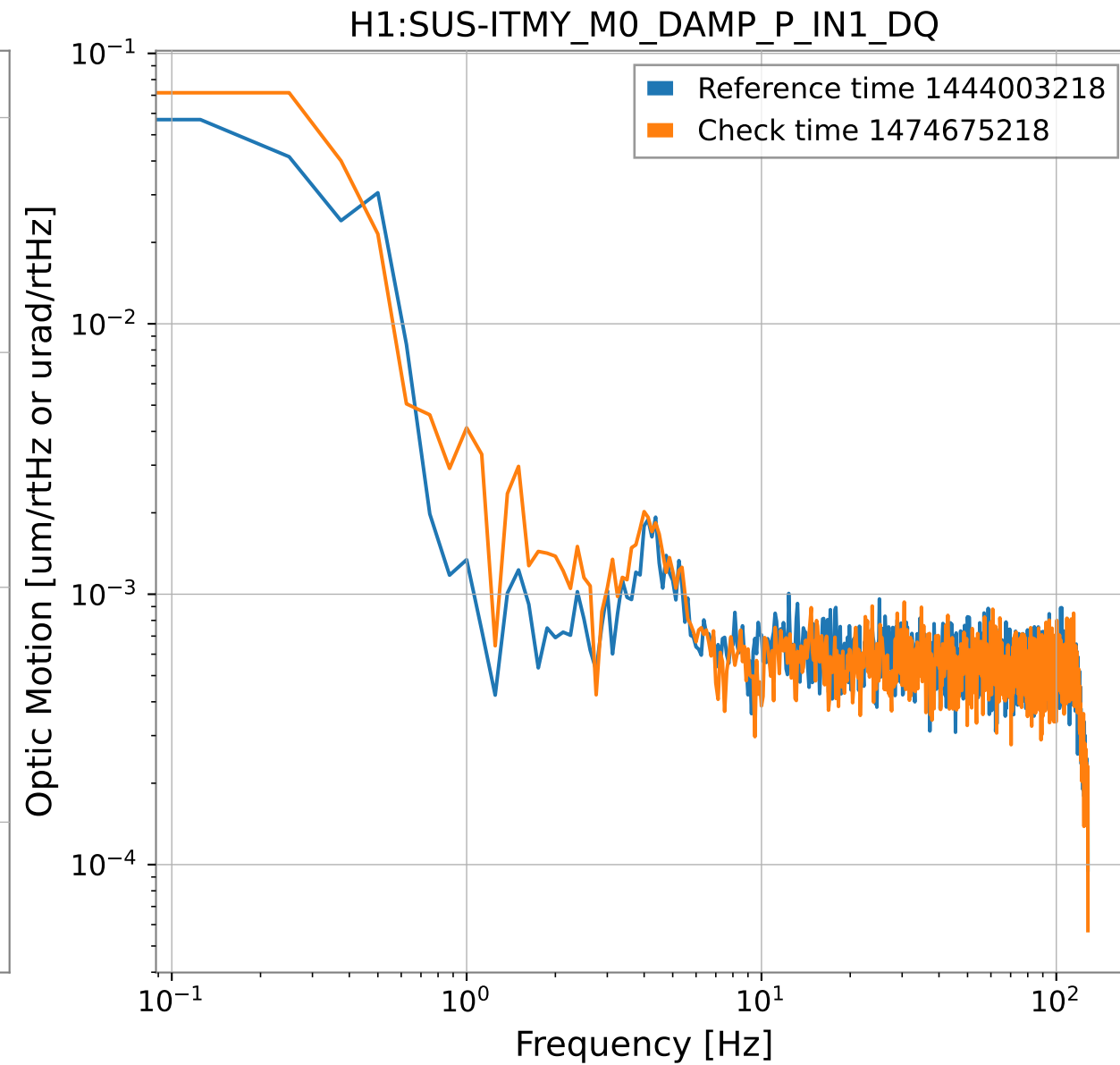
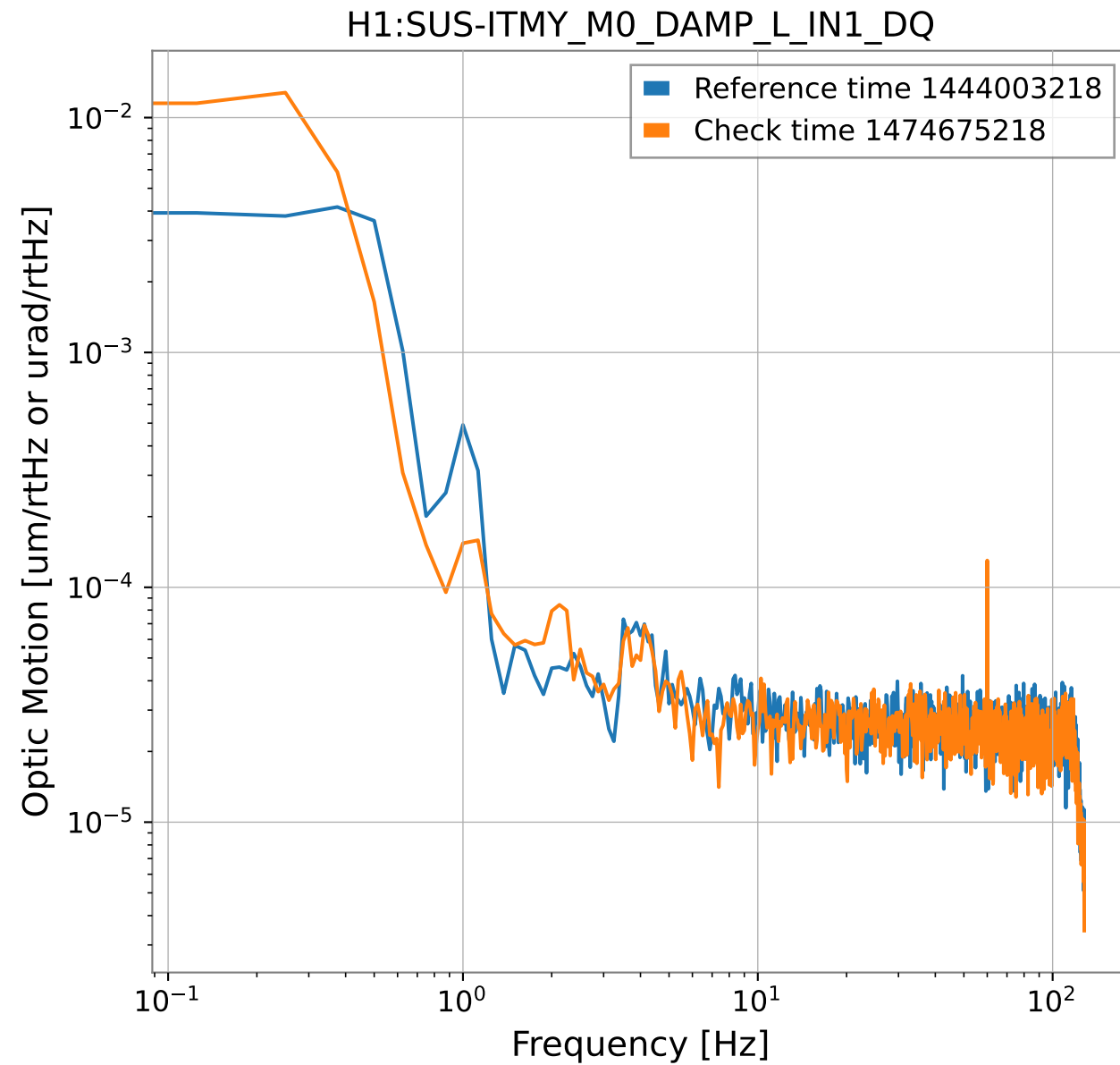


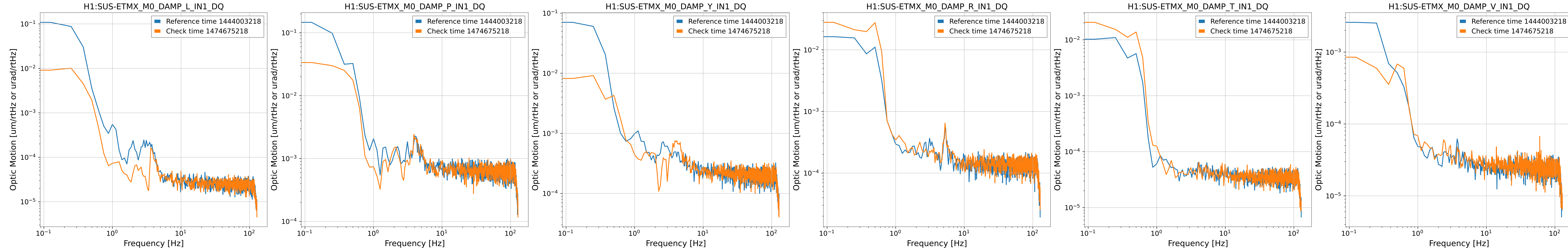
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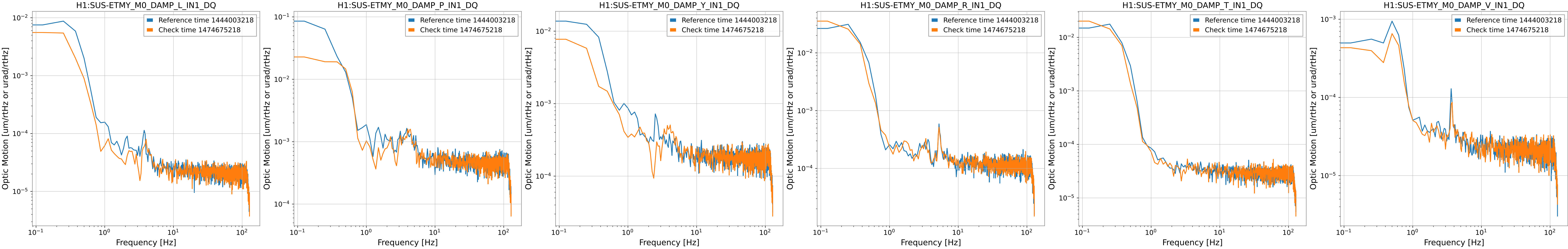
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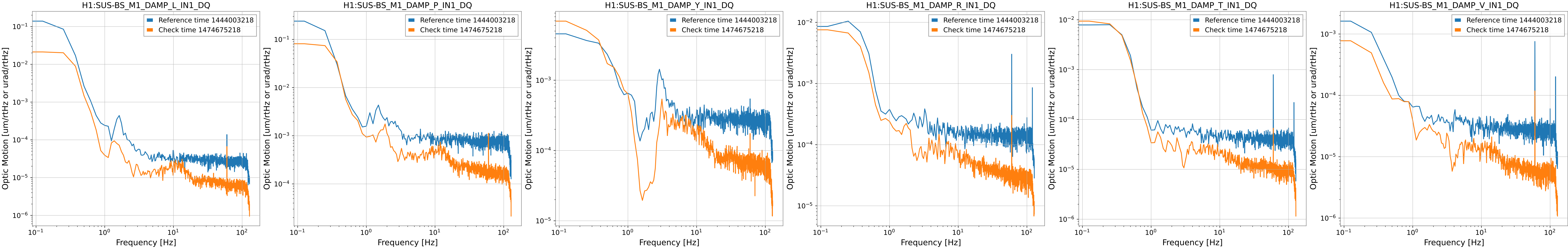
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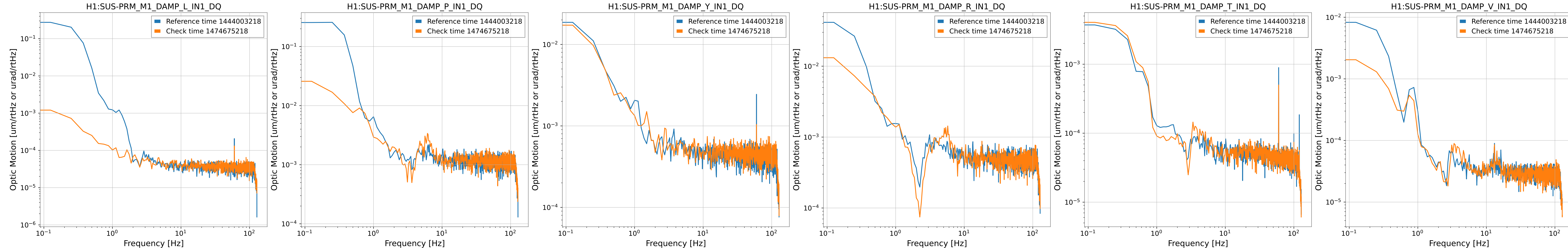
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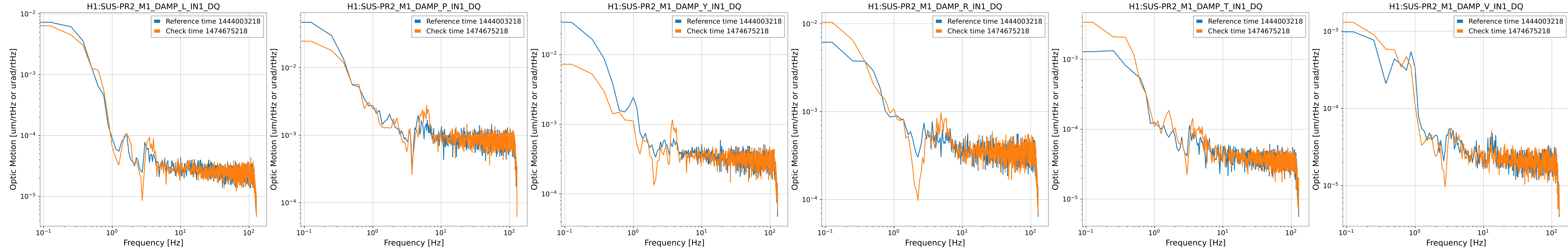
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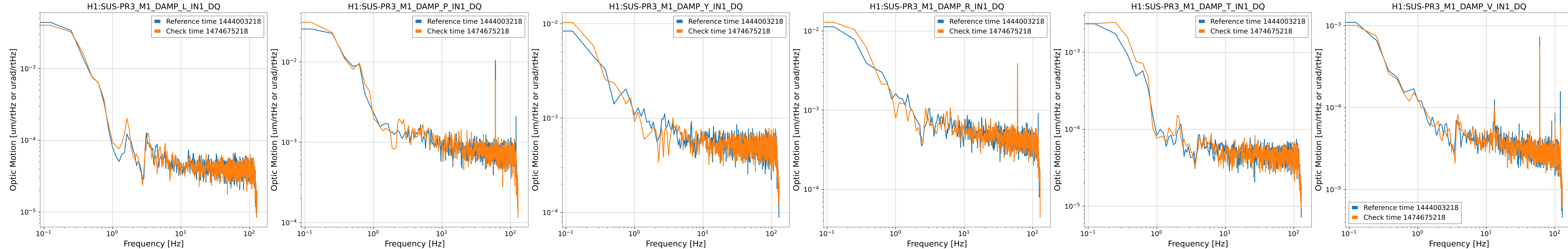
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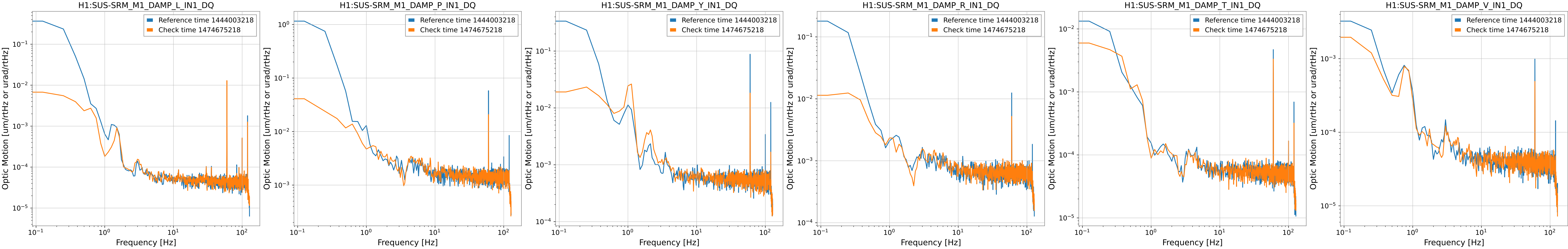
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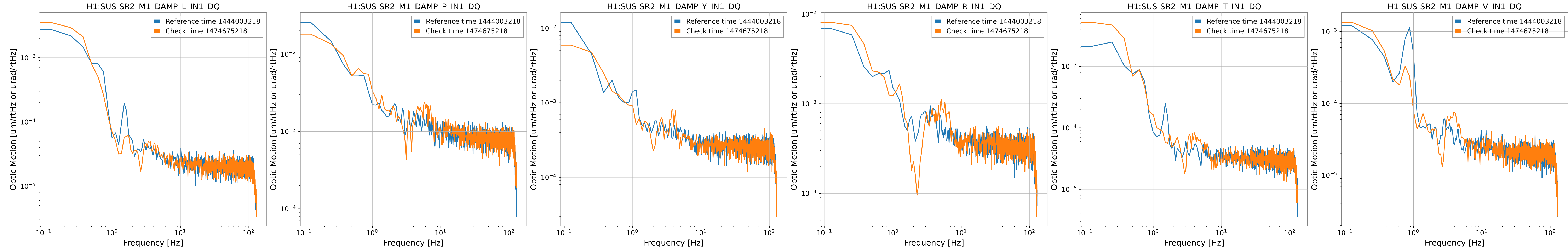
PR3



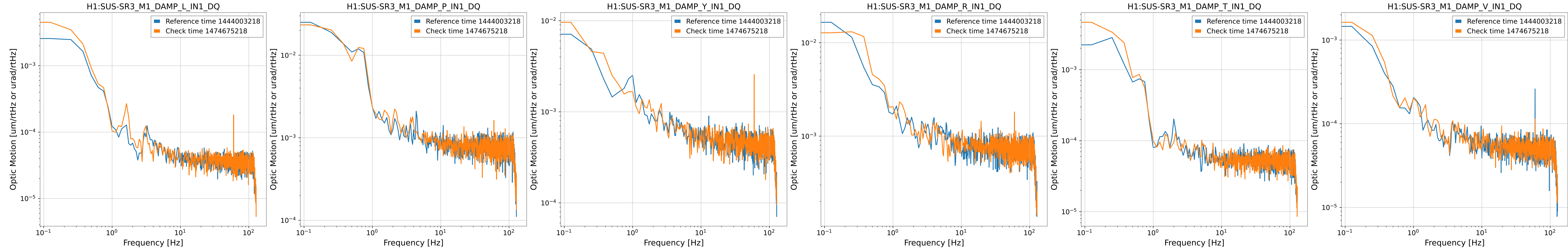
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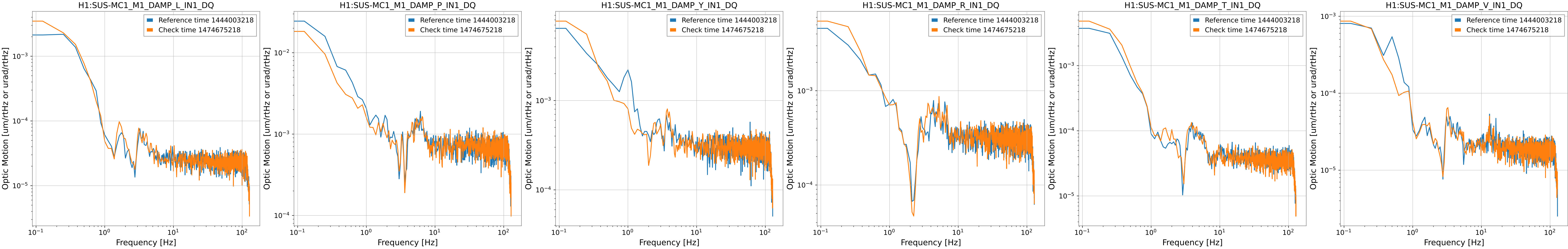
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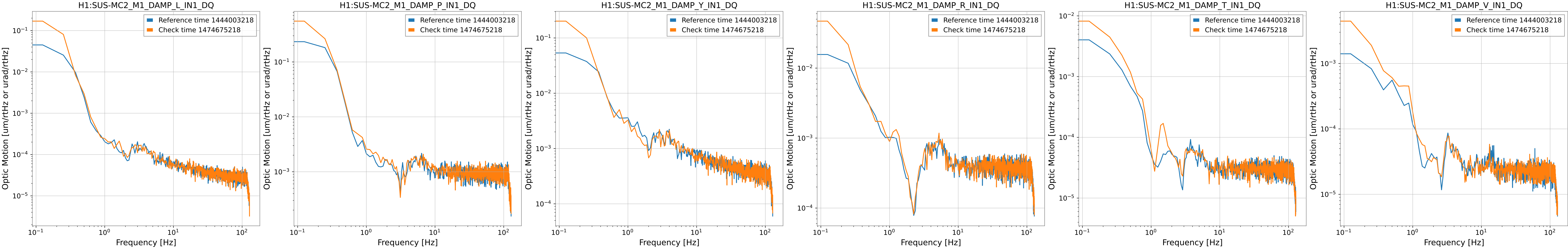
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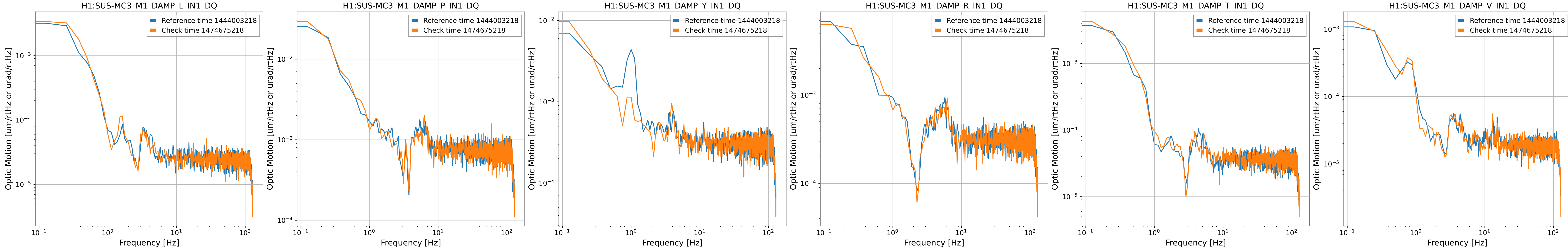
MC1



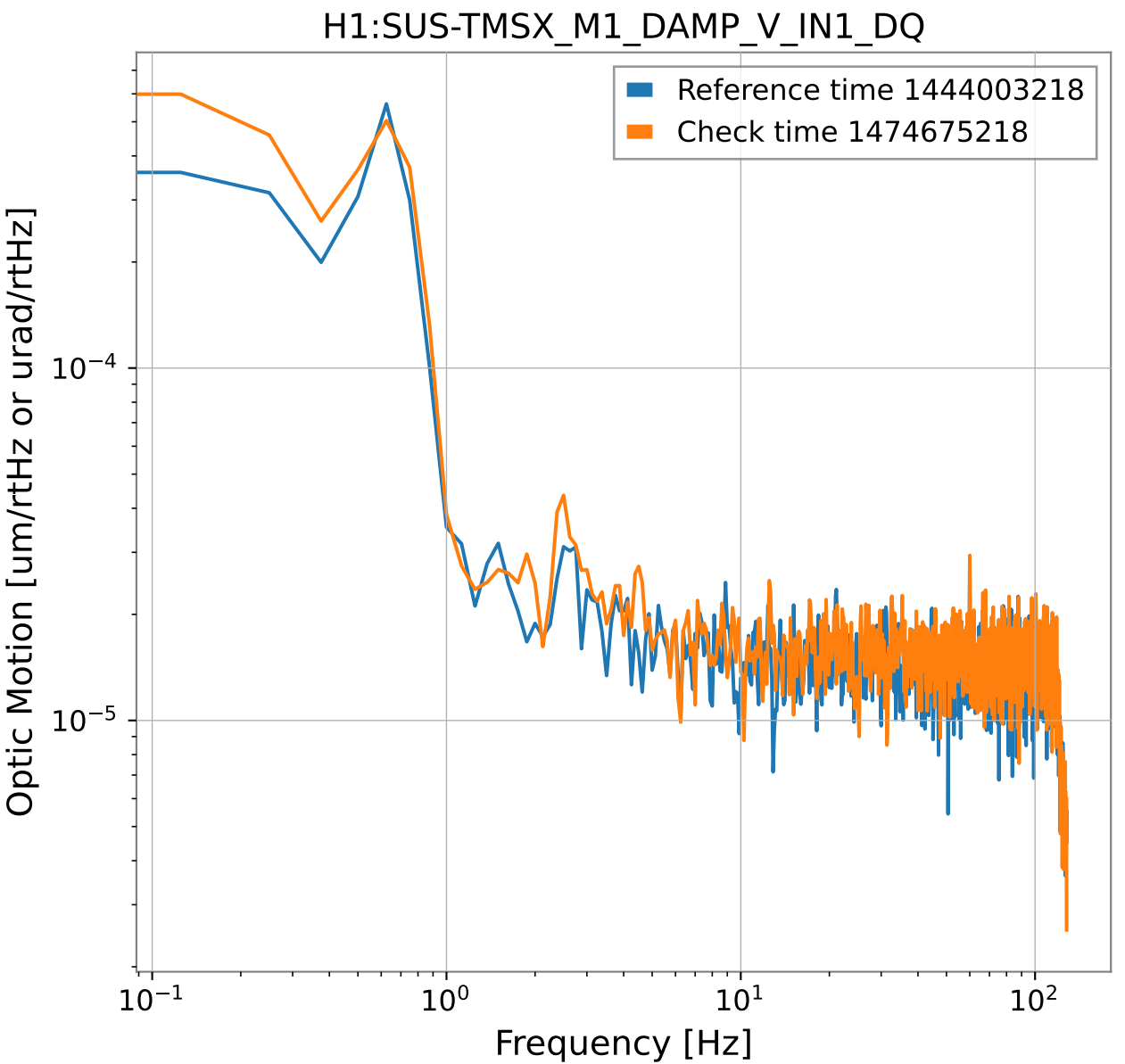
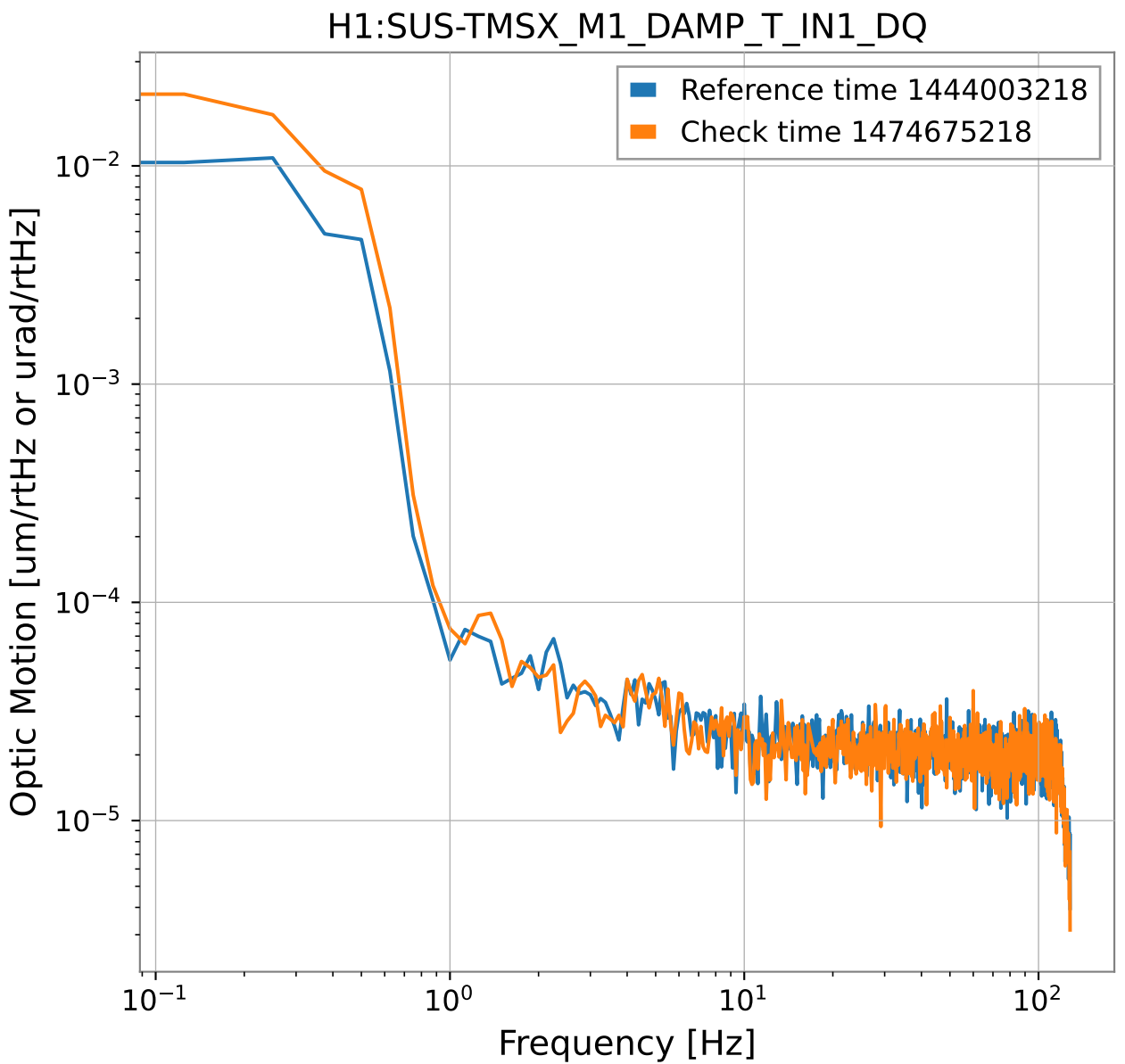
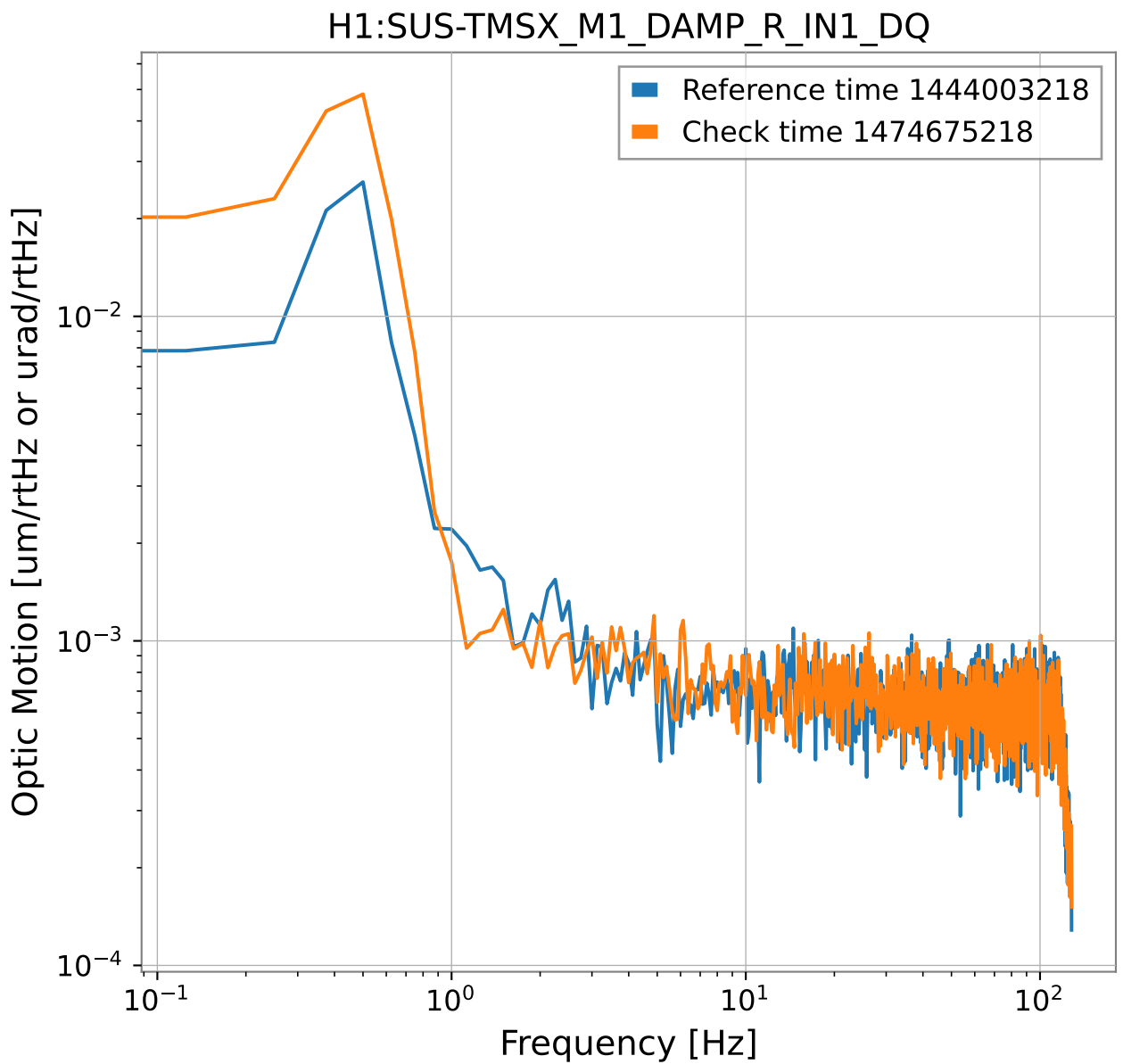
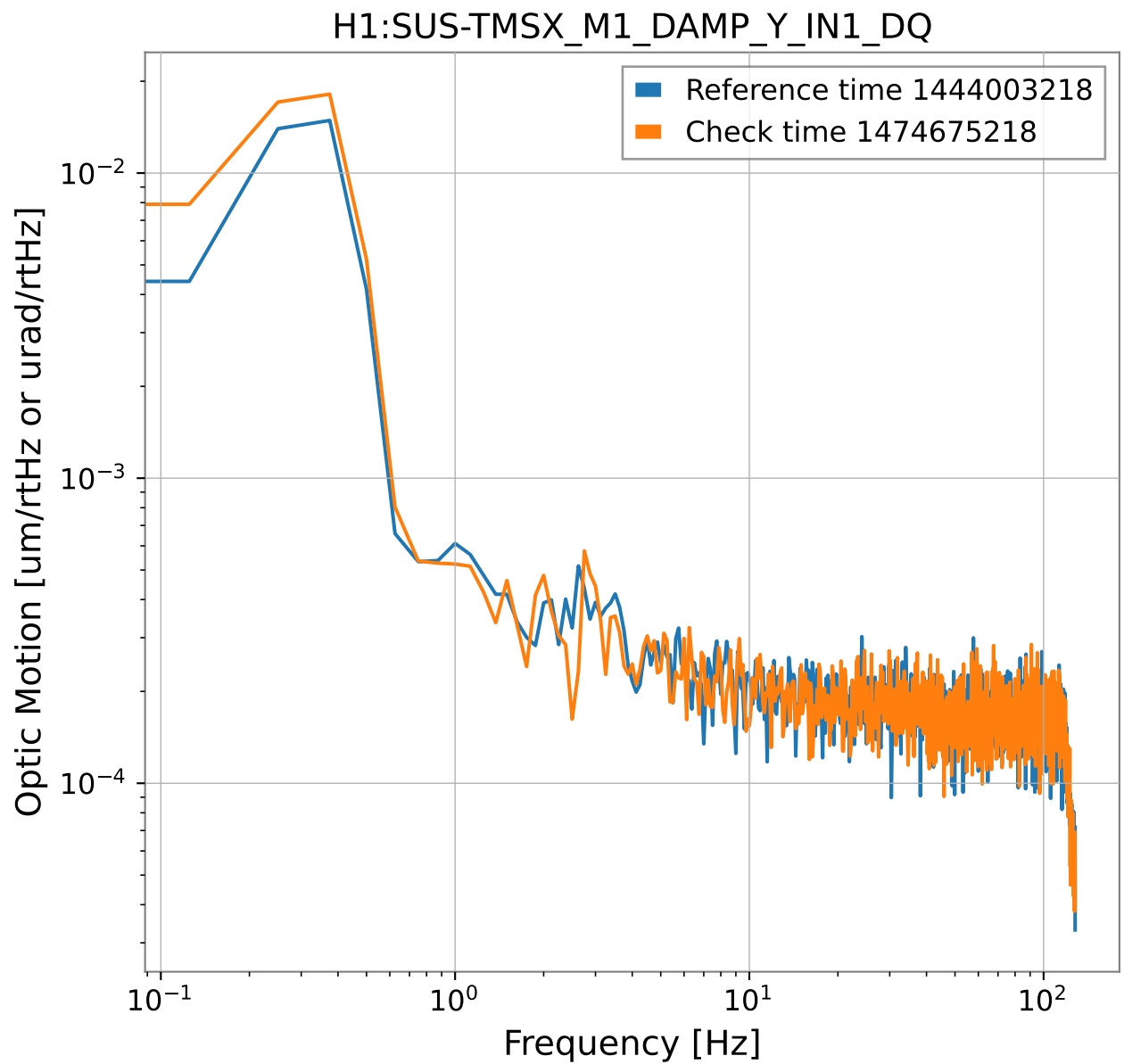
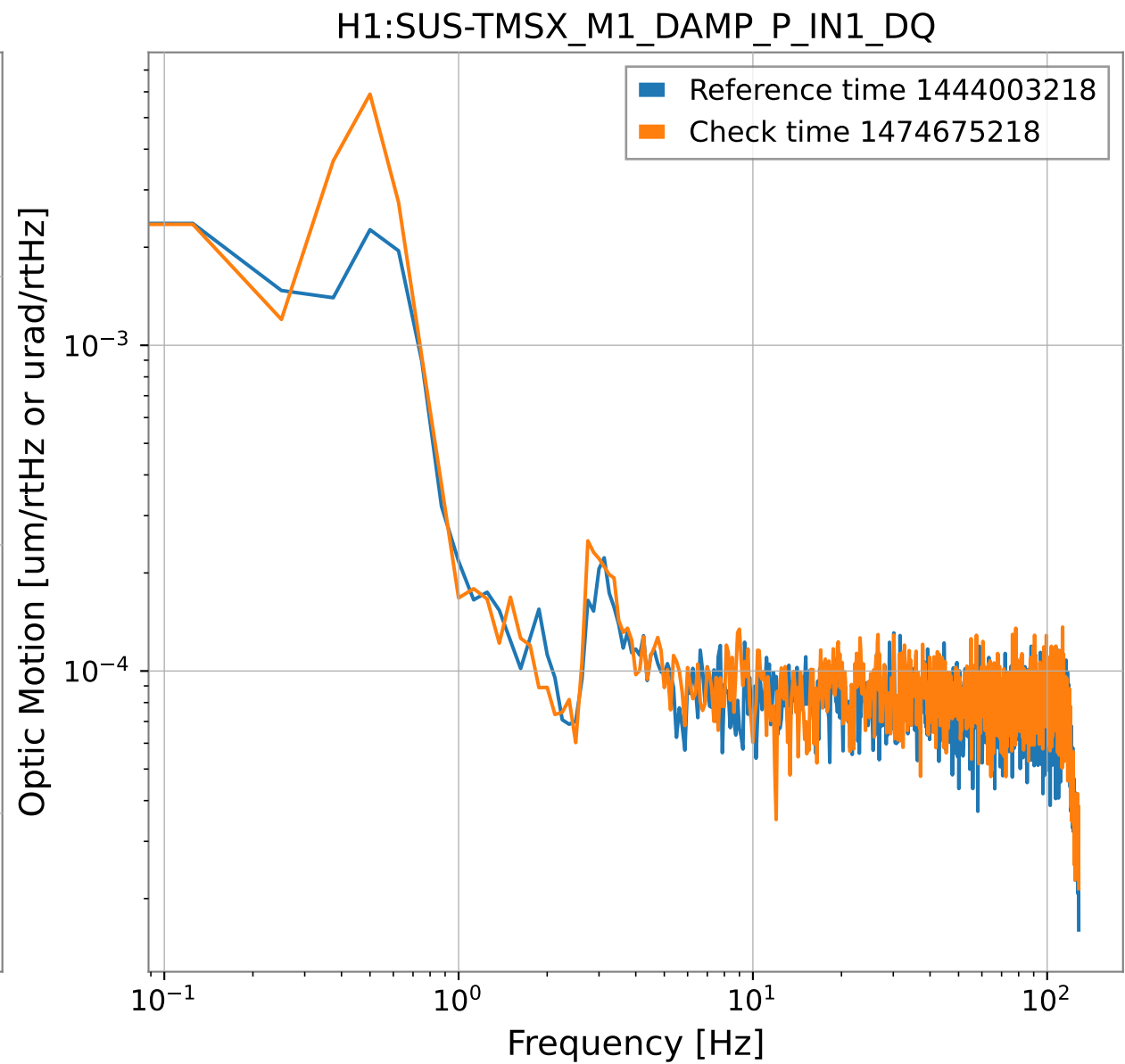
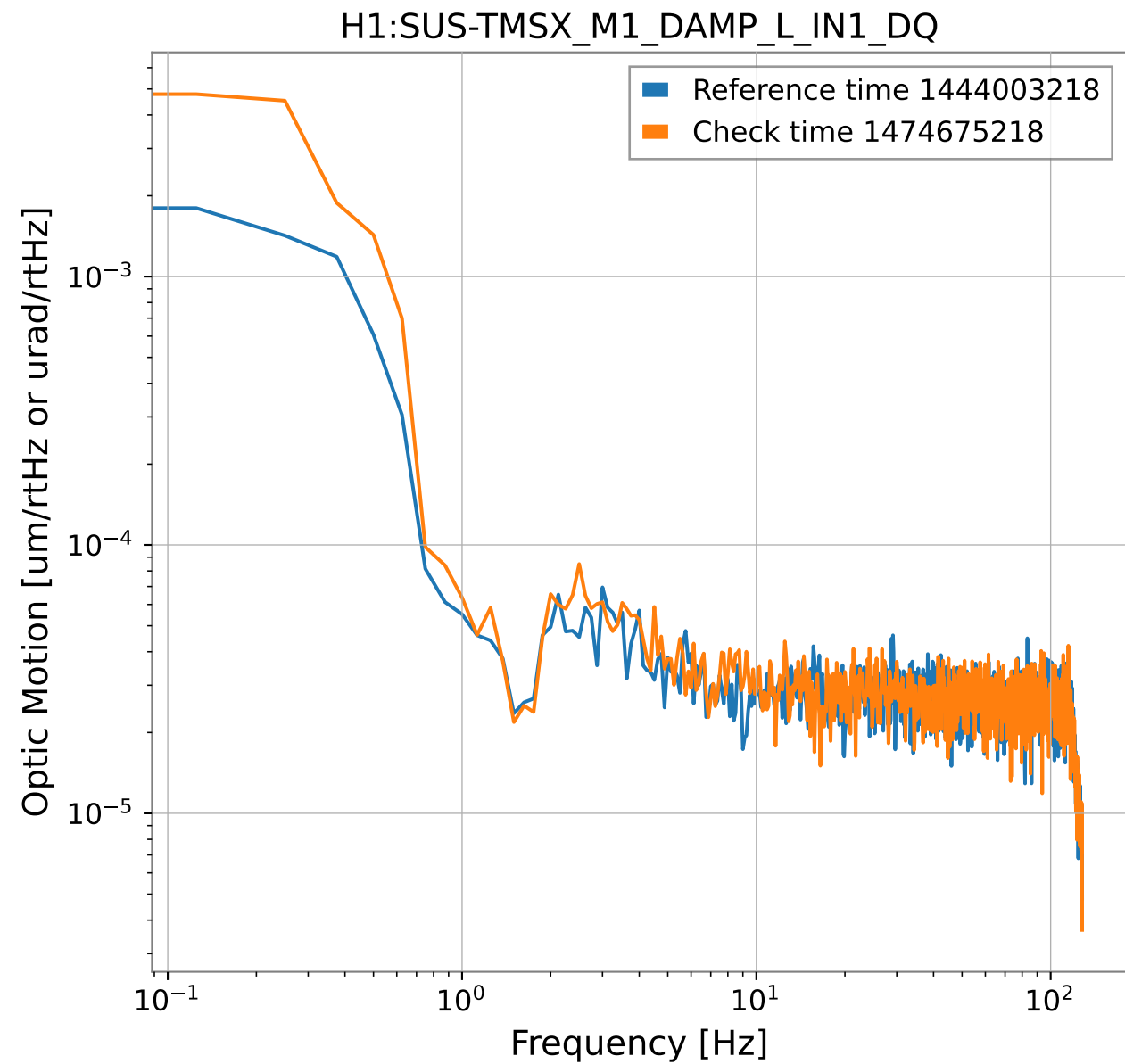
MC2



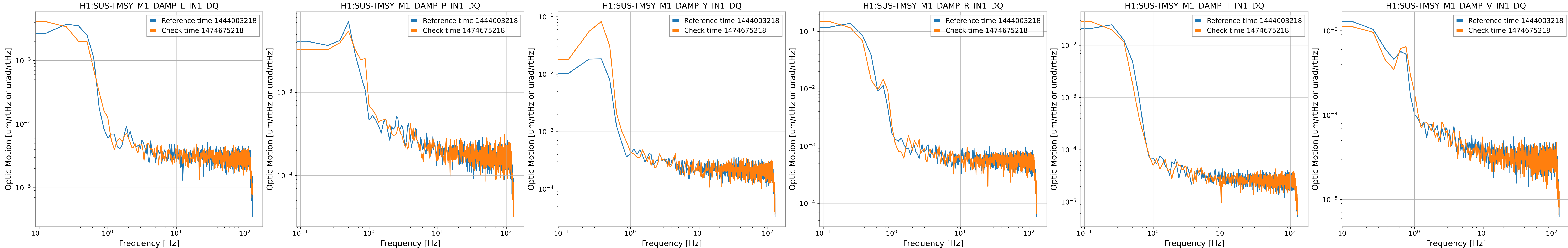
MC3



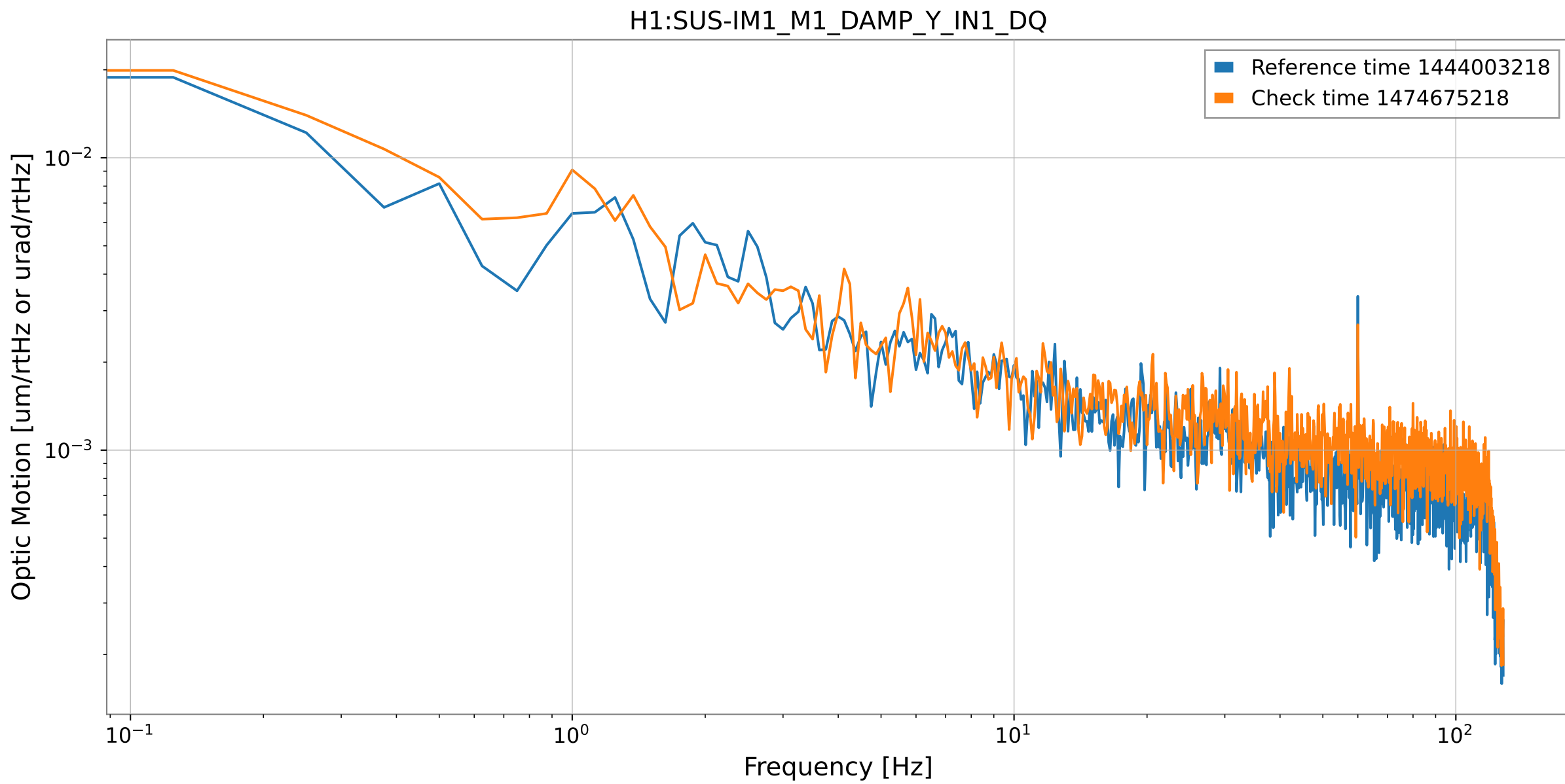
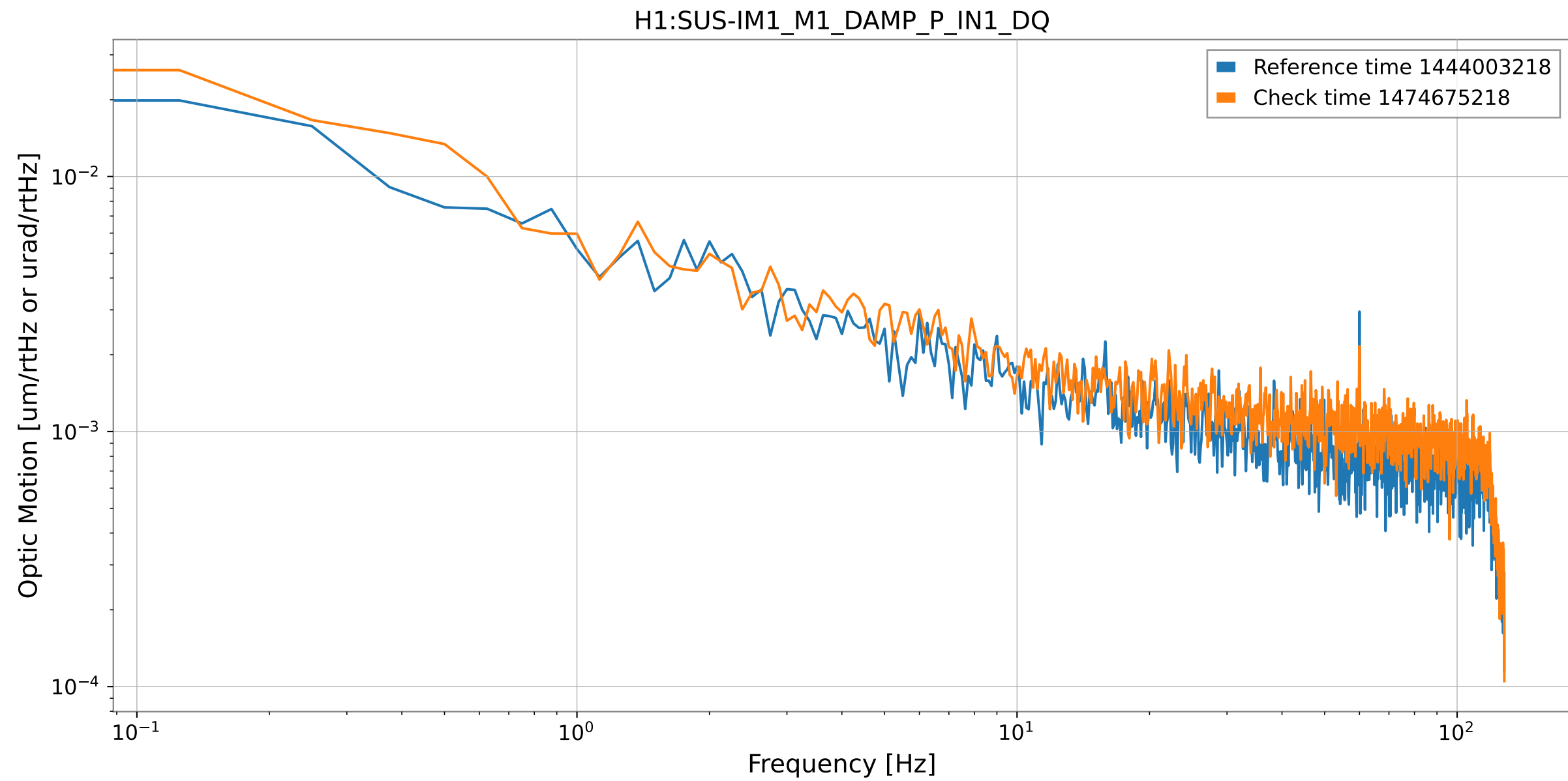
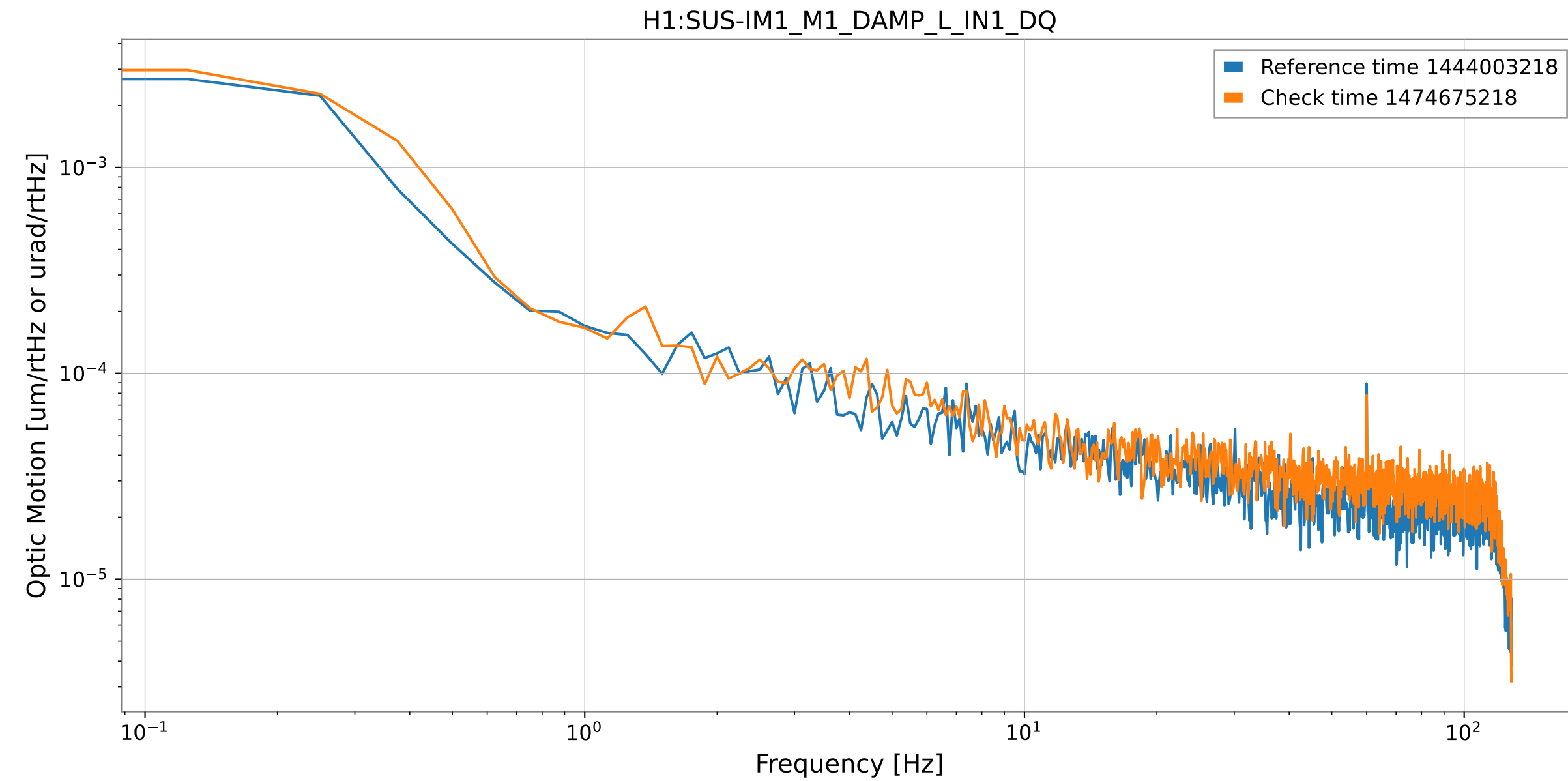
TMSX



TMSY

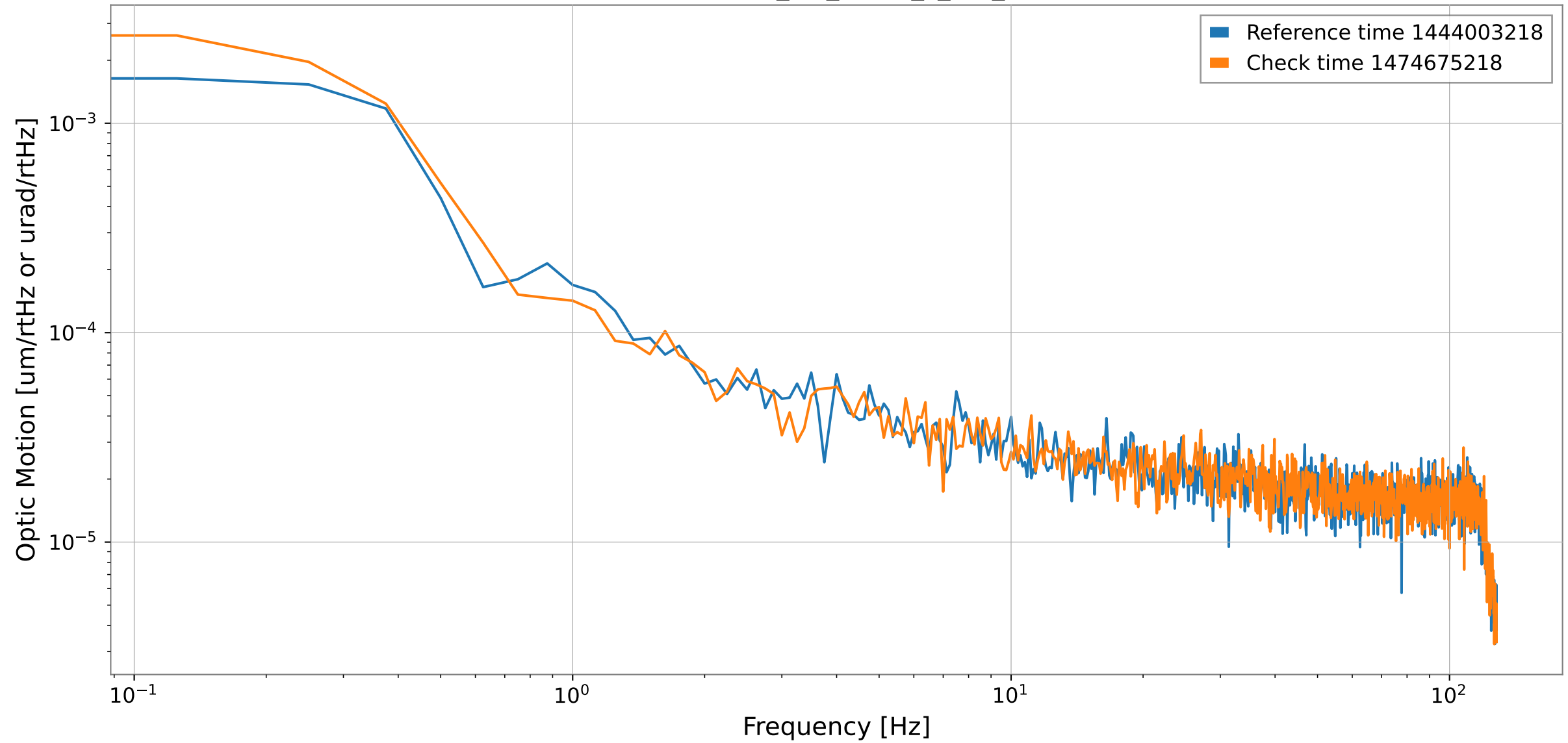


IM1

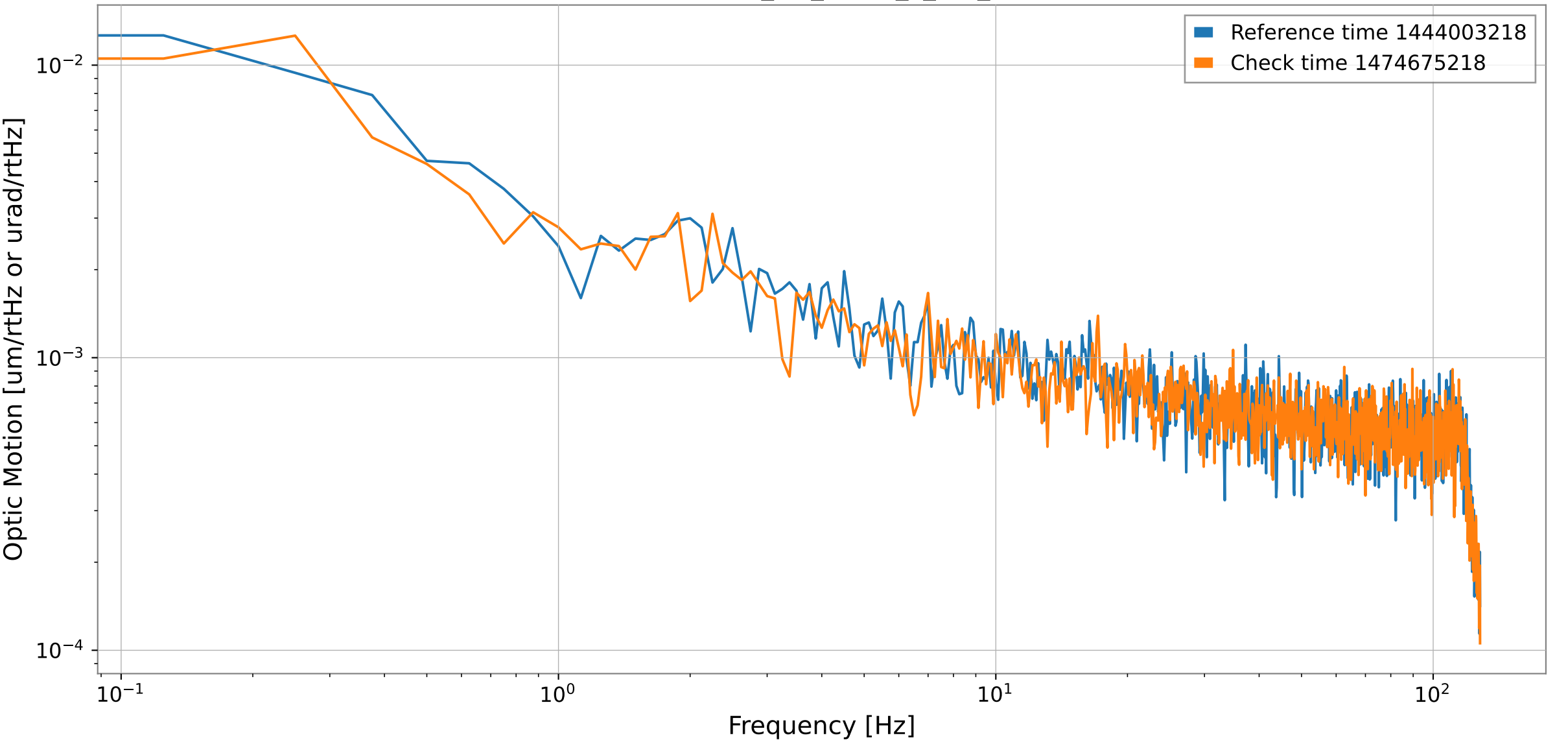


IM2

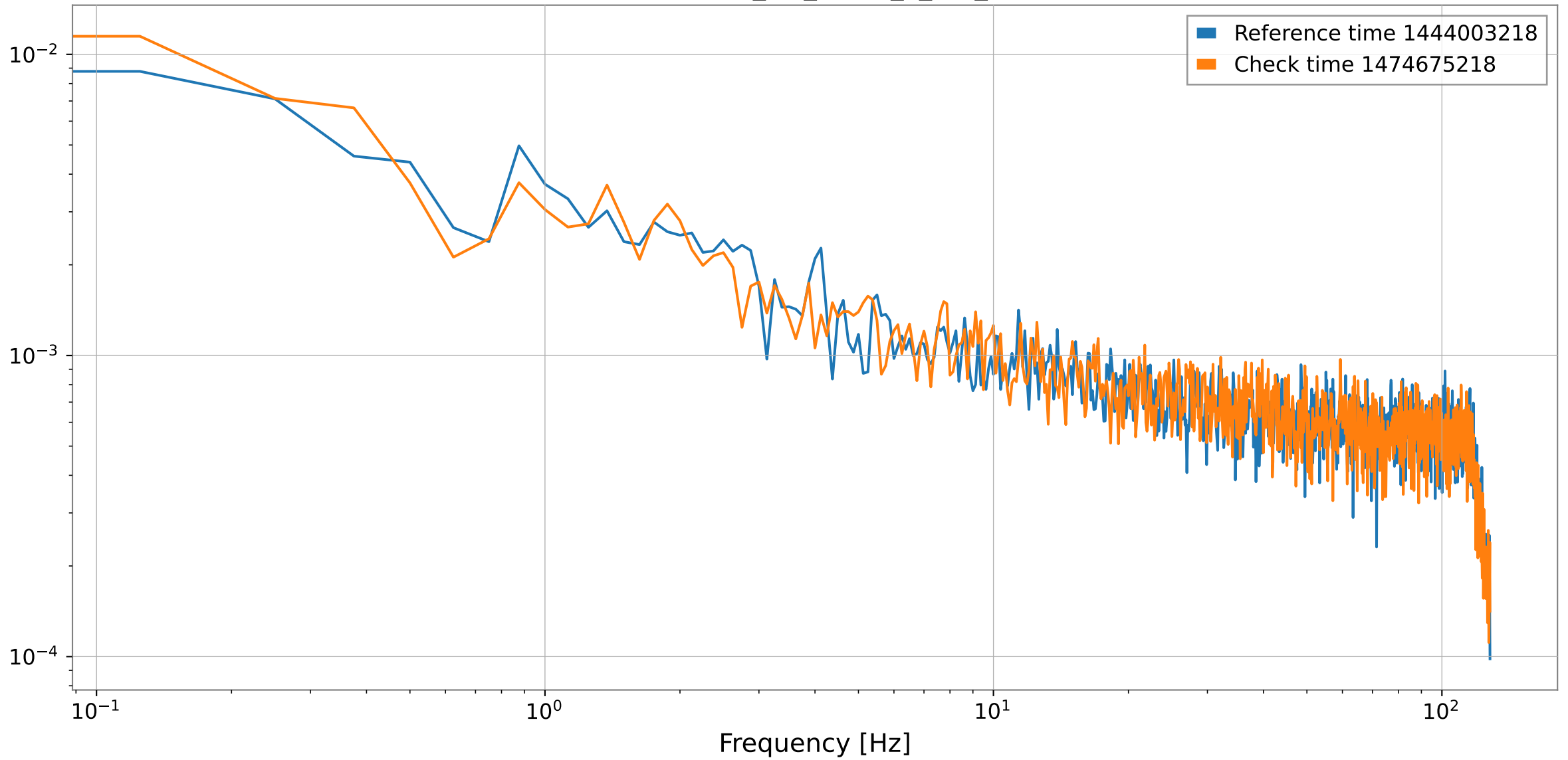
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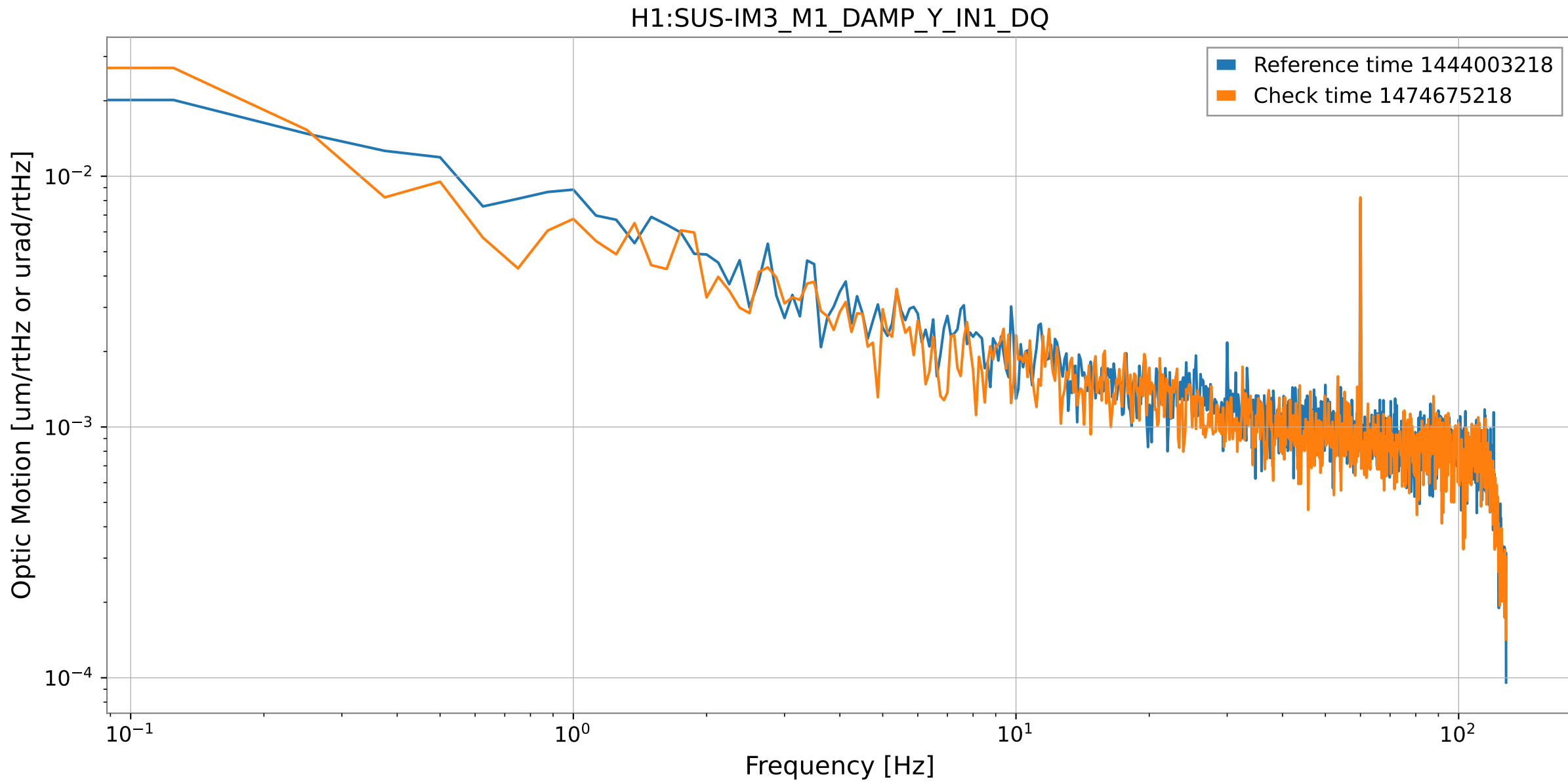
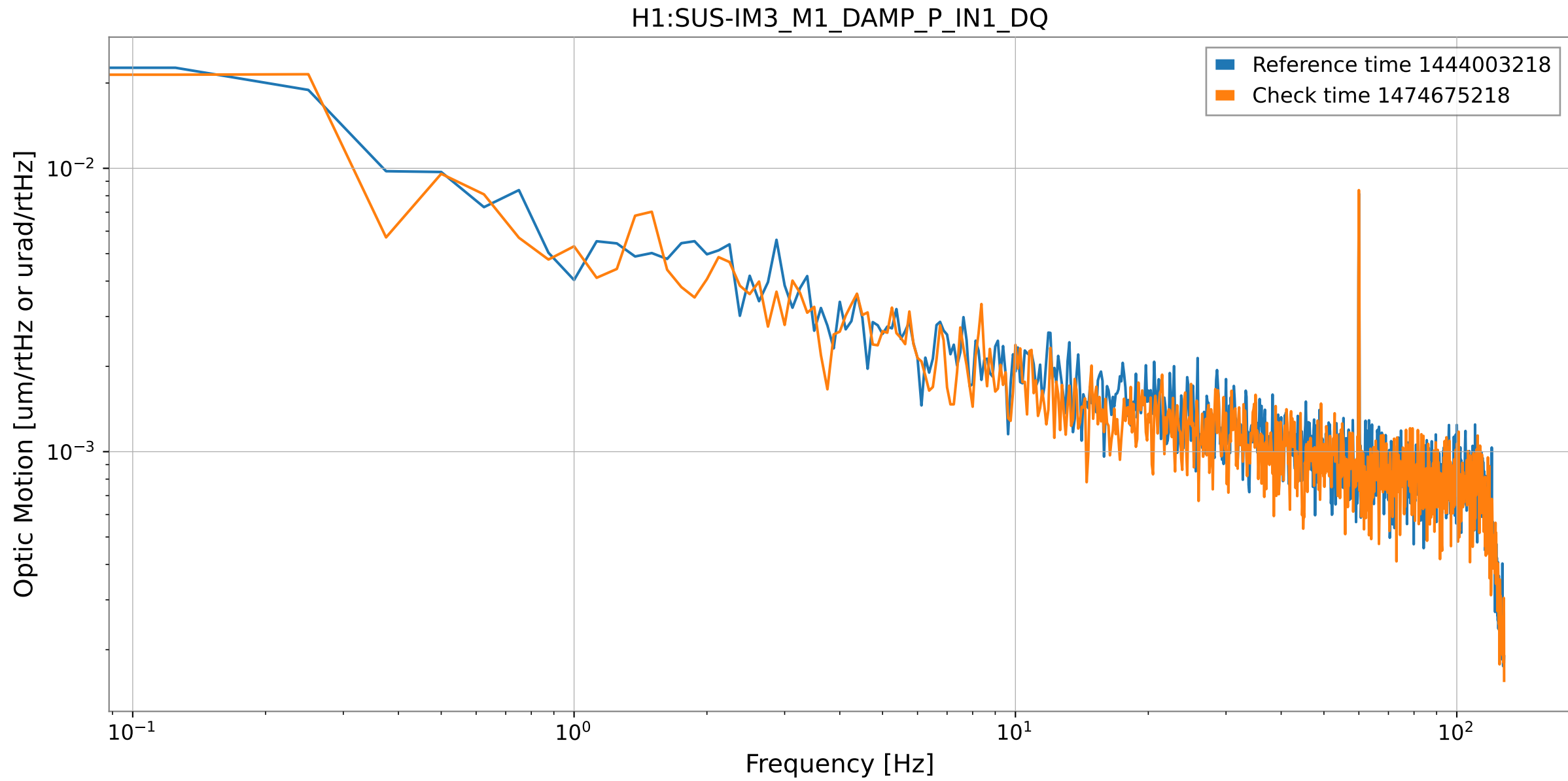
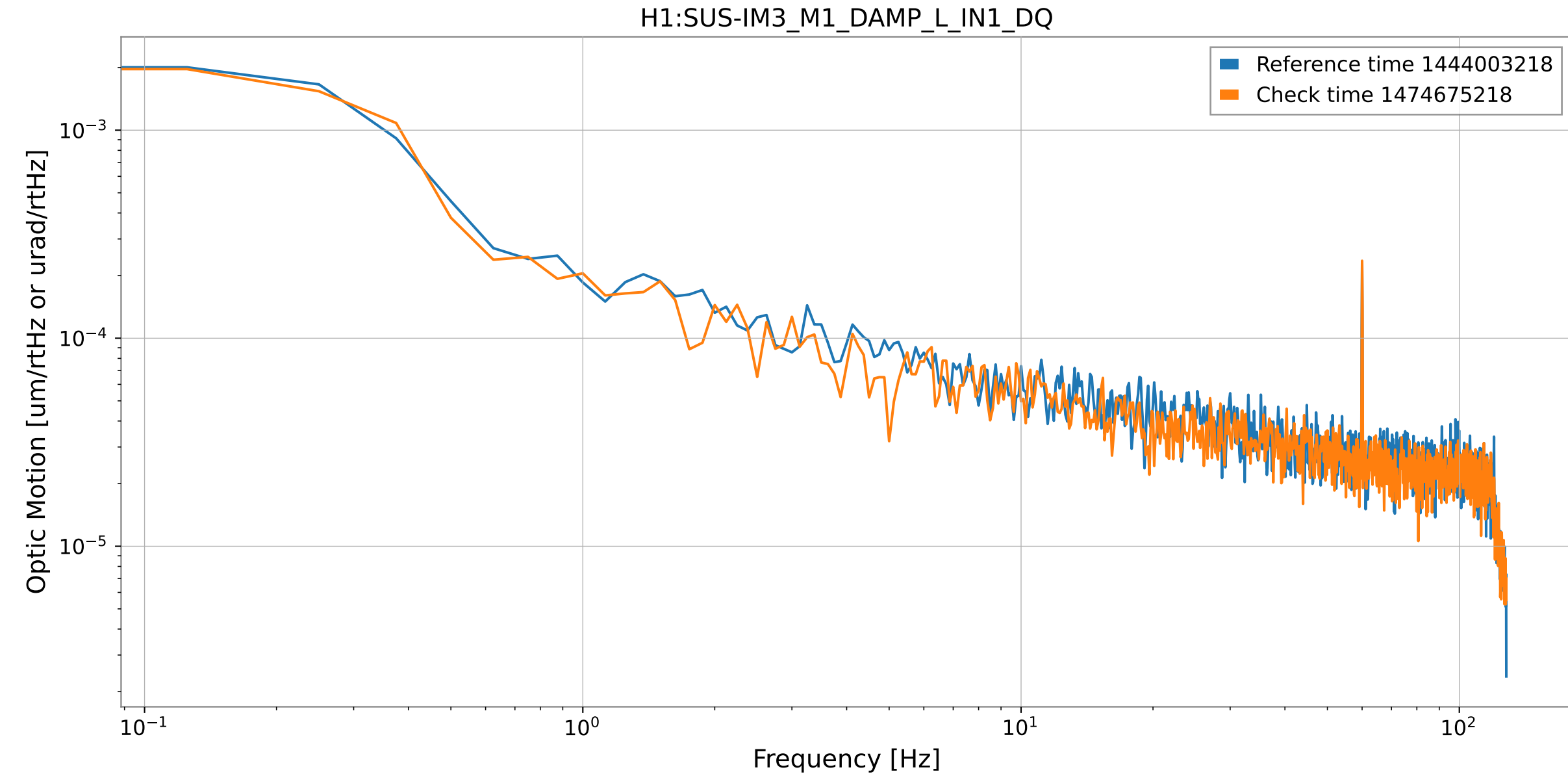
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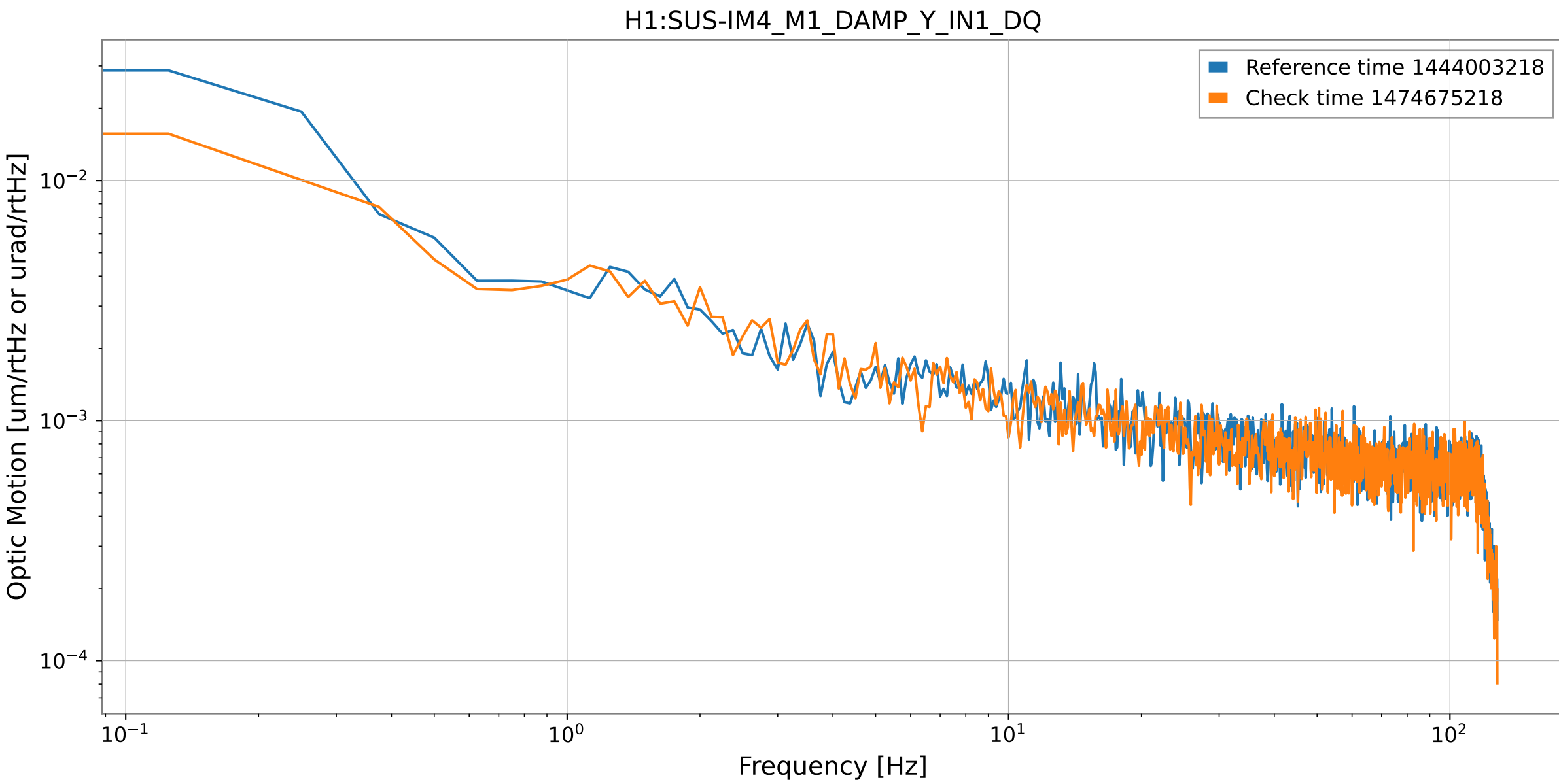
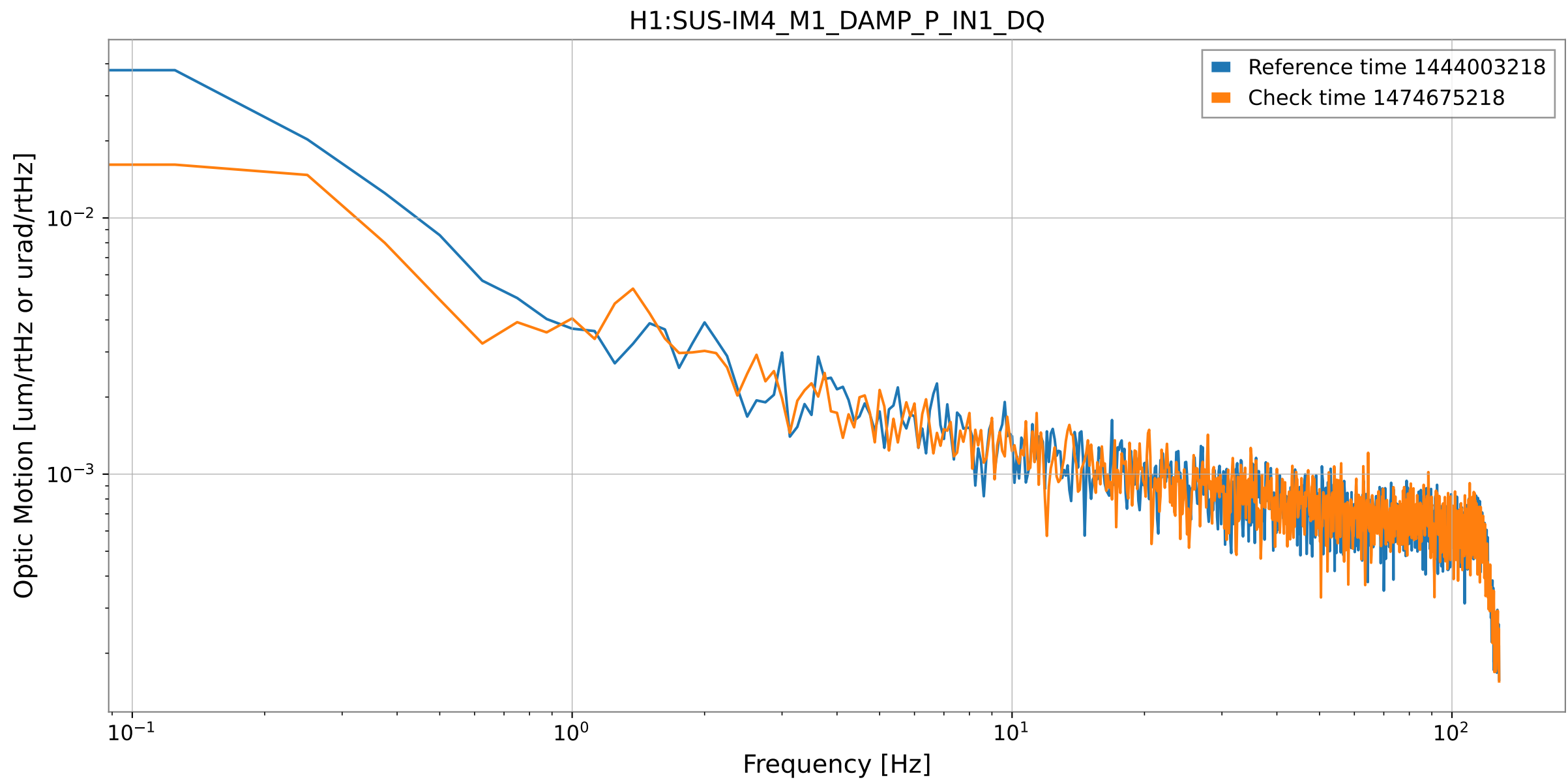
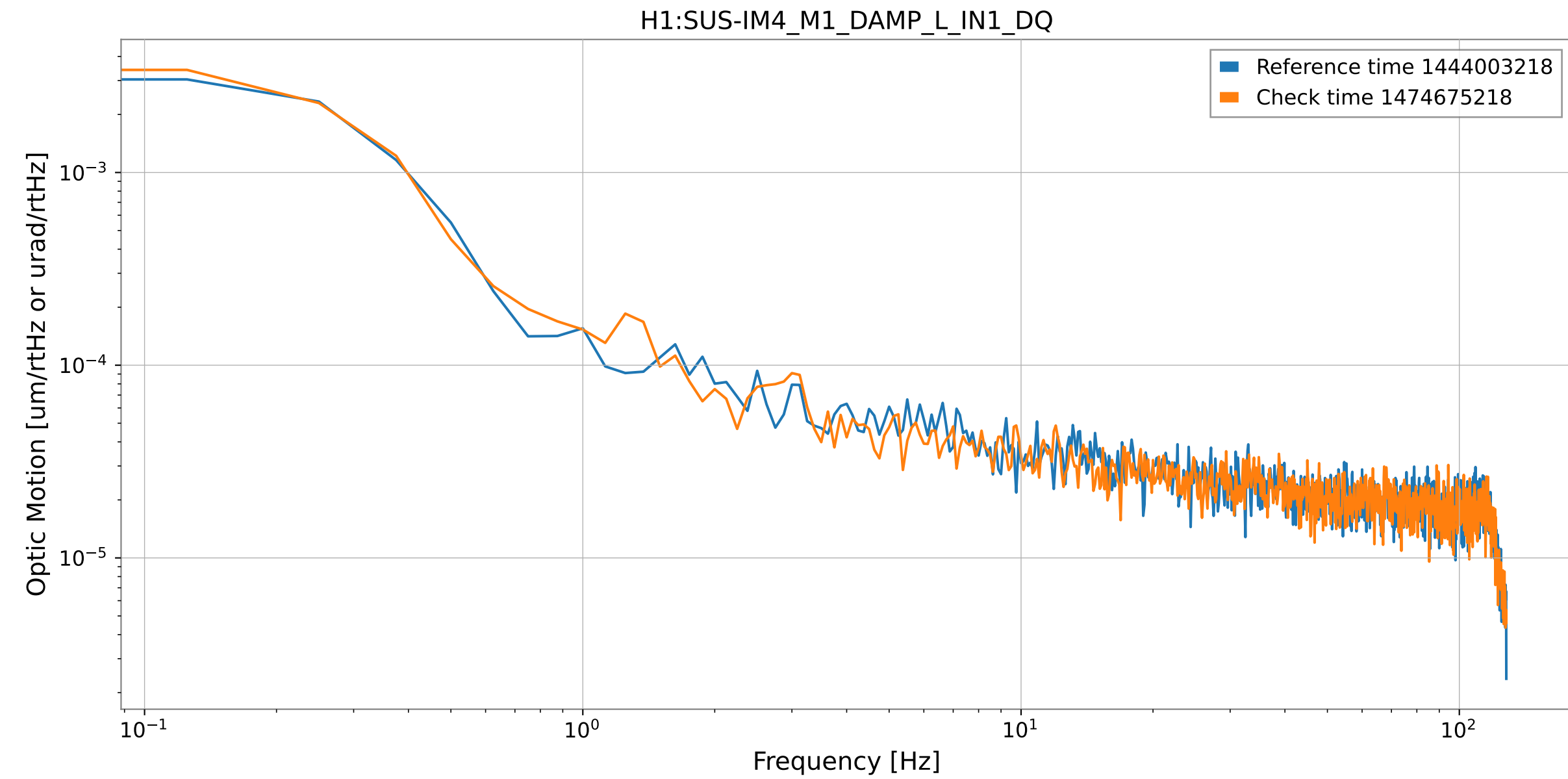
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IM3

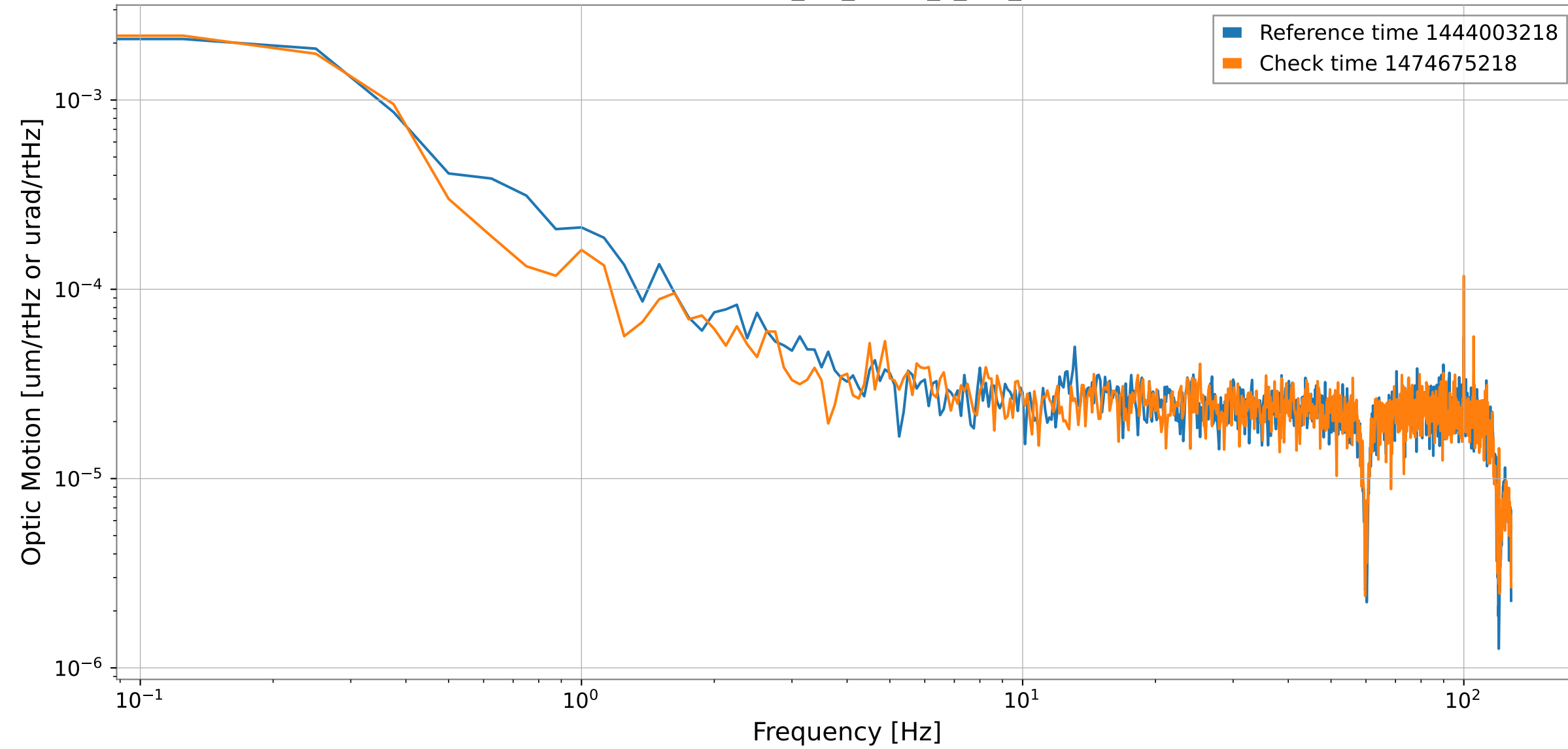


IM4

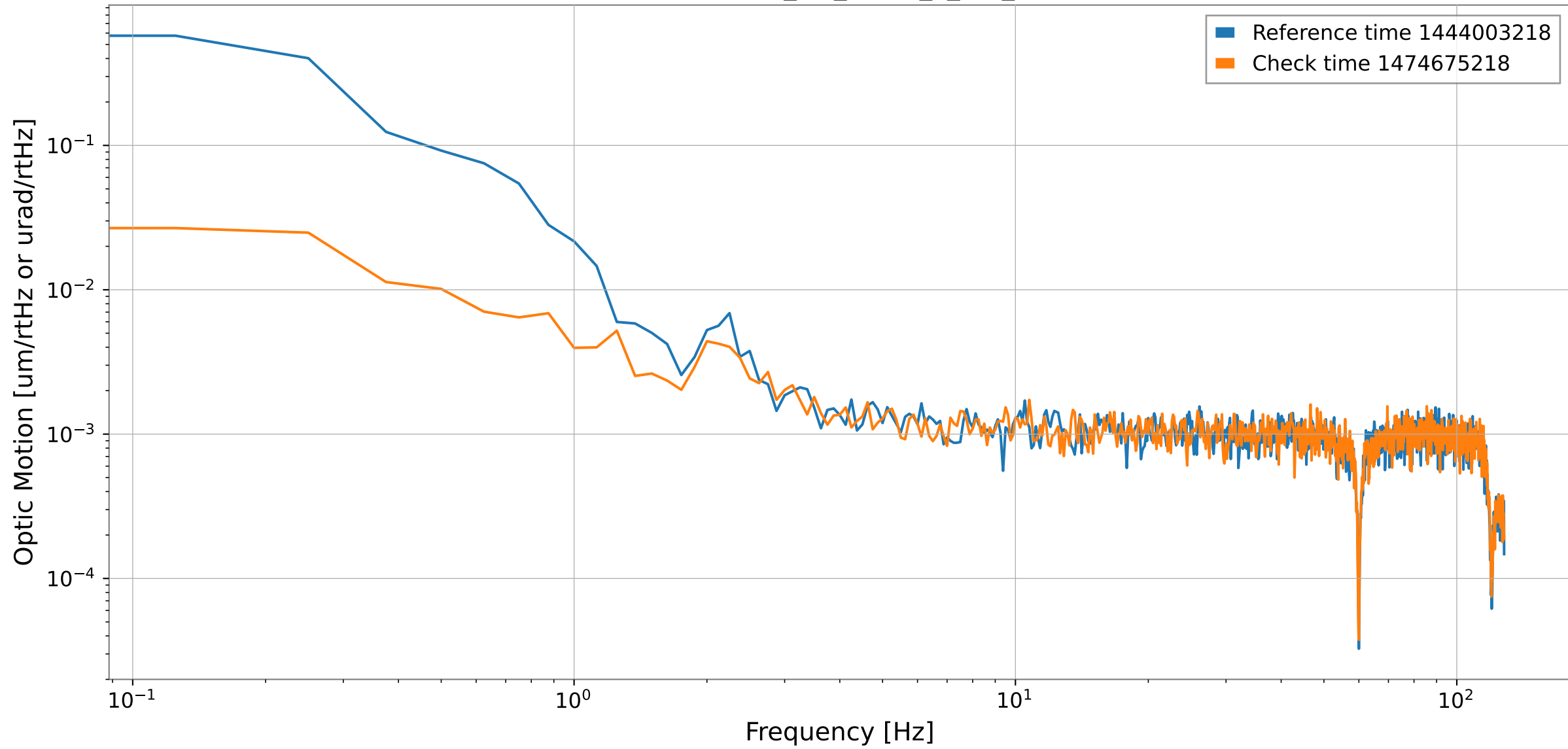


RM1

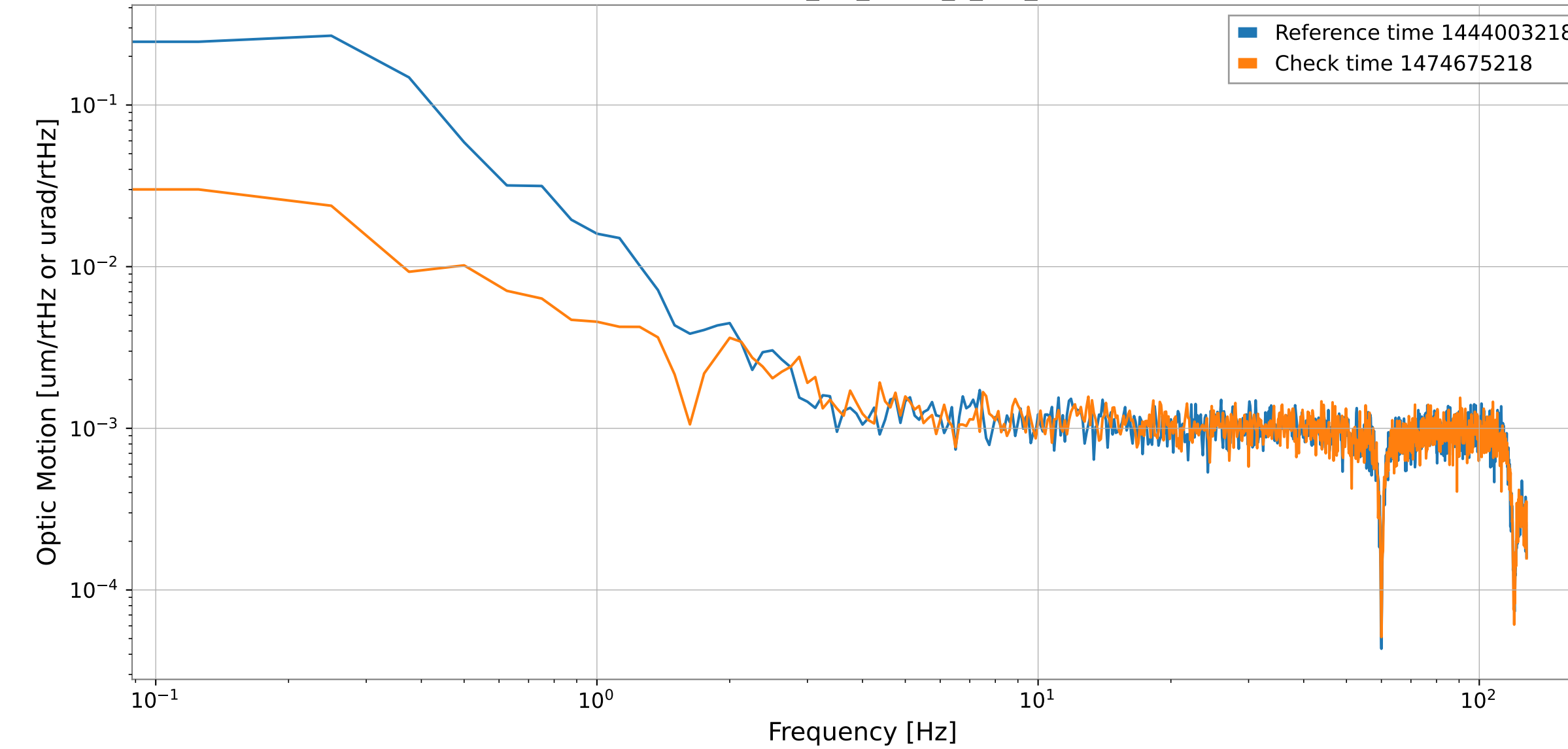
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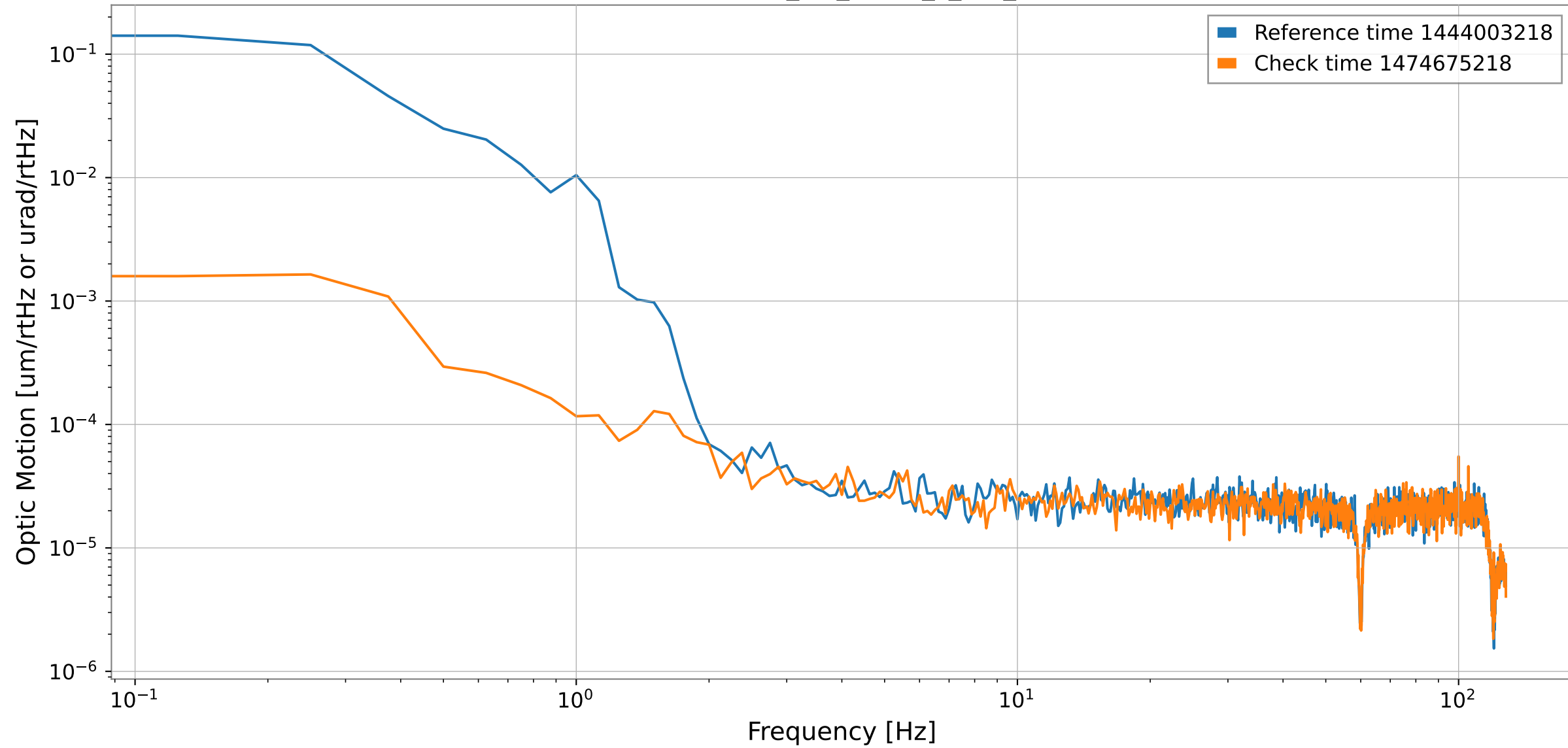


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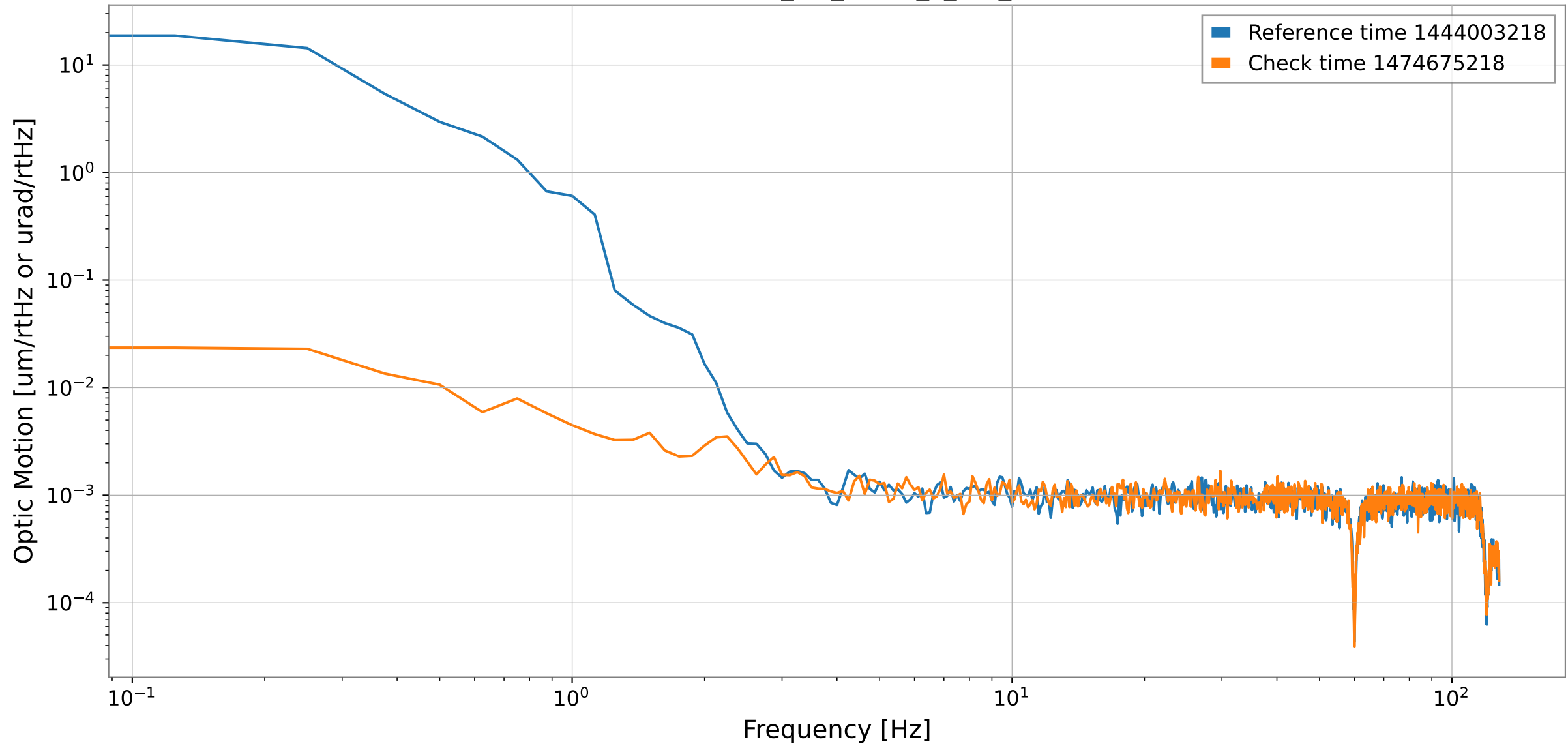


RM2

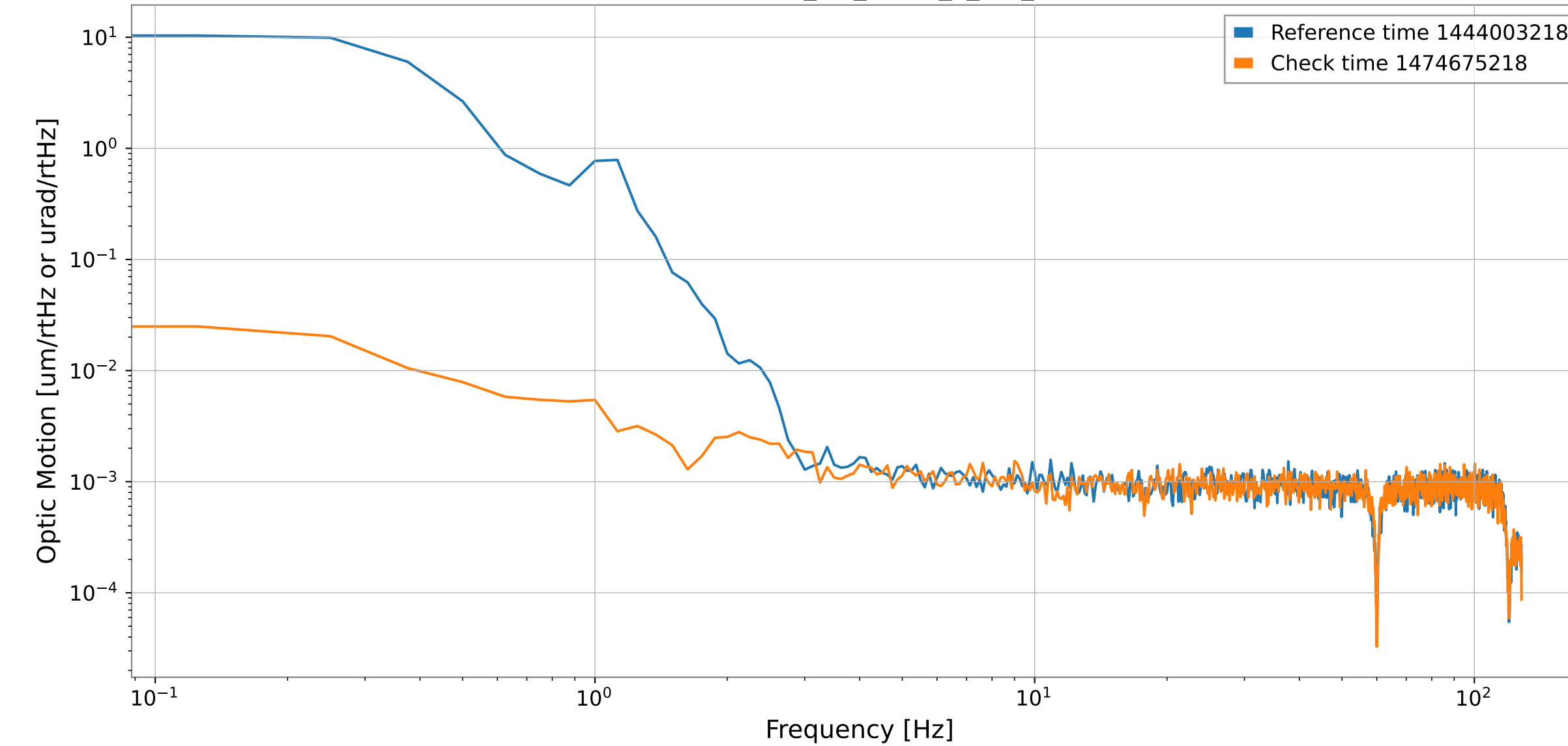
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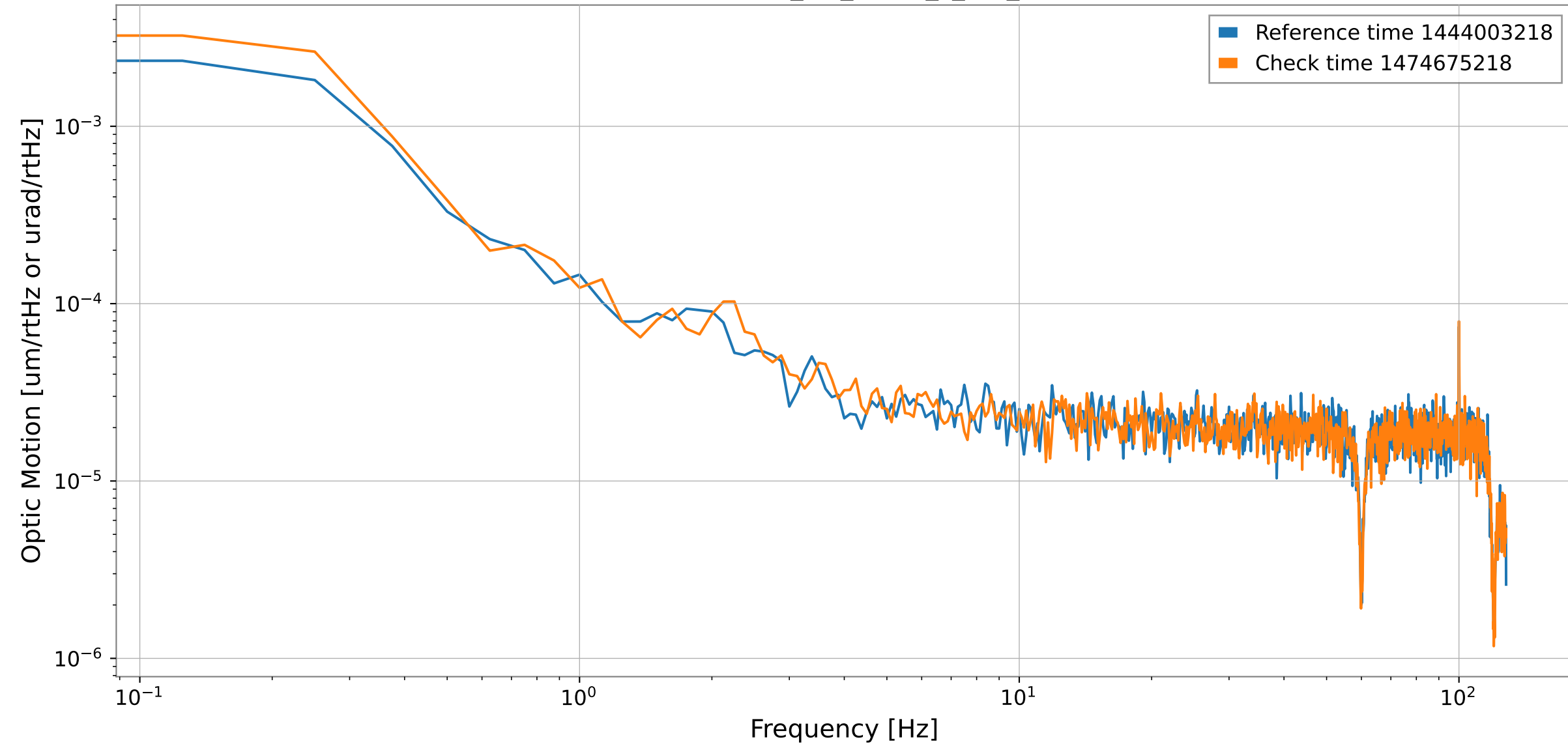


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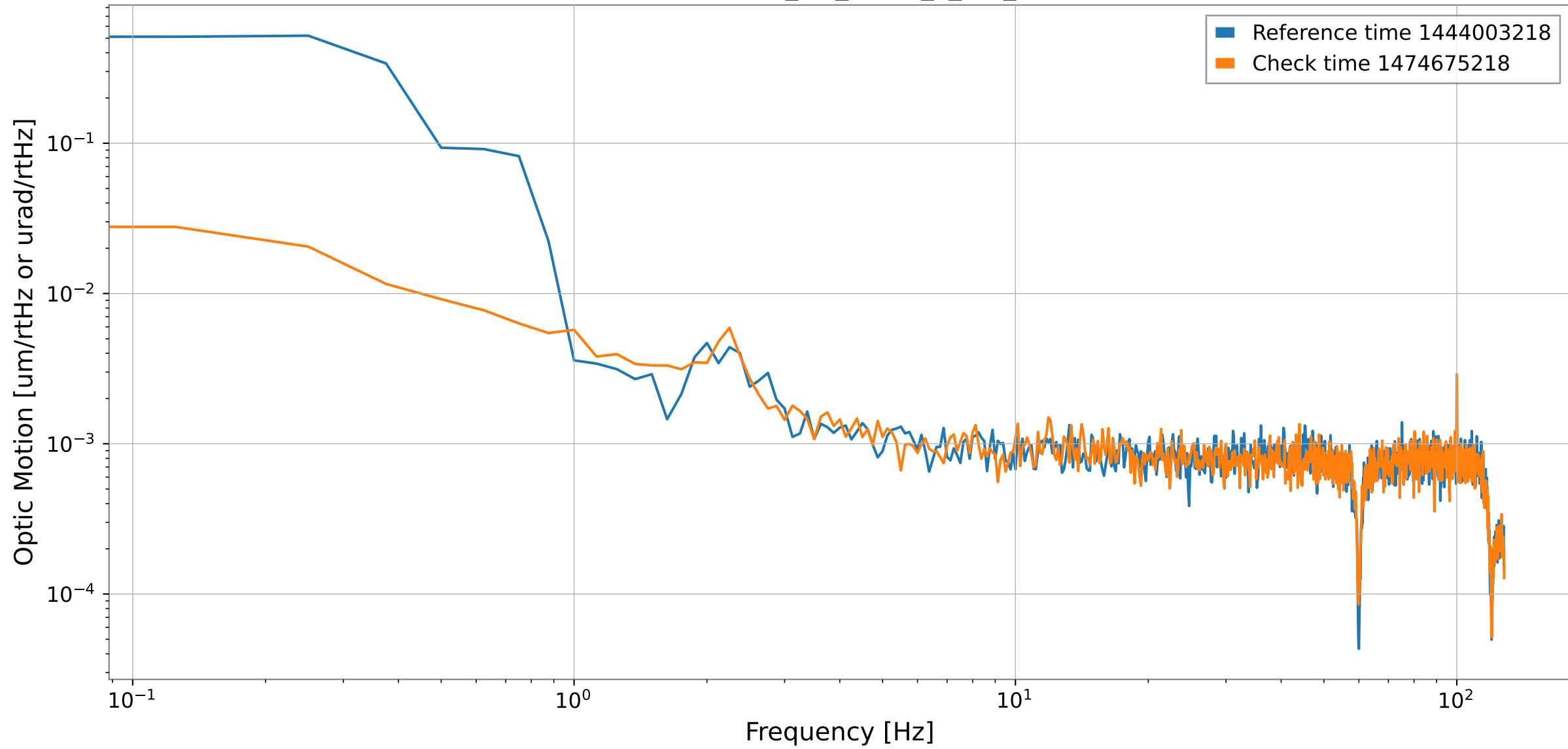


OM1

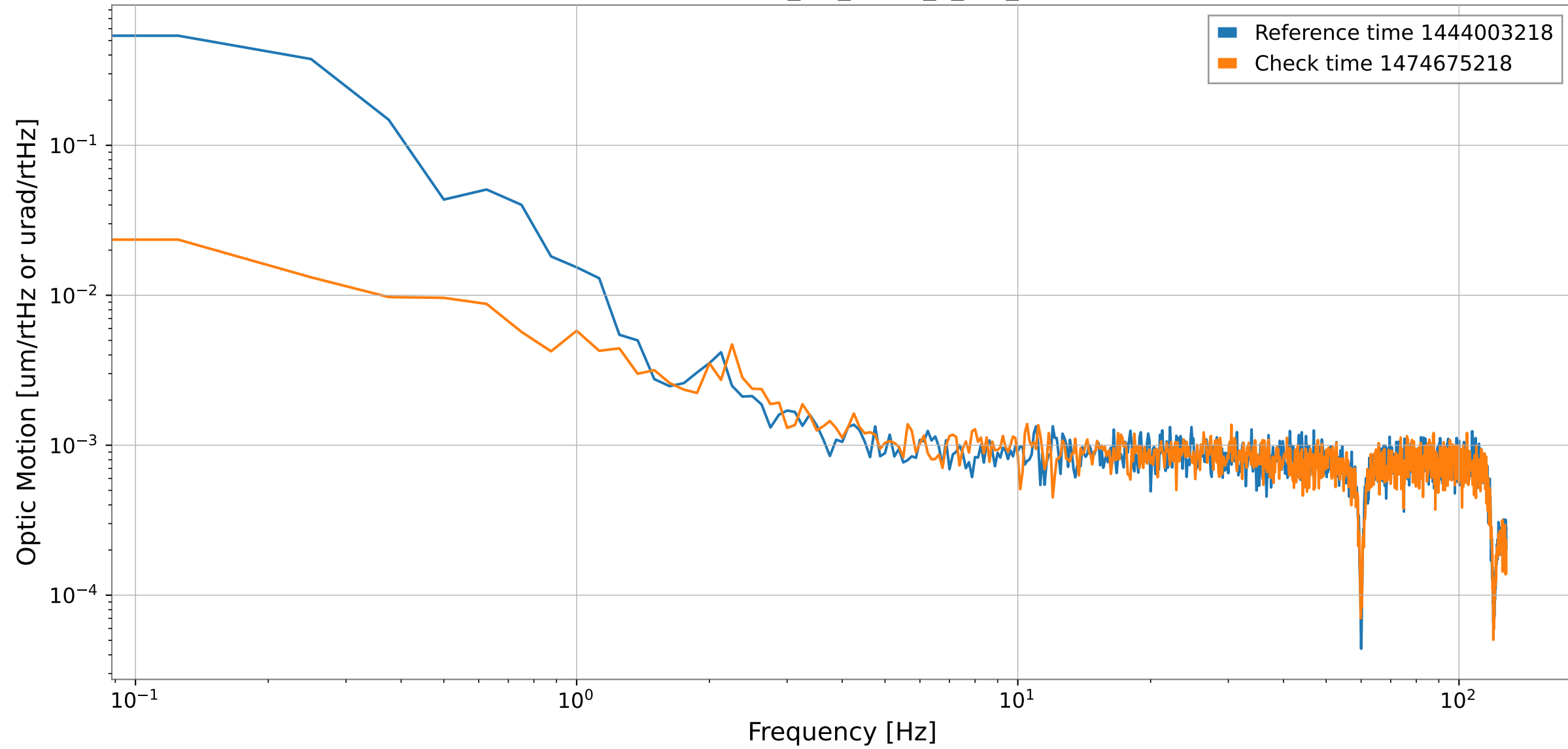
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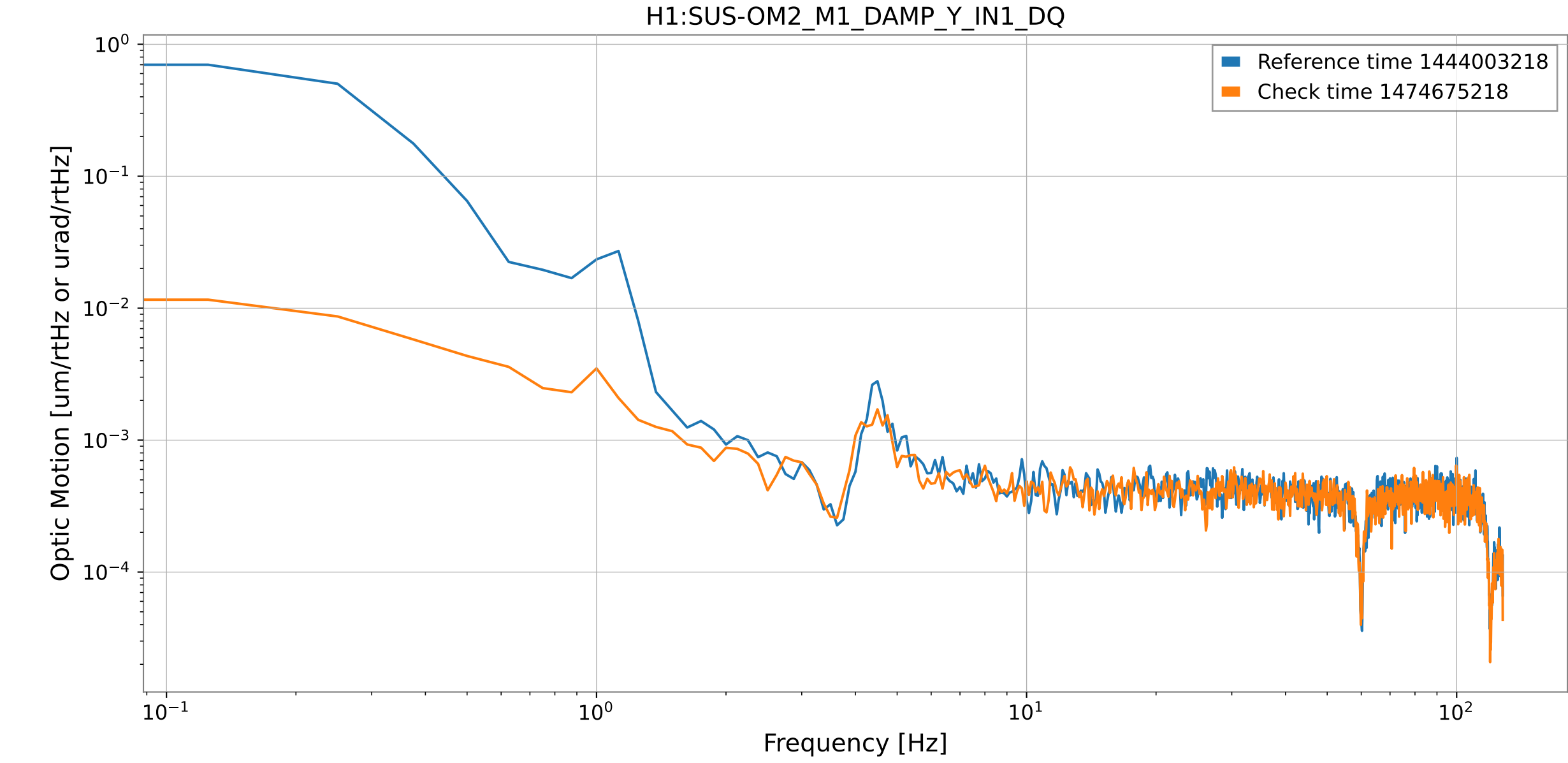
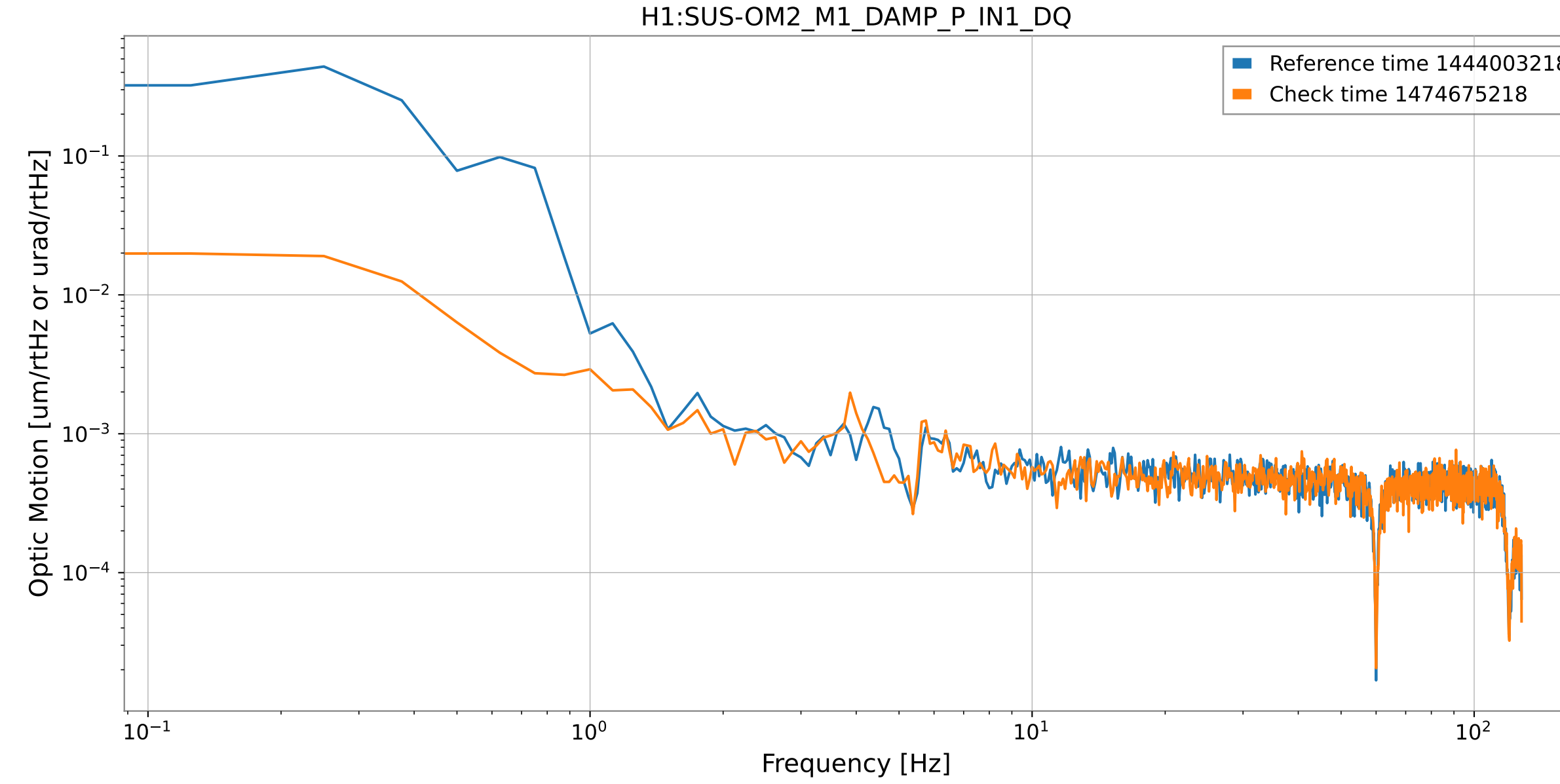
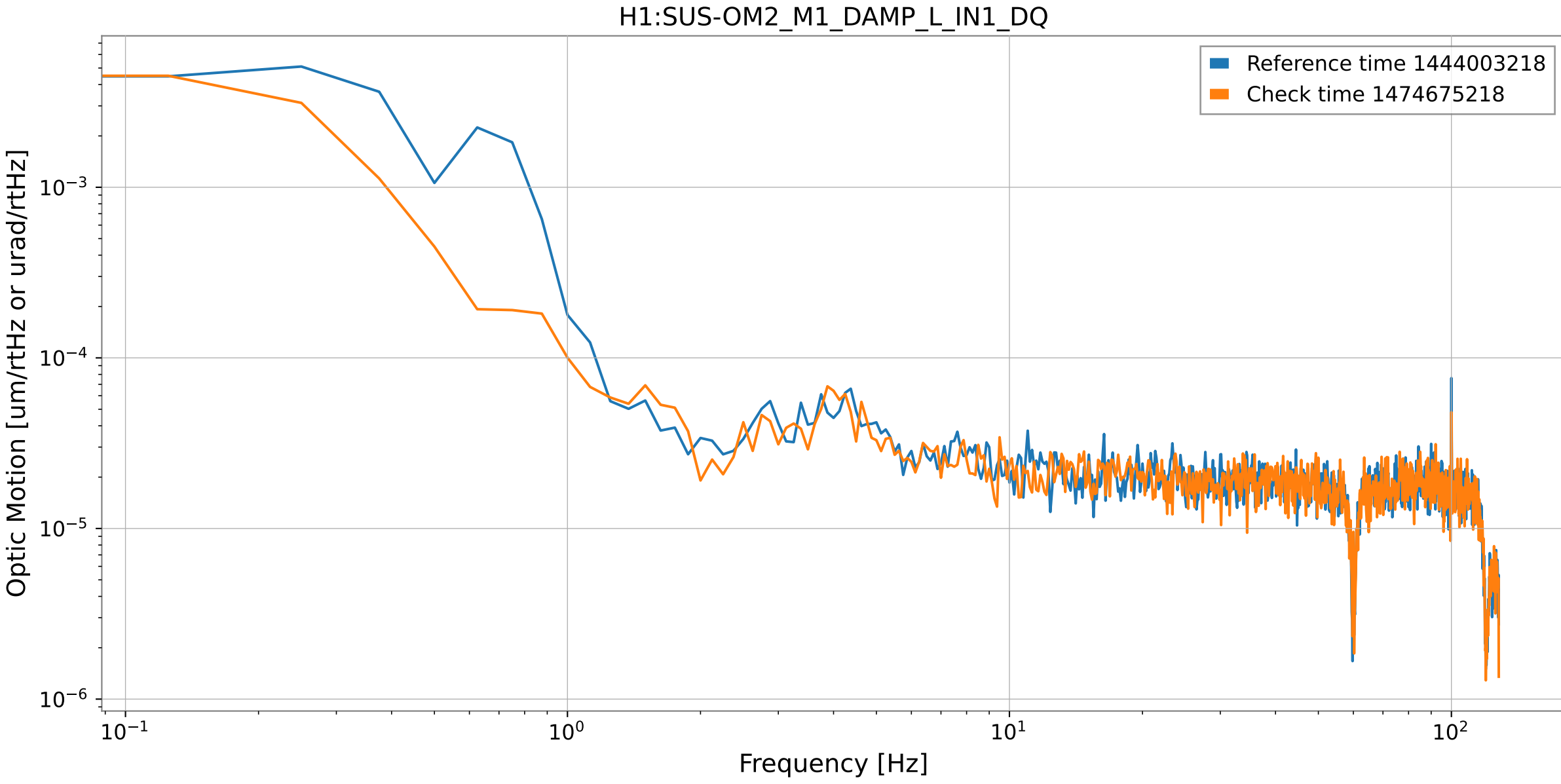
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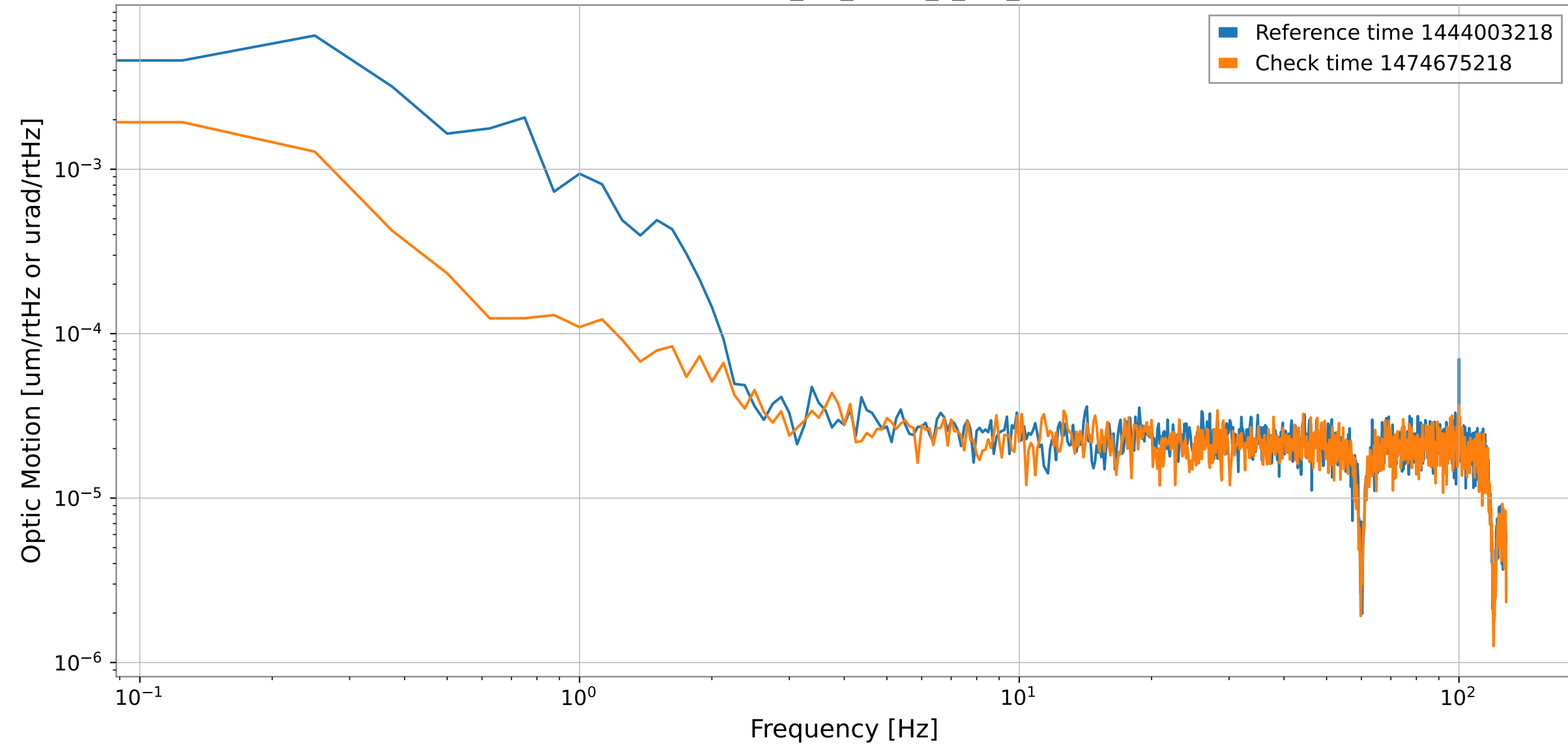


OM2

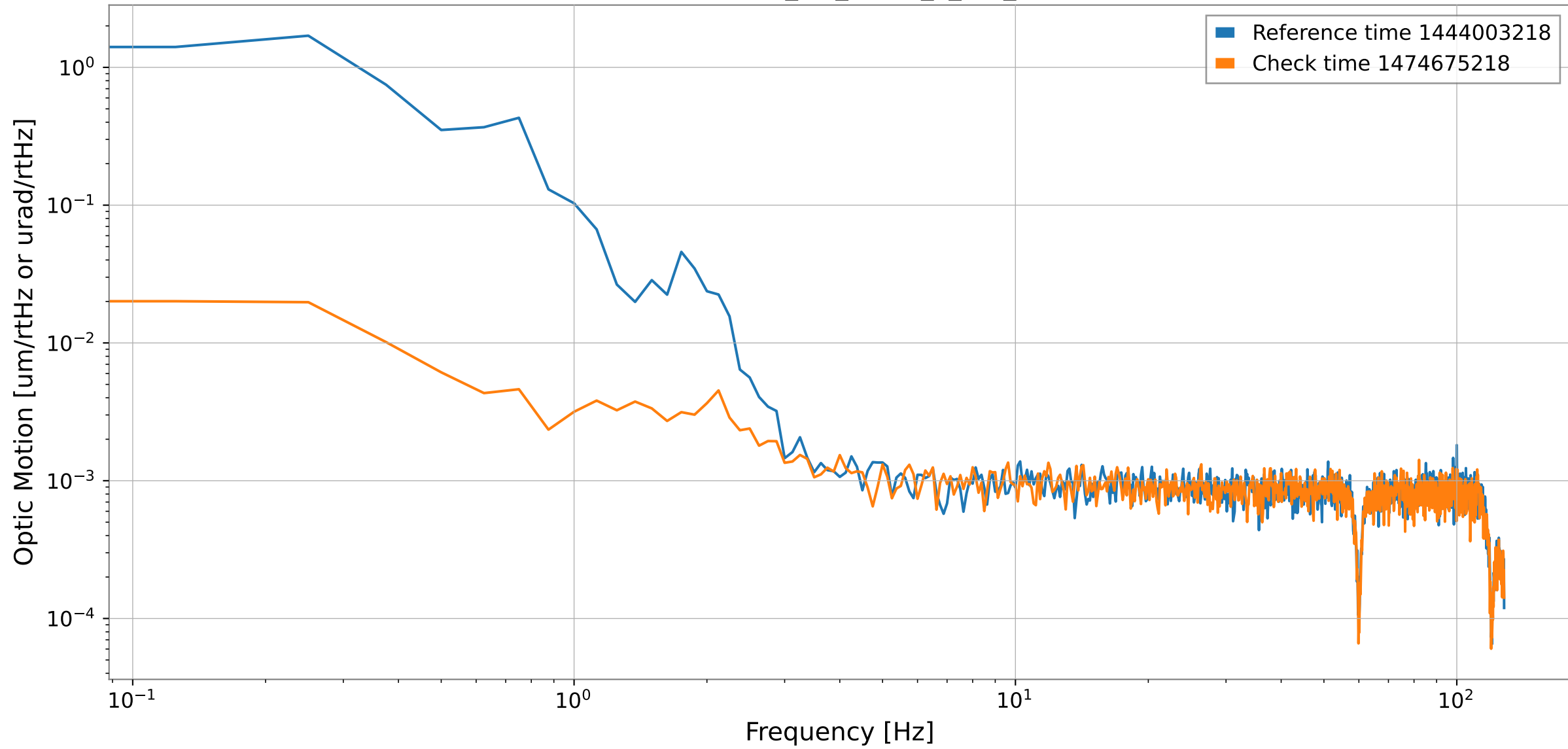


OM3

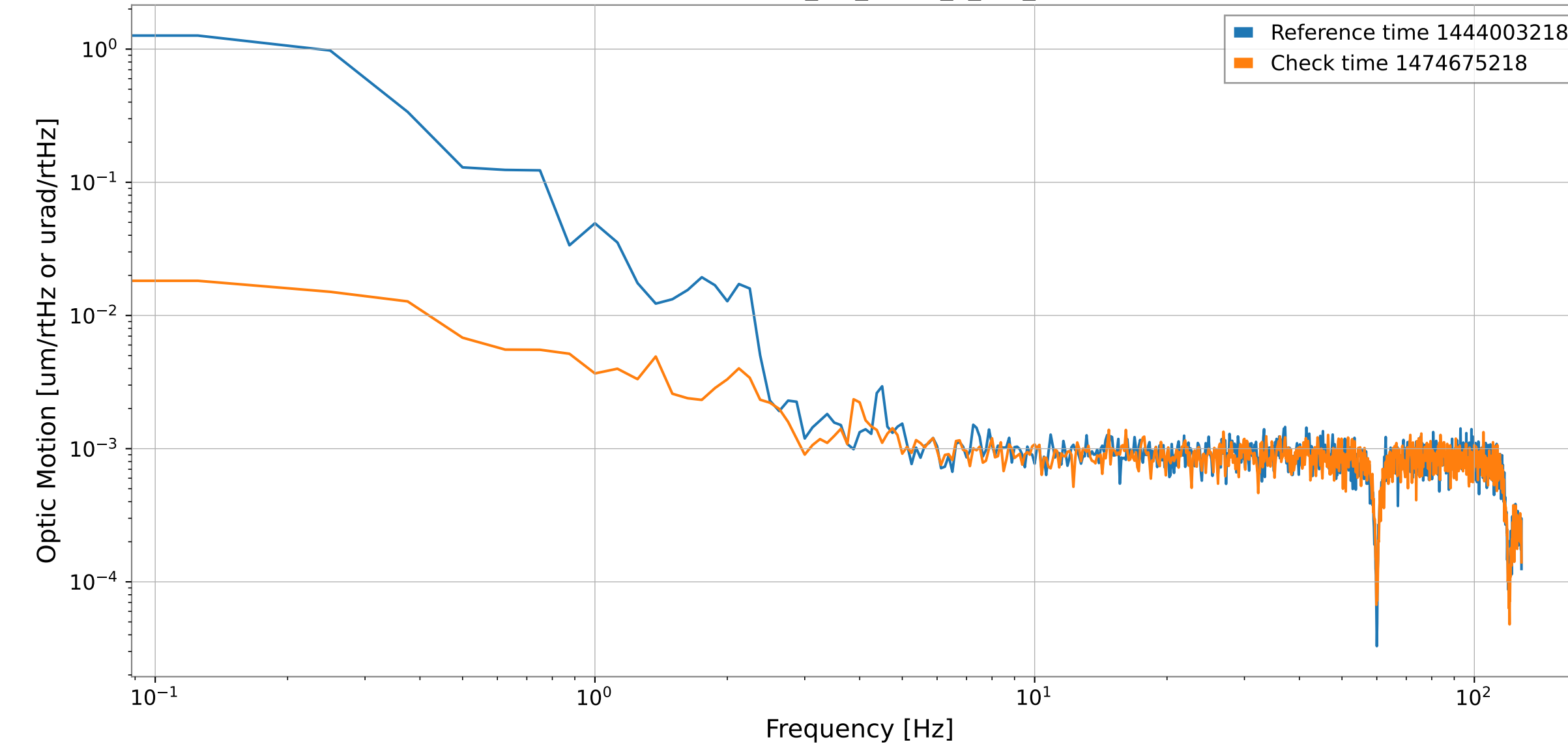
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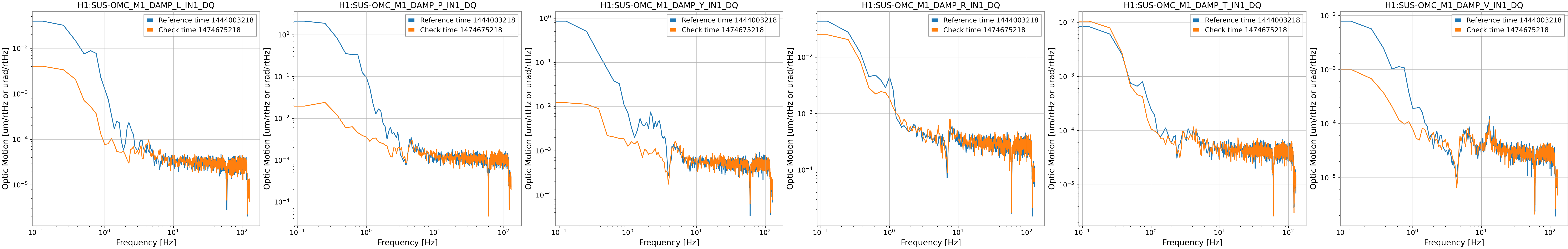
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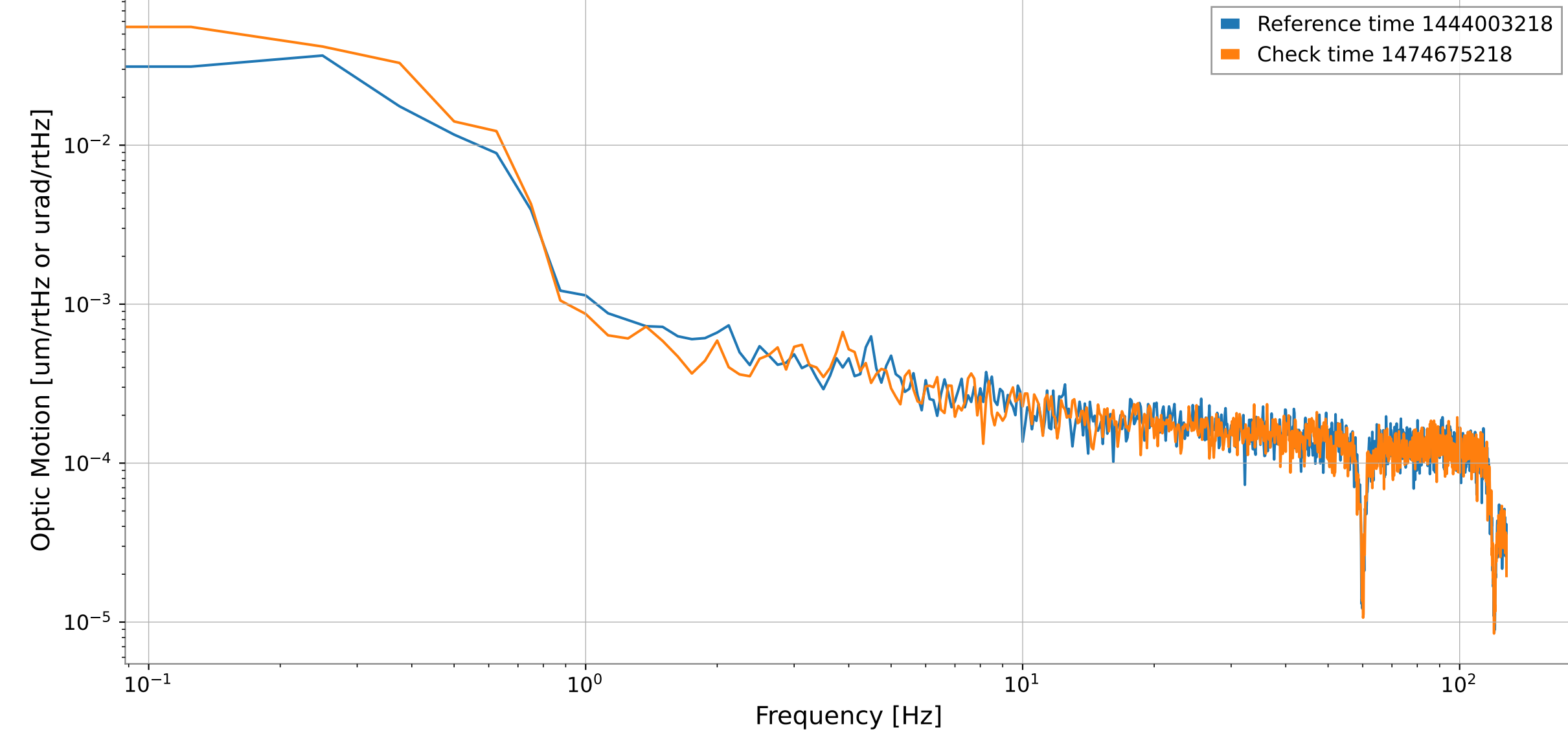


OMC

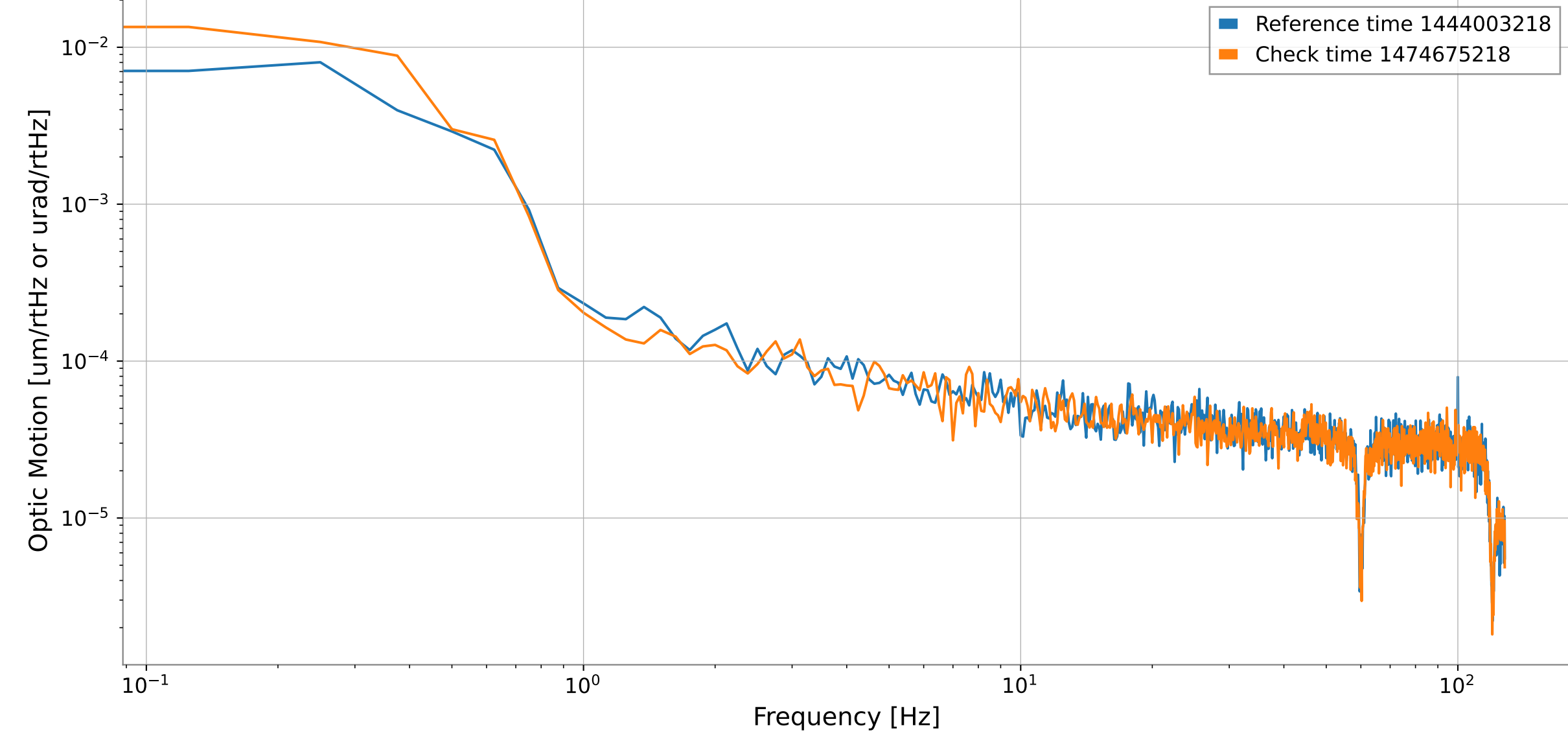


OFI

H1:SUS-OFI_M1_DAMP_L_IN1_DQ



H1:SUS-OFI_M1_DAMP_T_IN1_DQ



H1:SUS-OFI_M1_DAMP_Y_IN1_DQ

