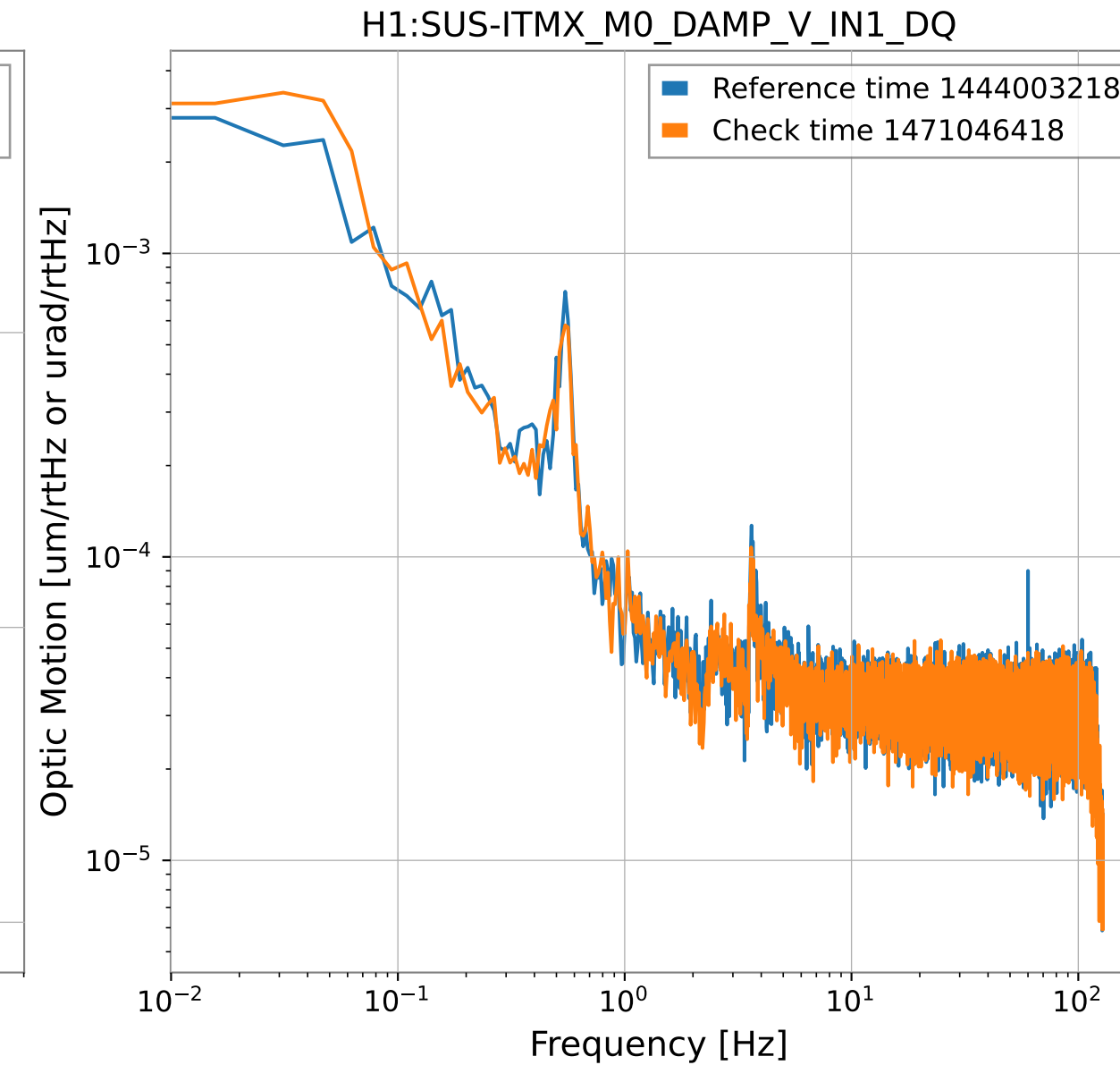
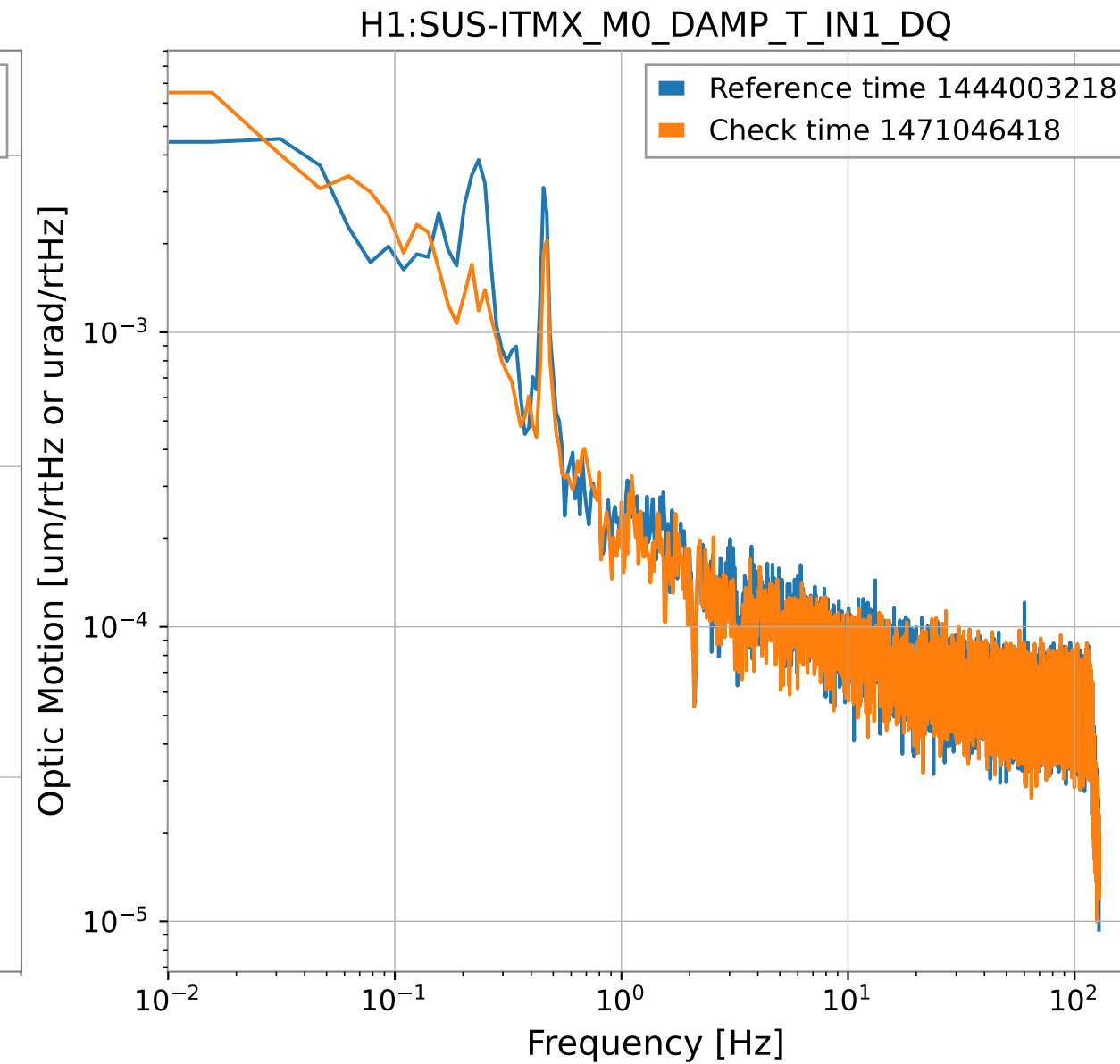
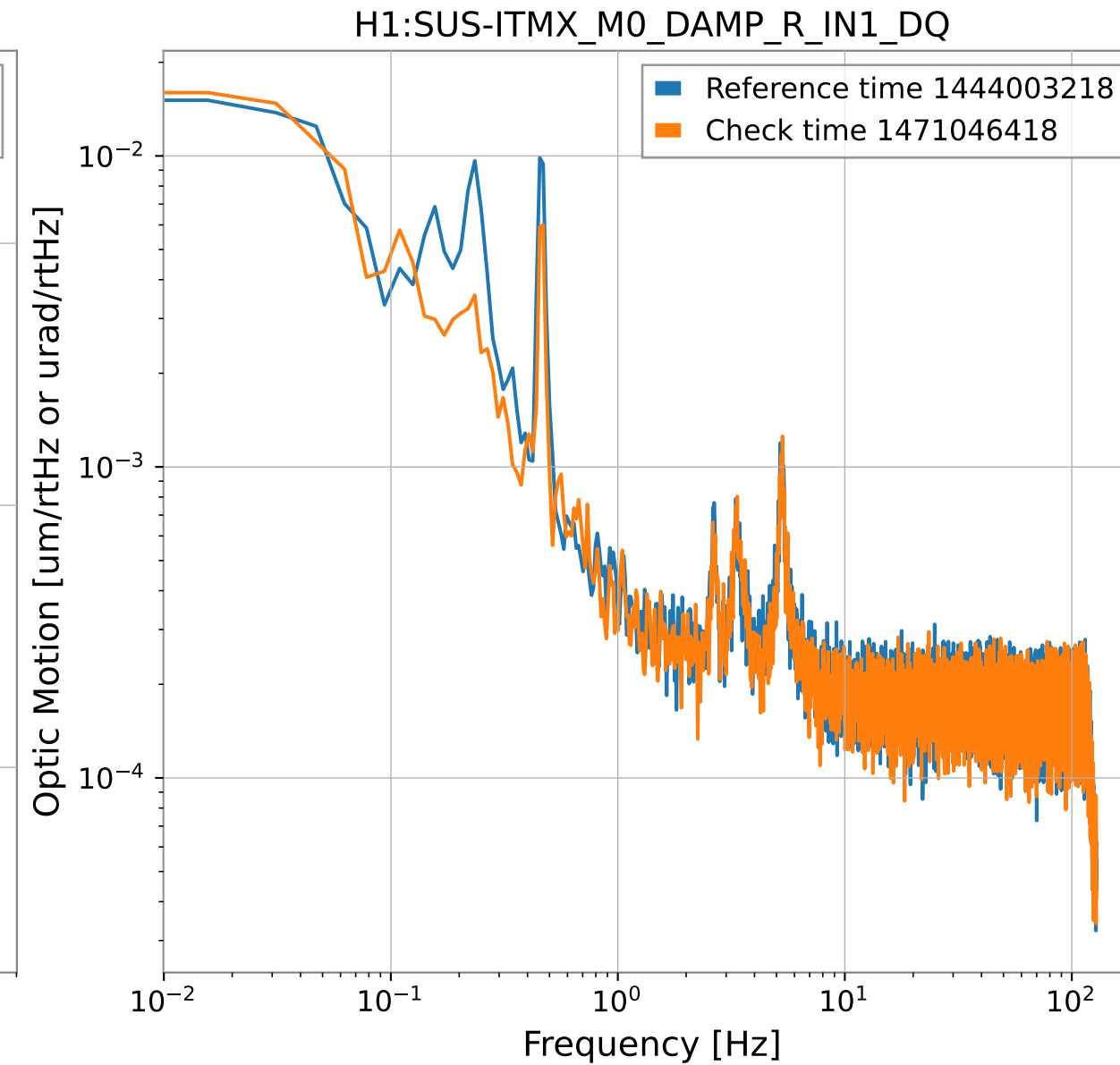
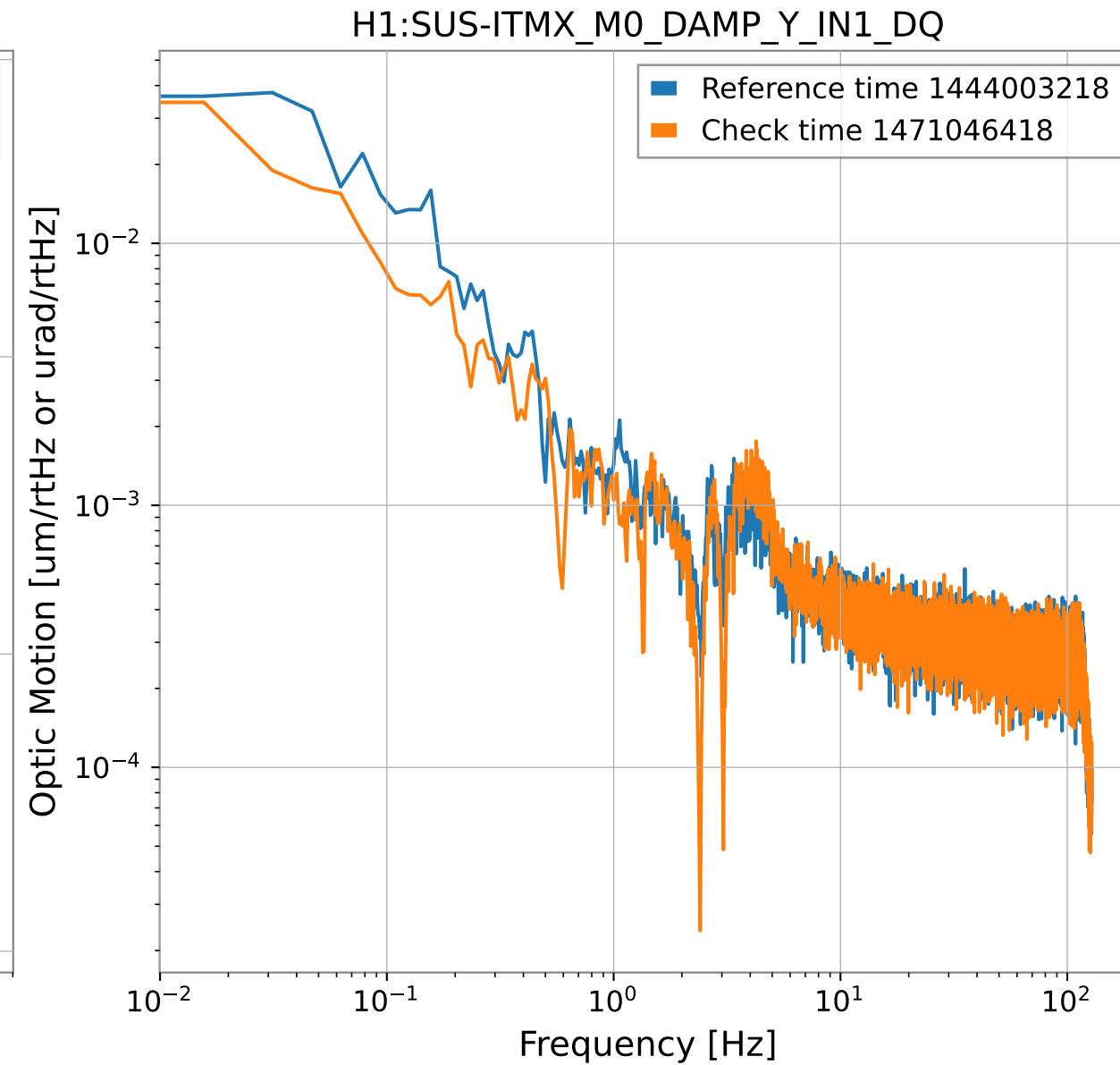
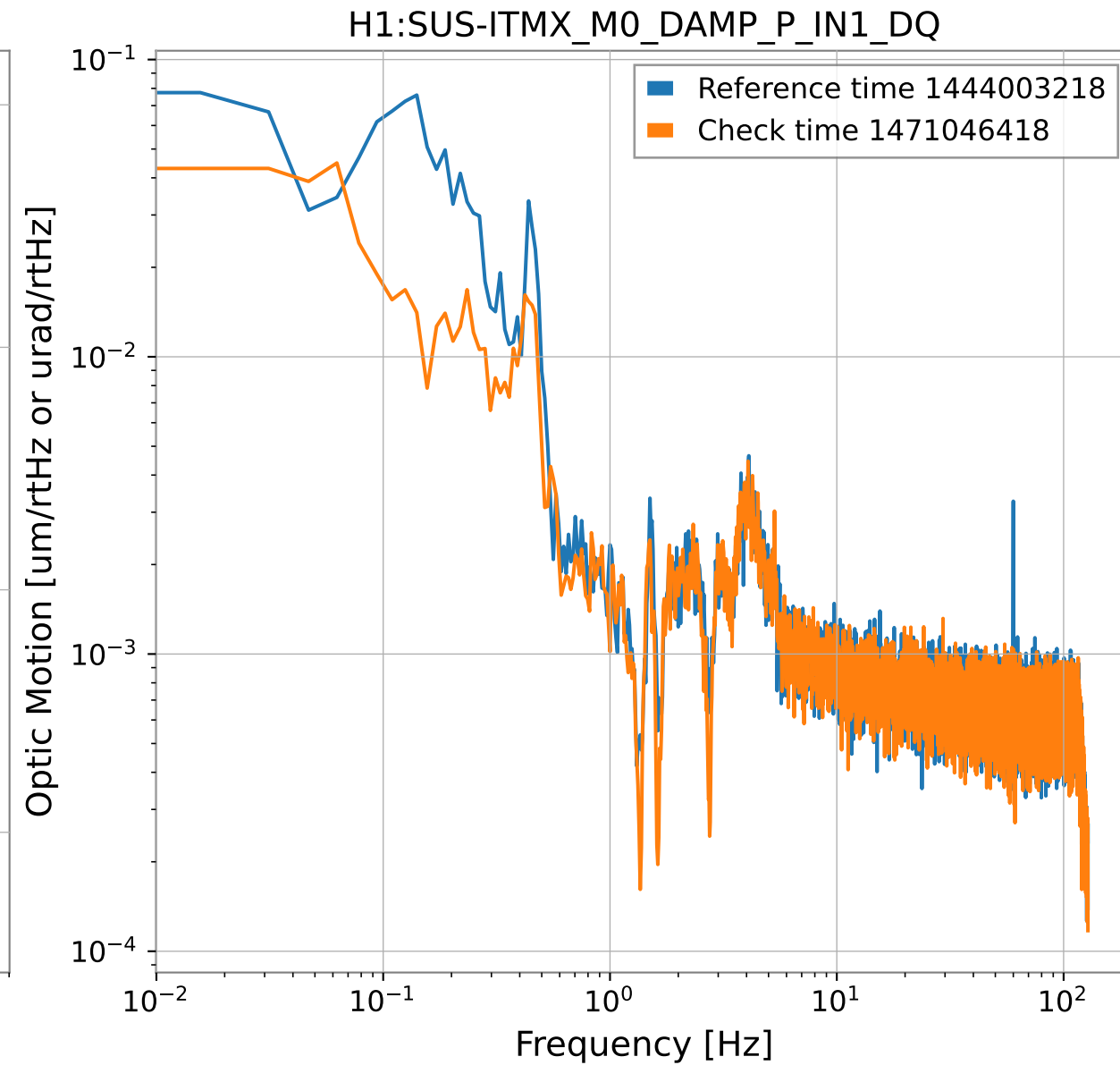
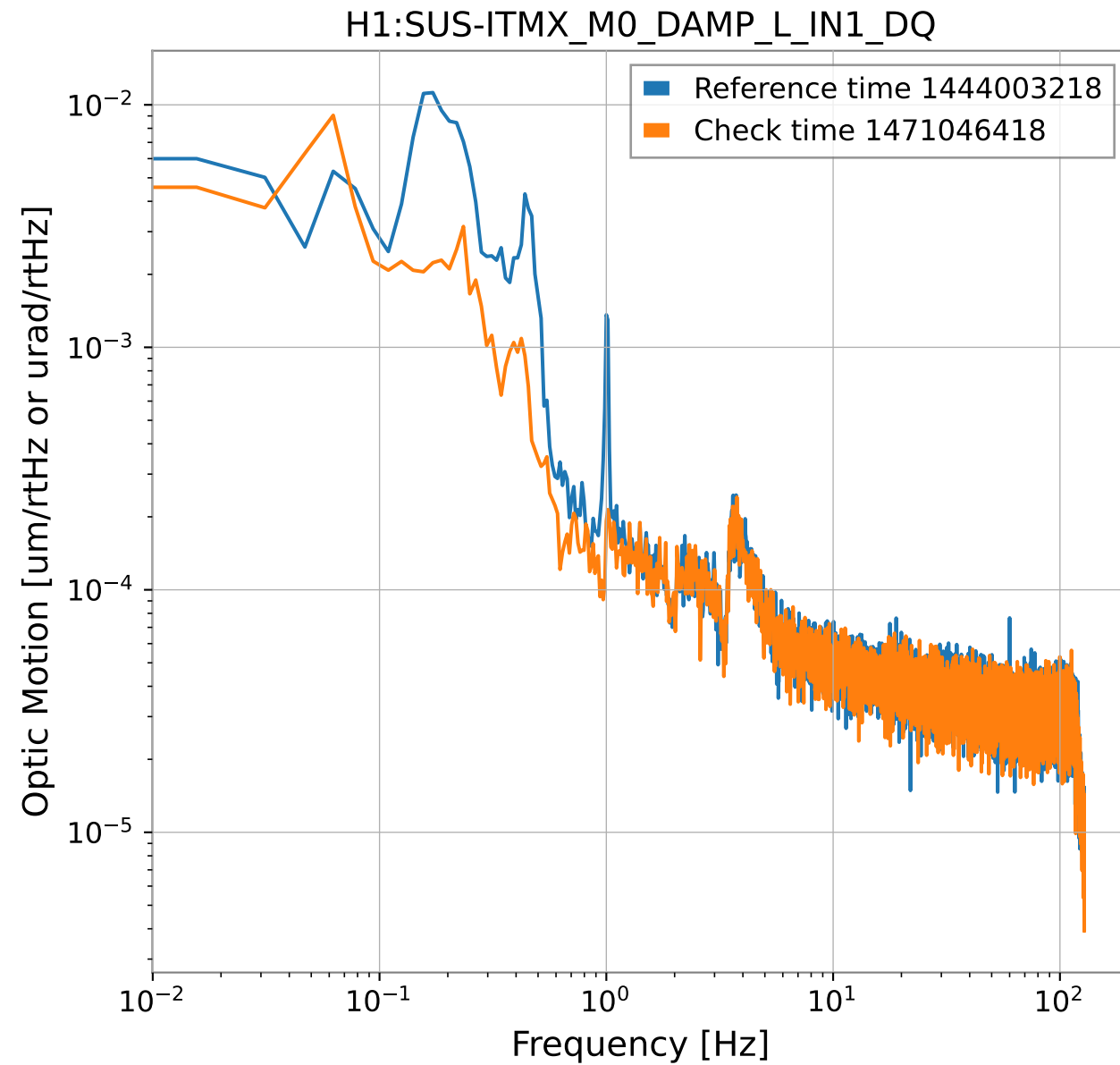
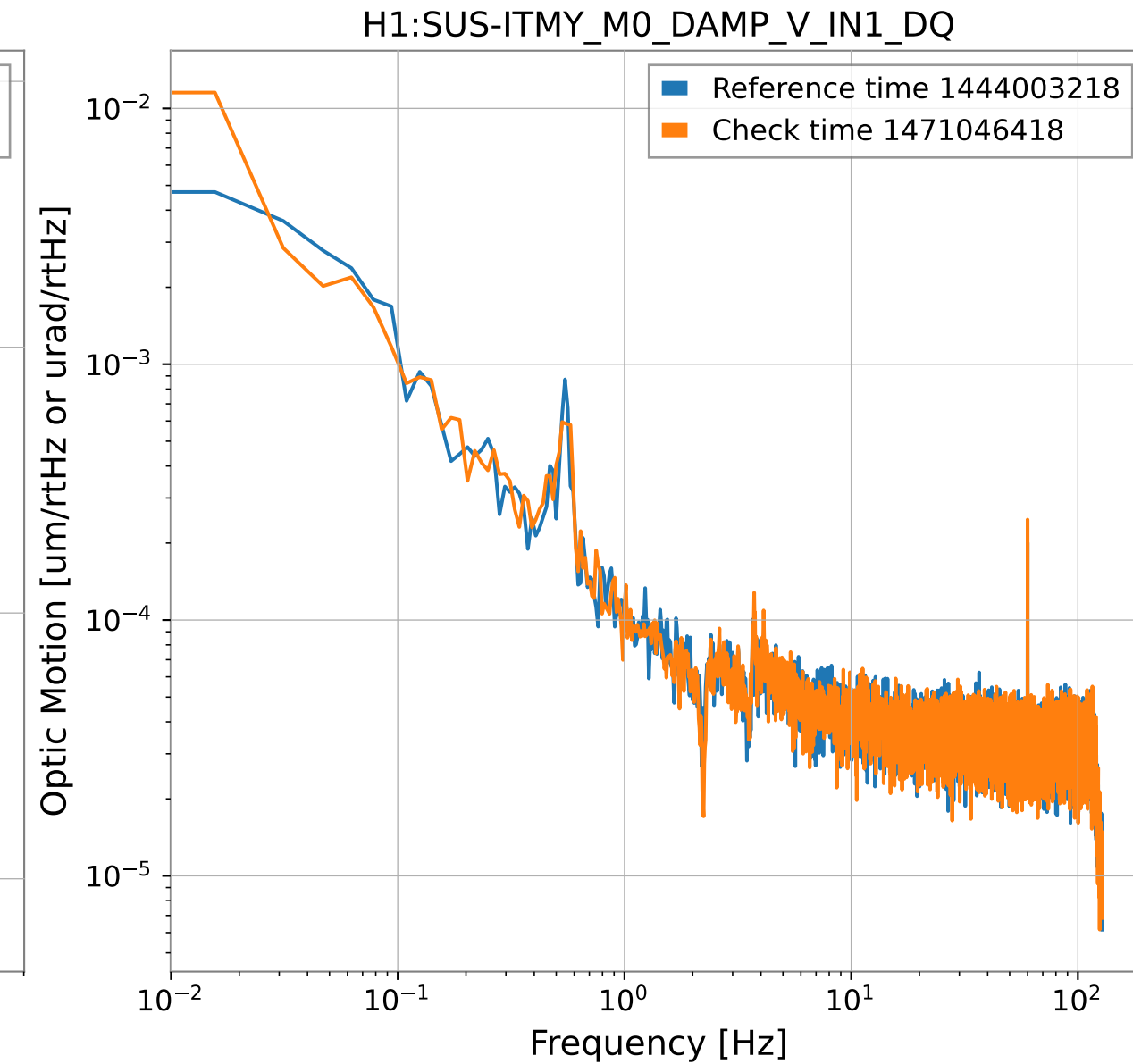
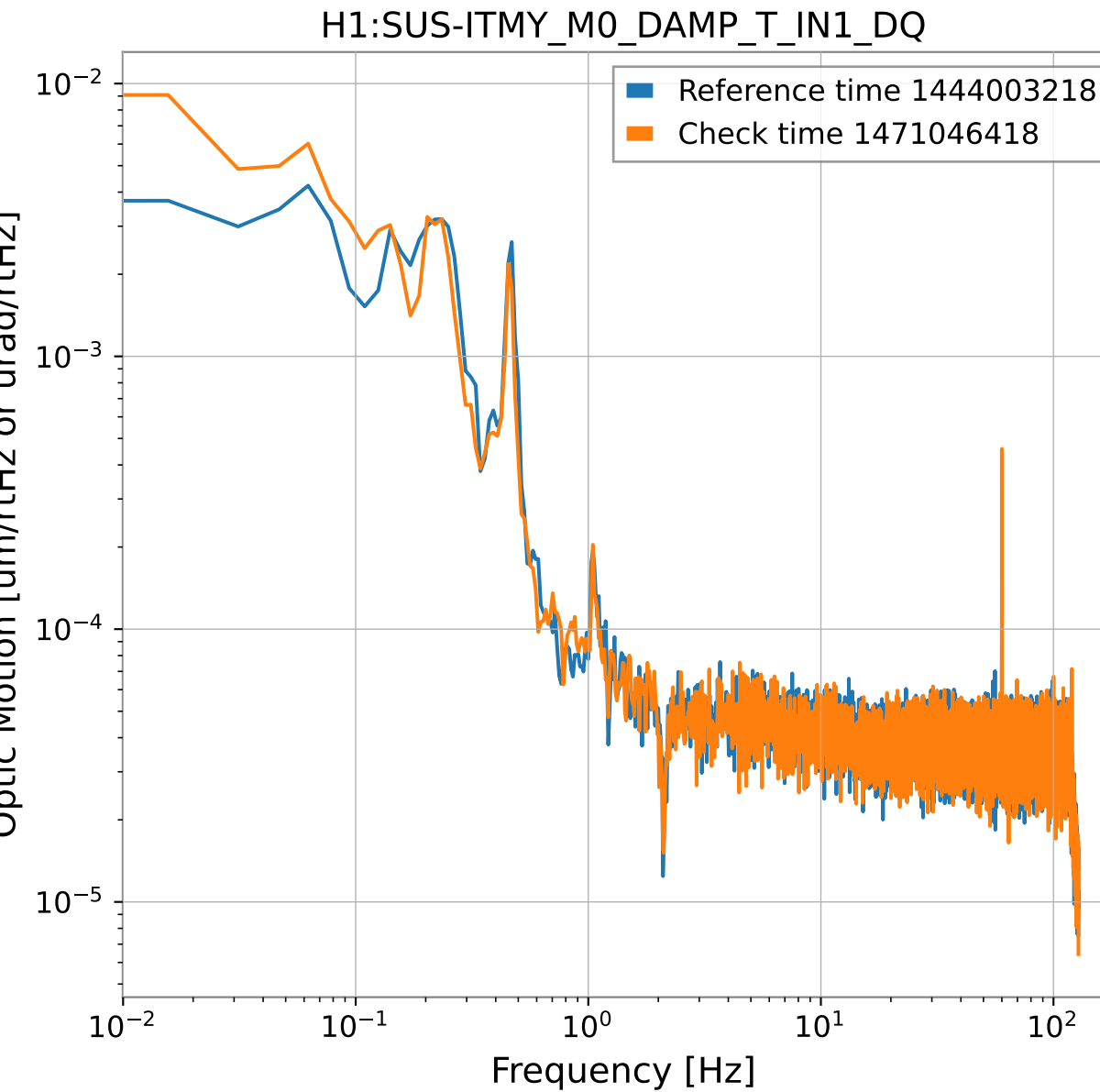
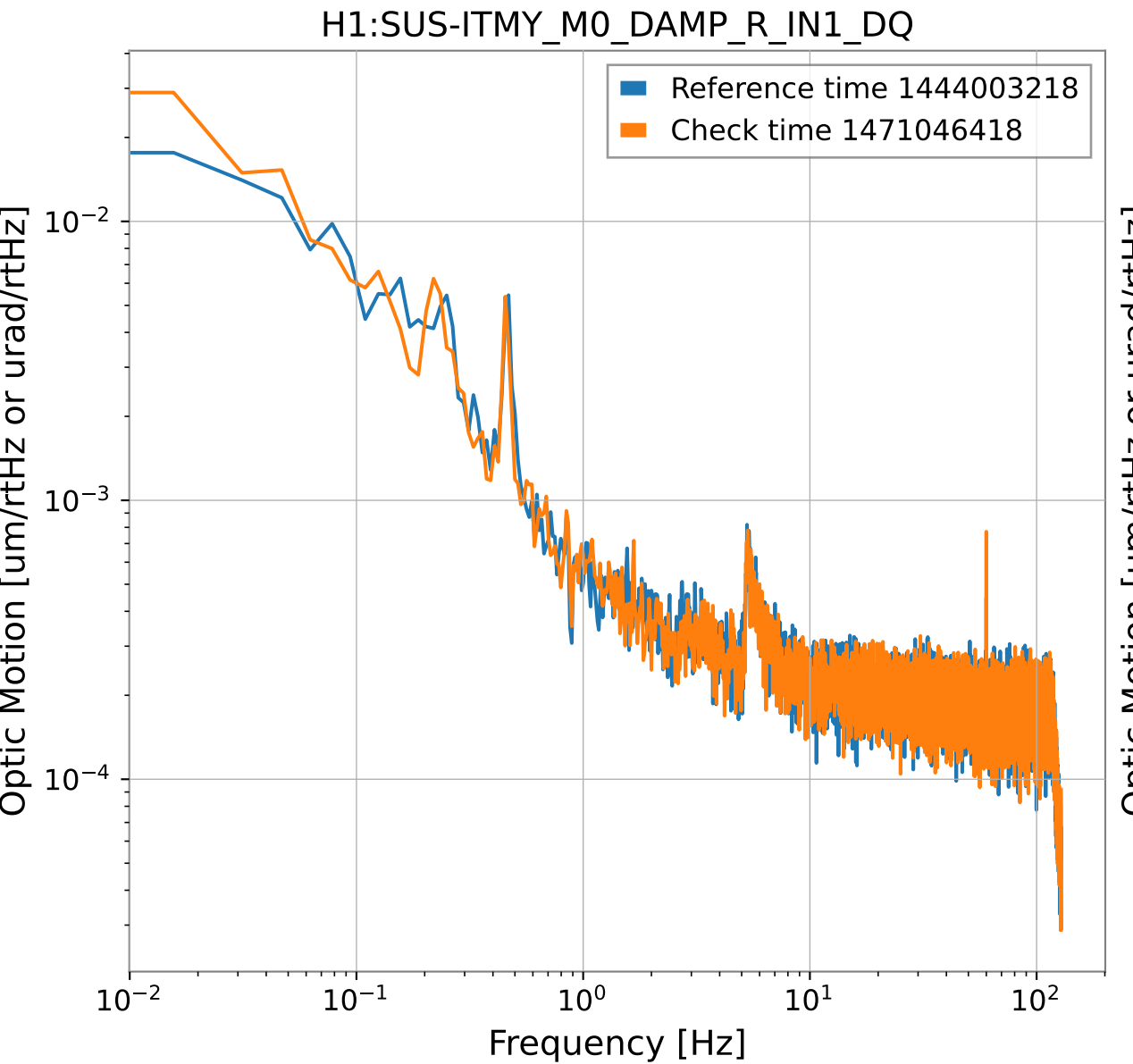
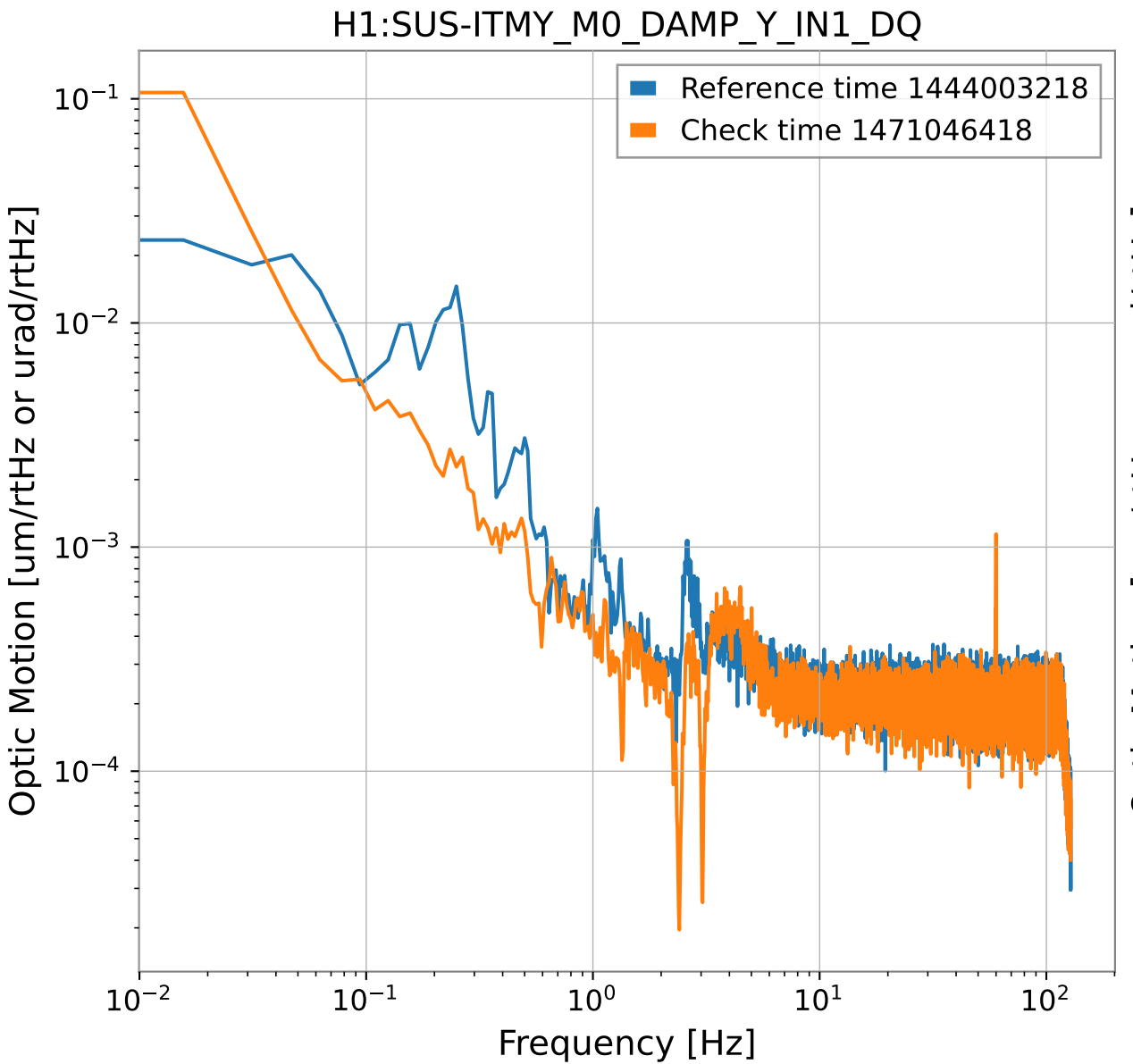
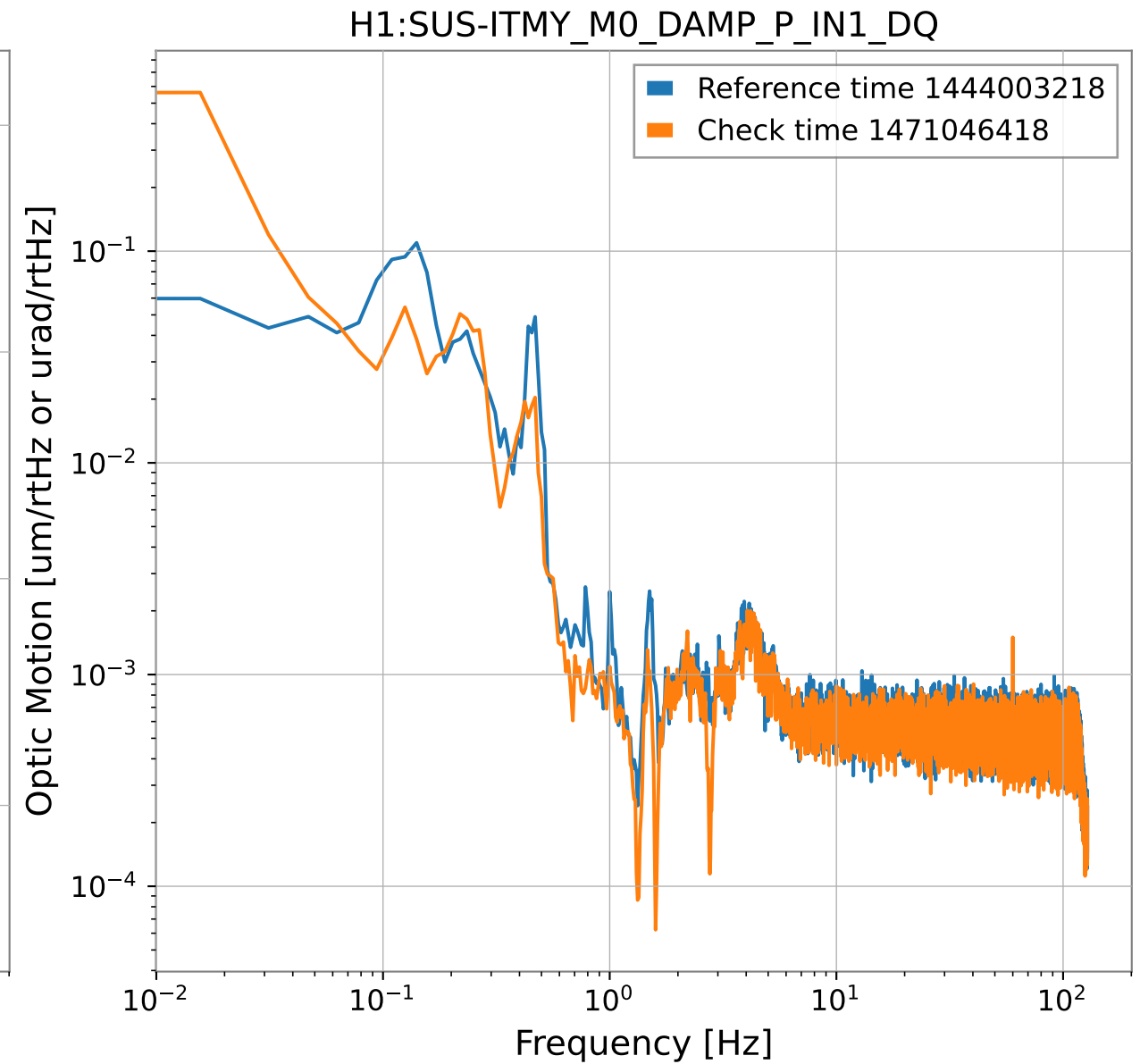
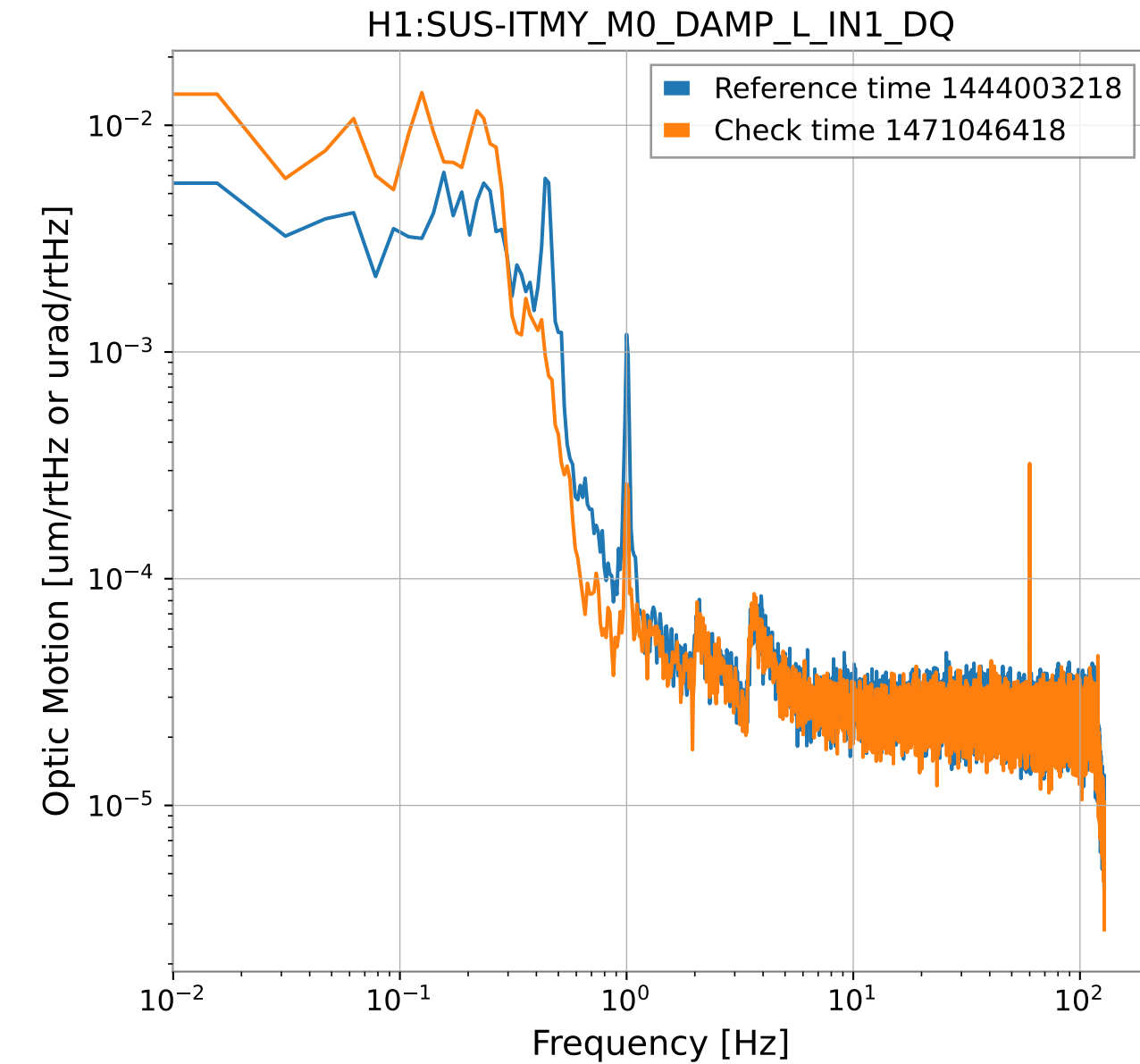


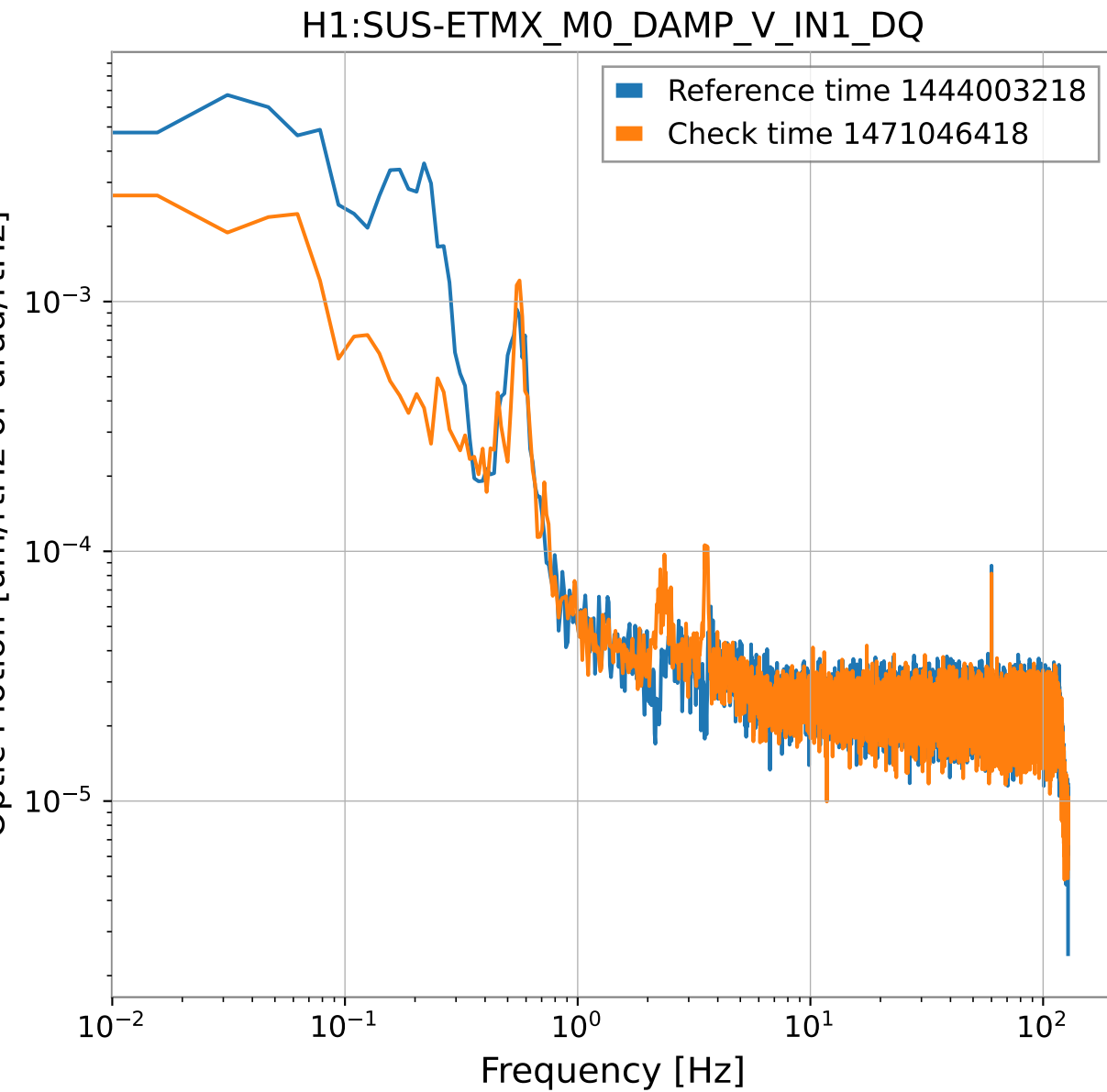
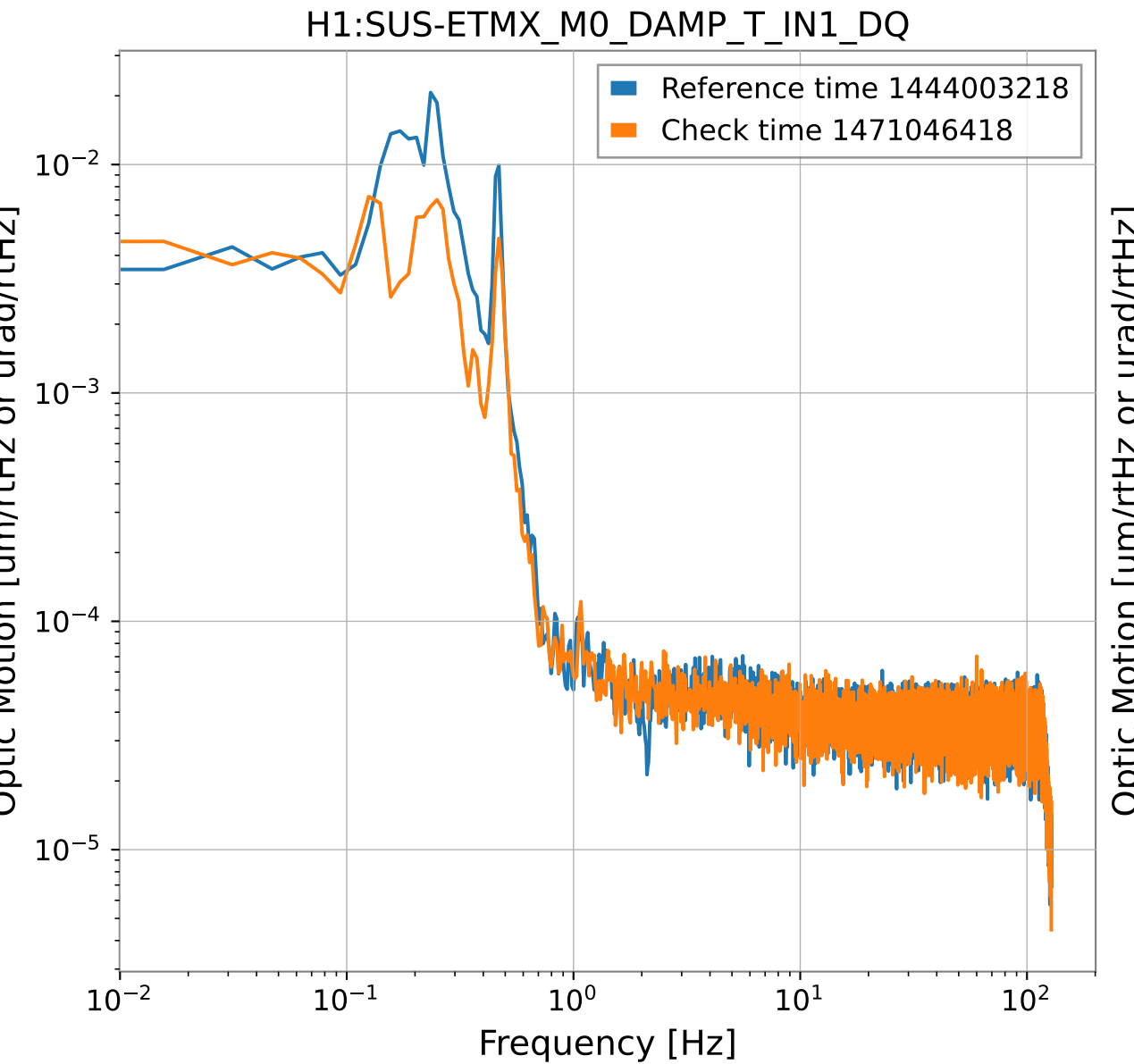
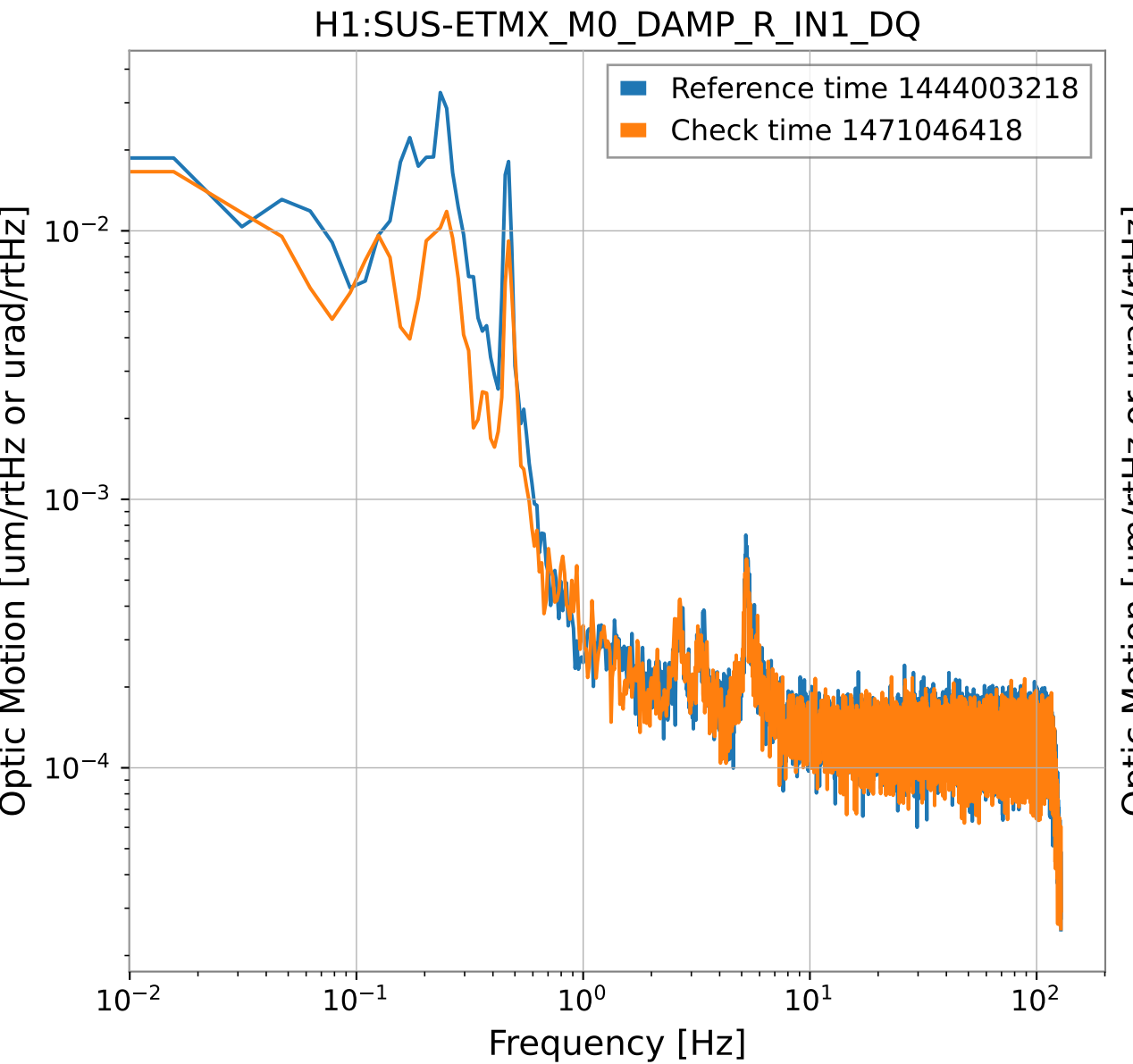
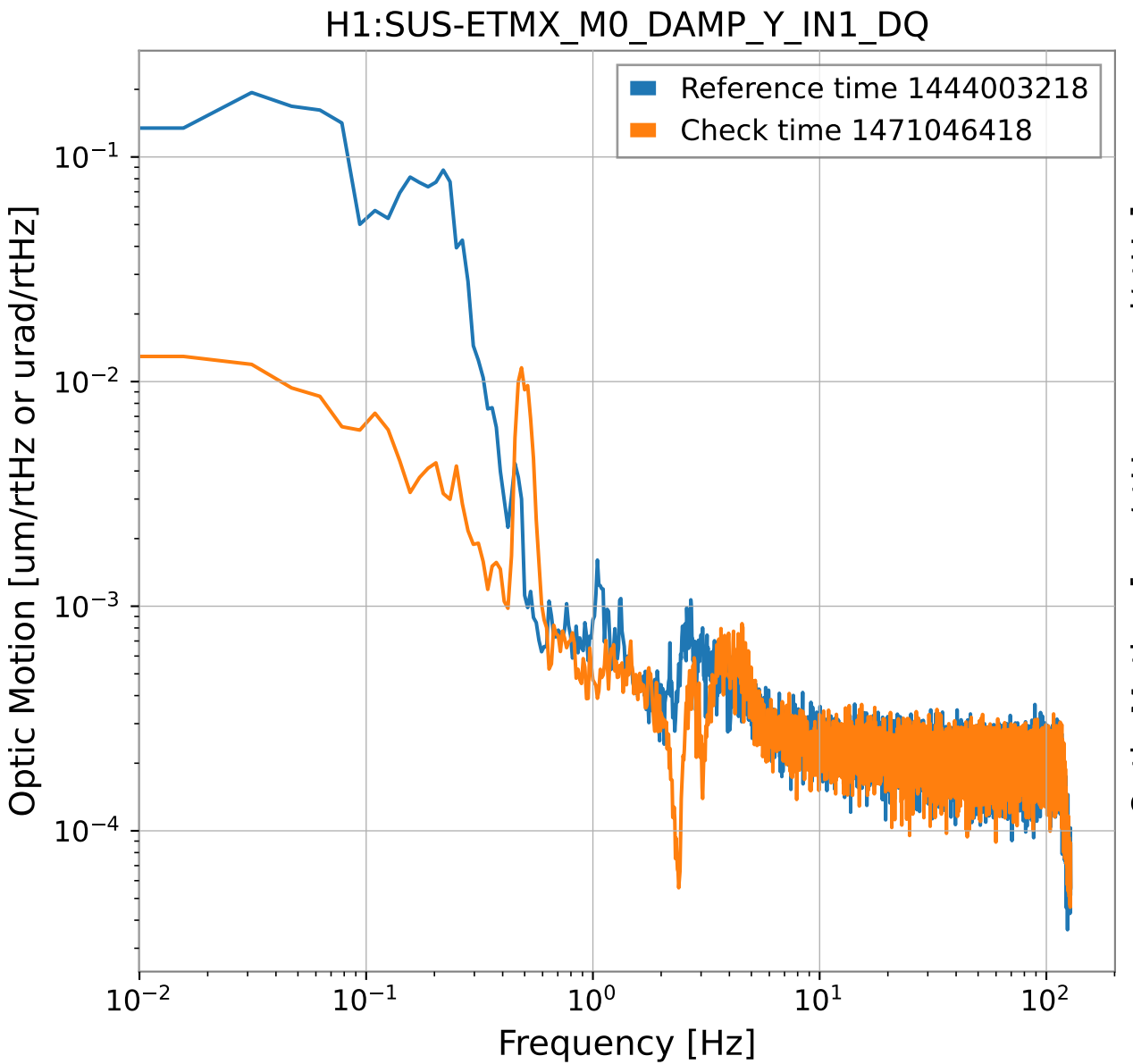
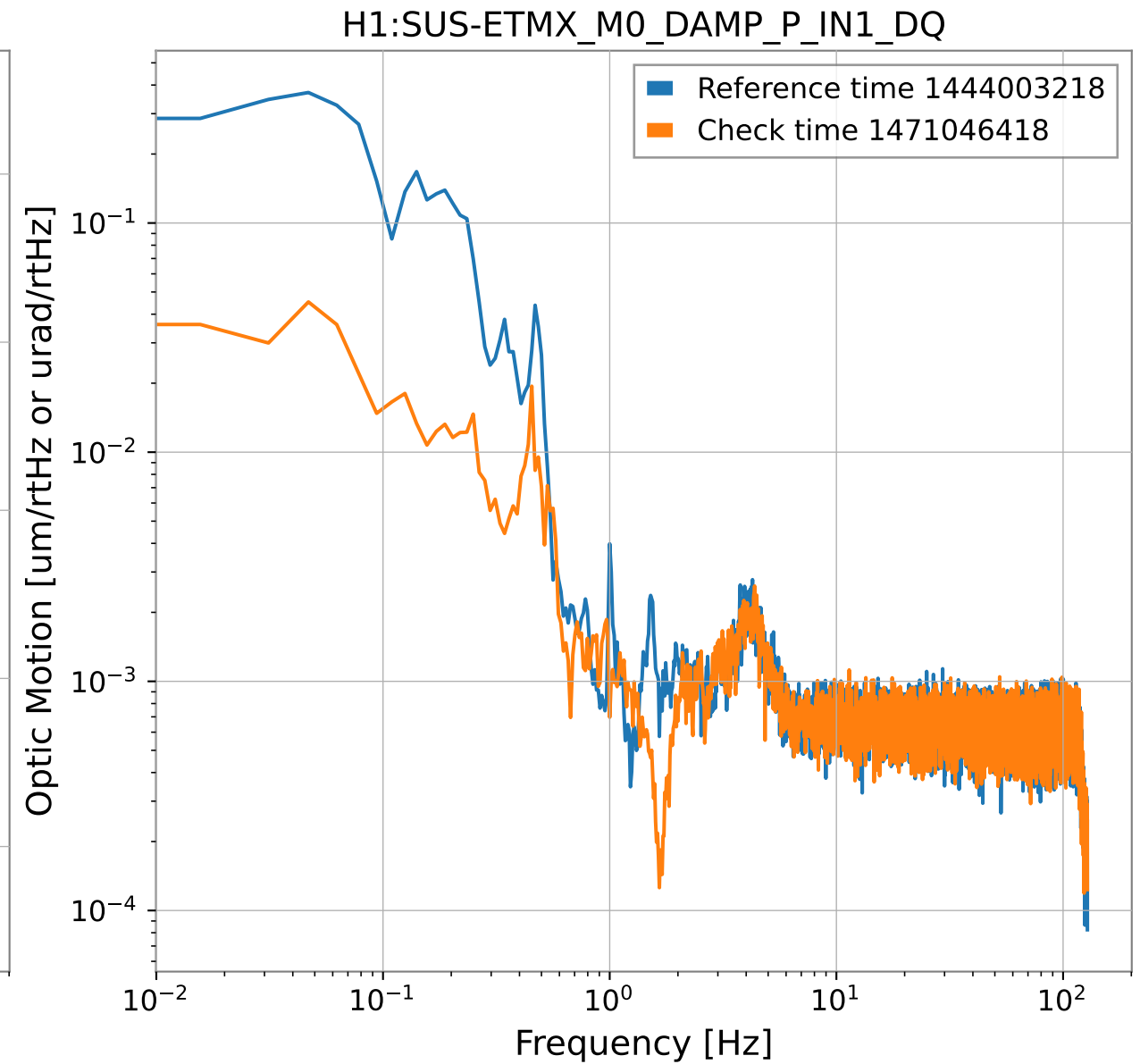
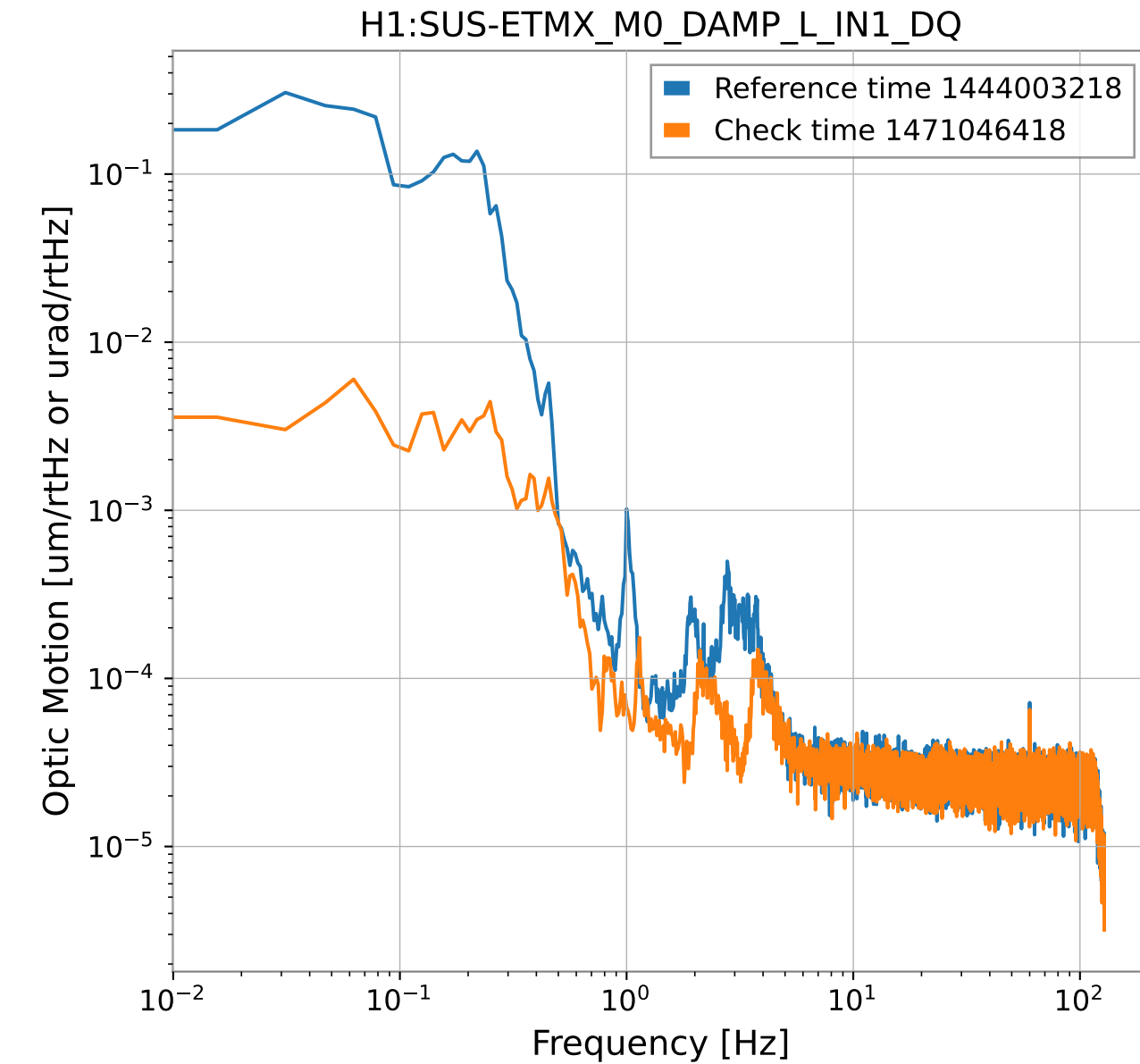
ITMX



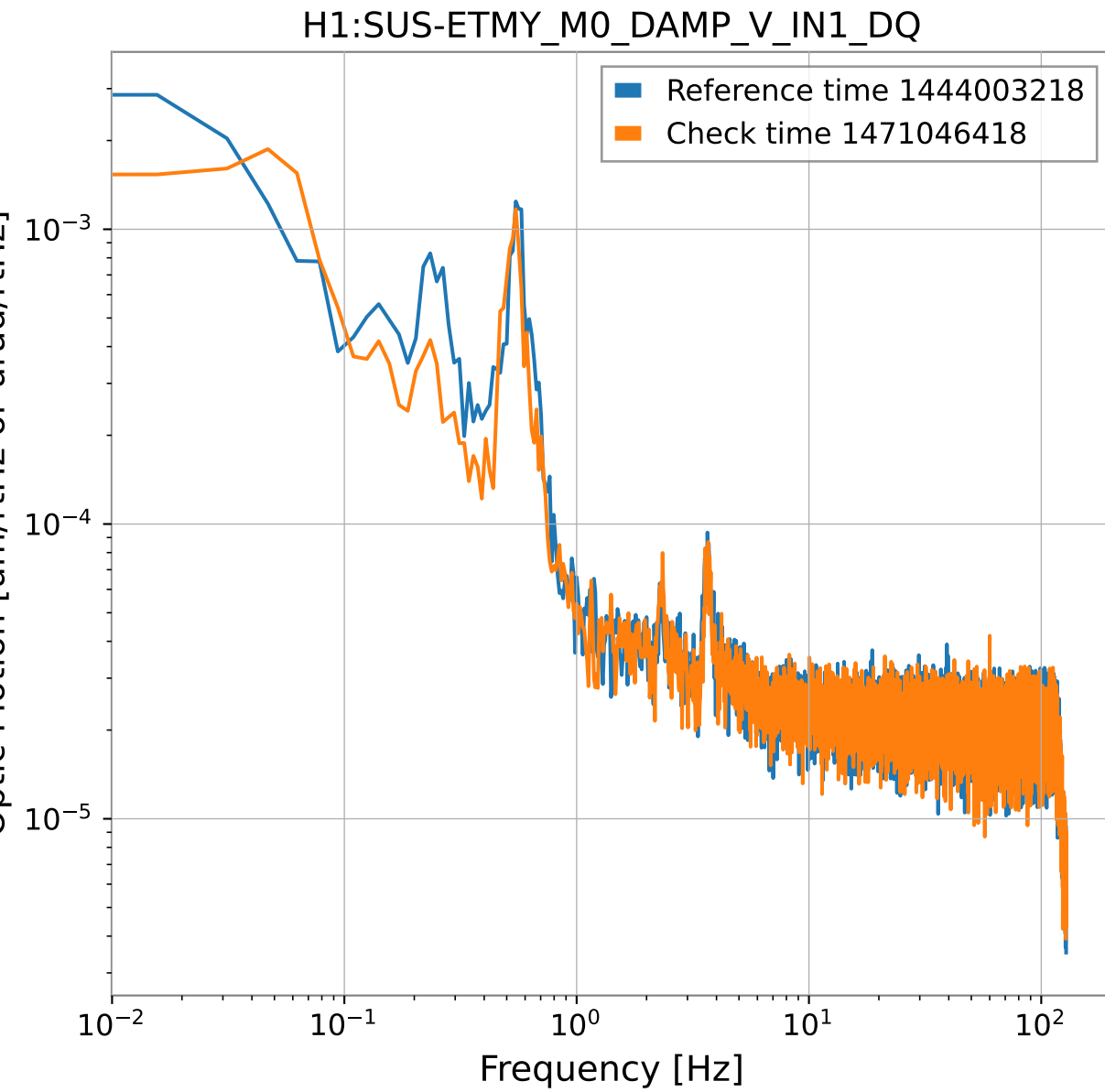
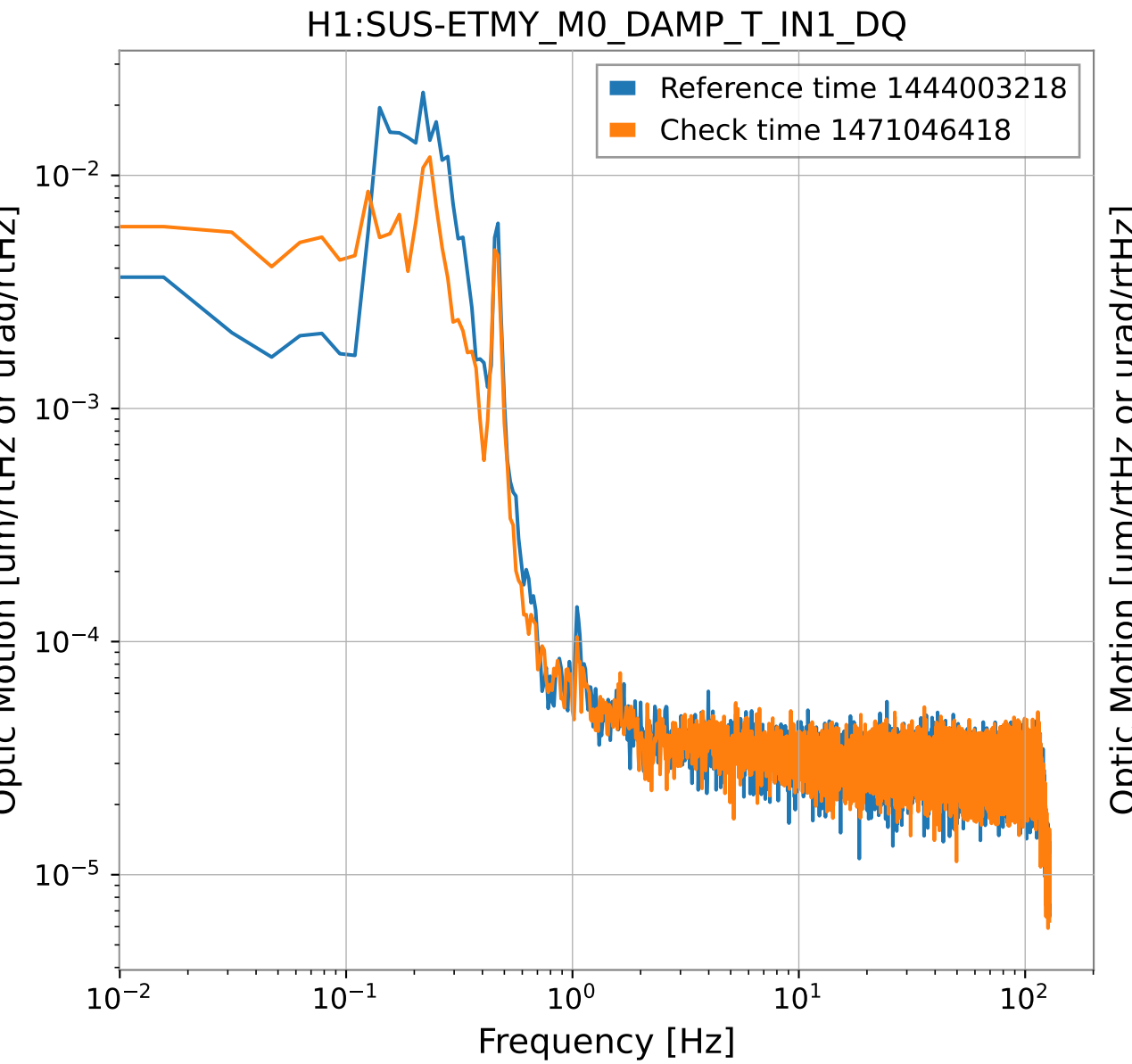
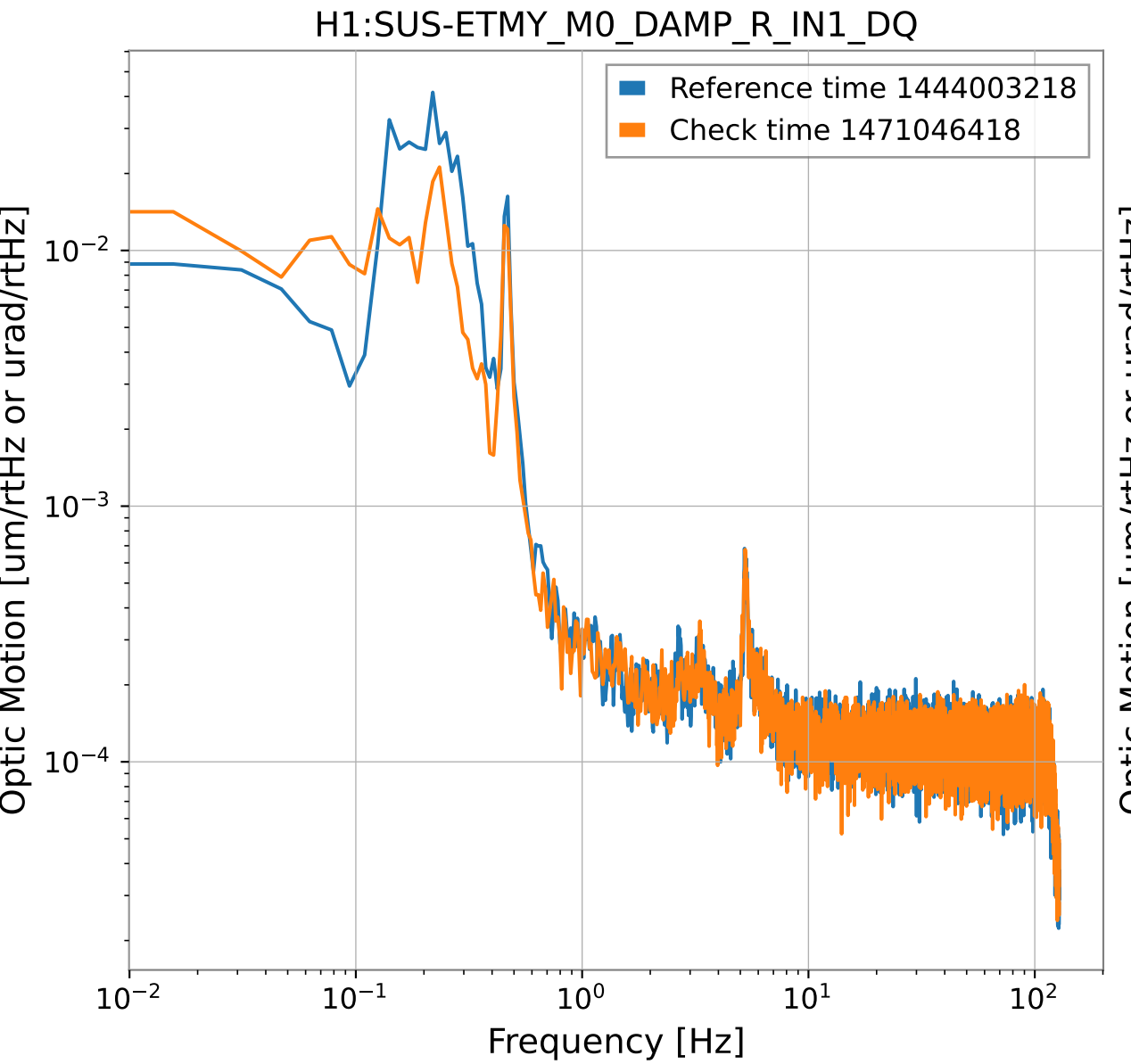
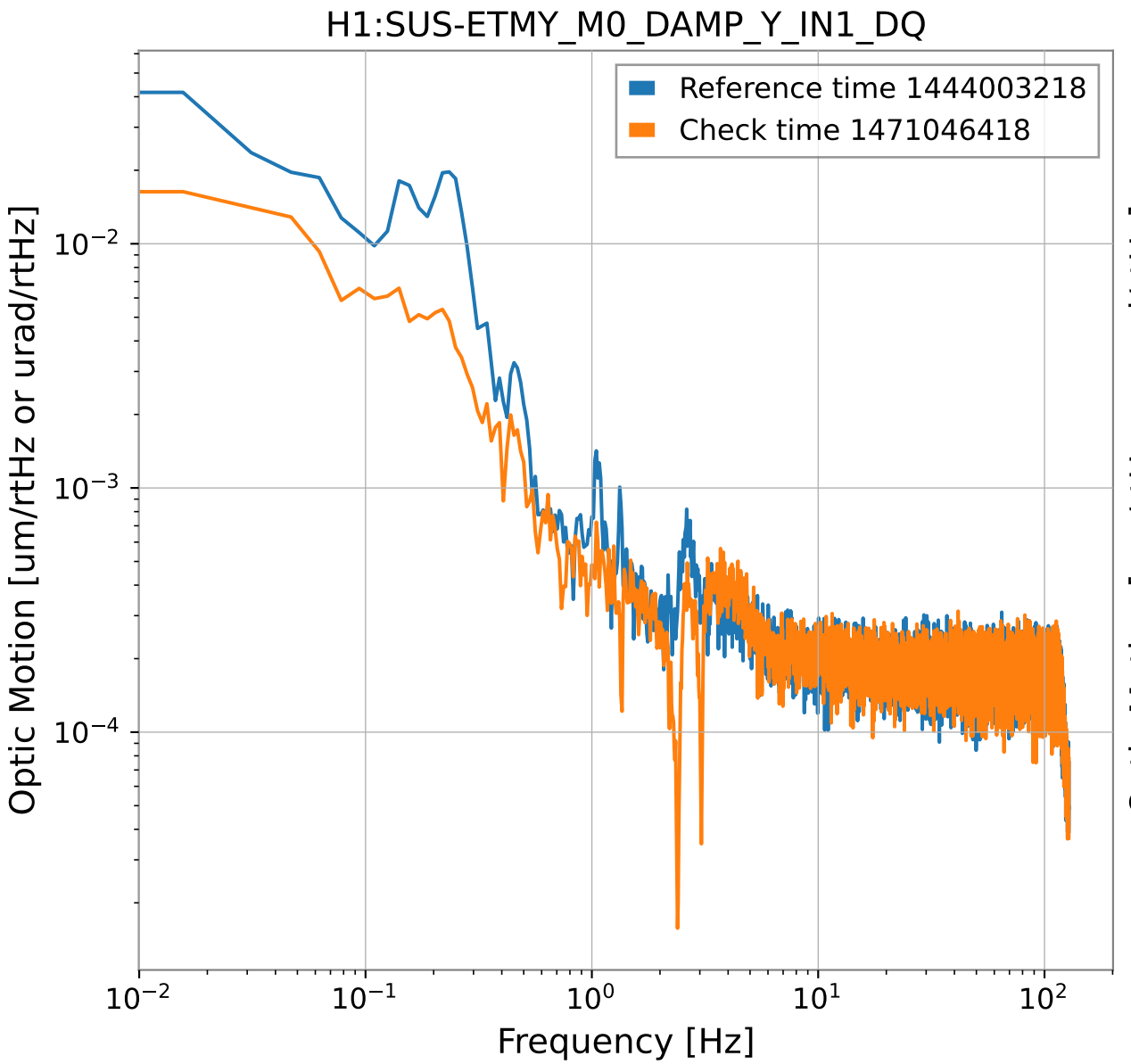
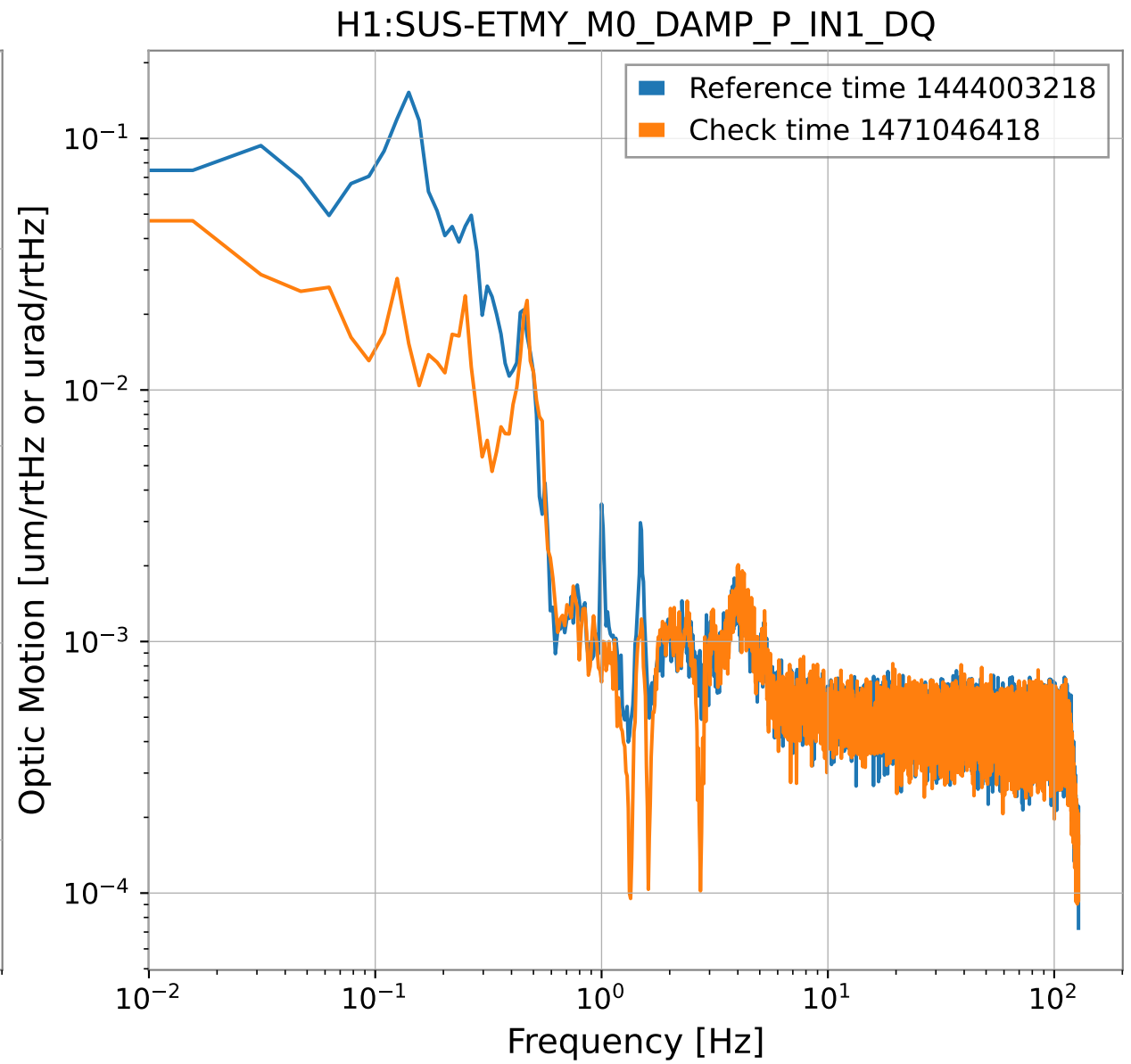
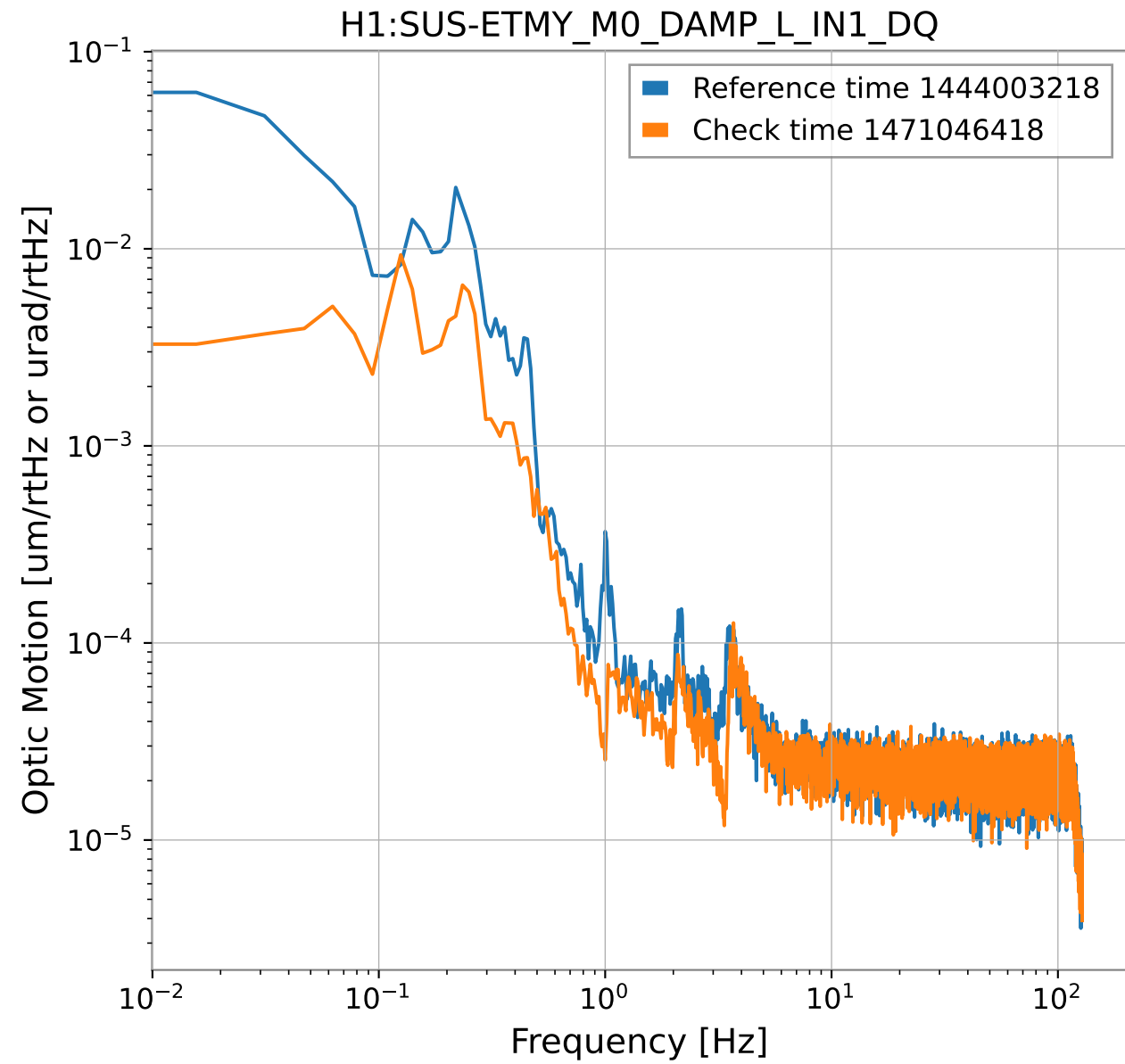
ITMY



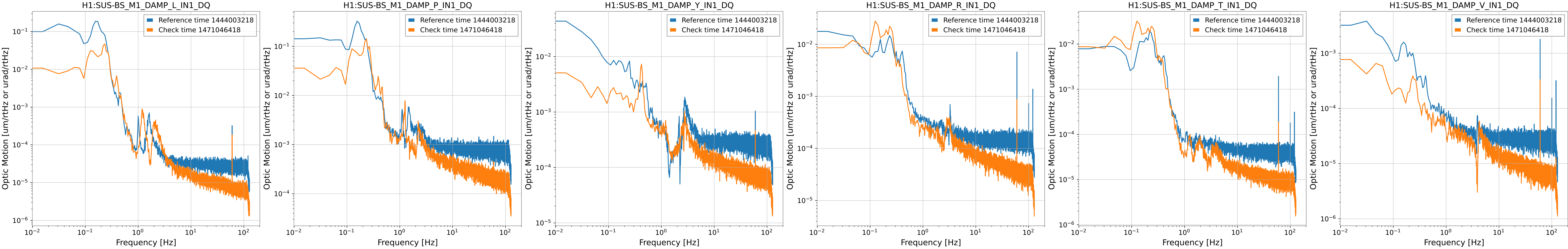
ETMX



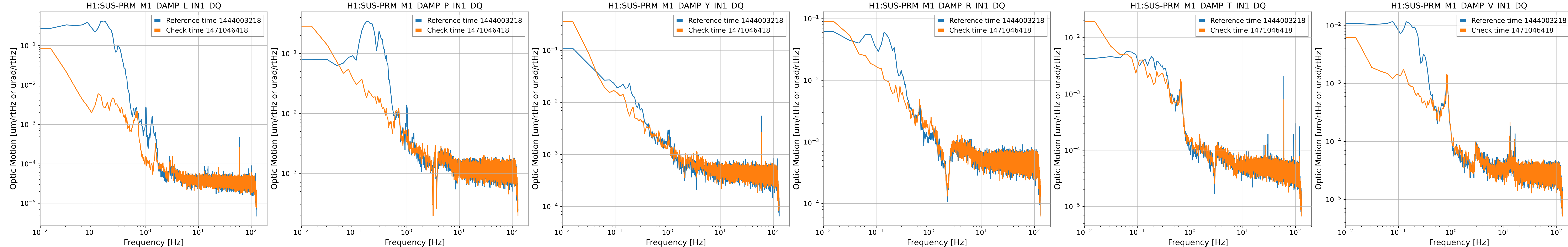
ETMY



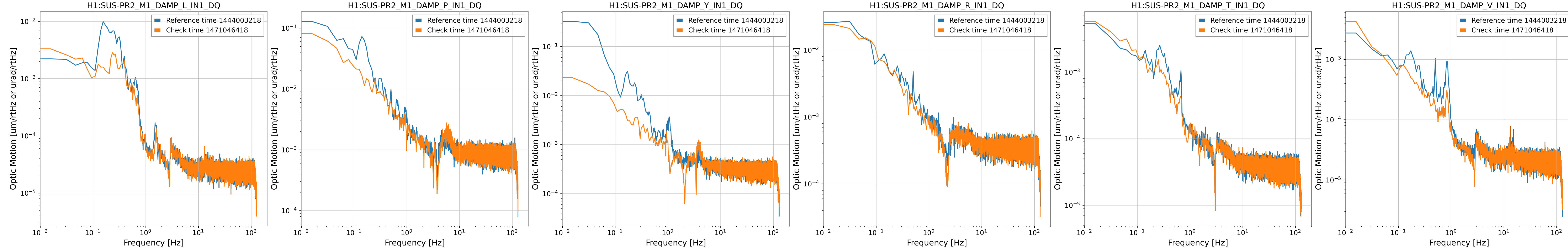
BS



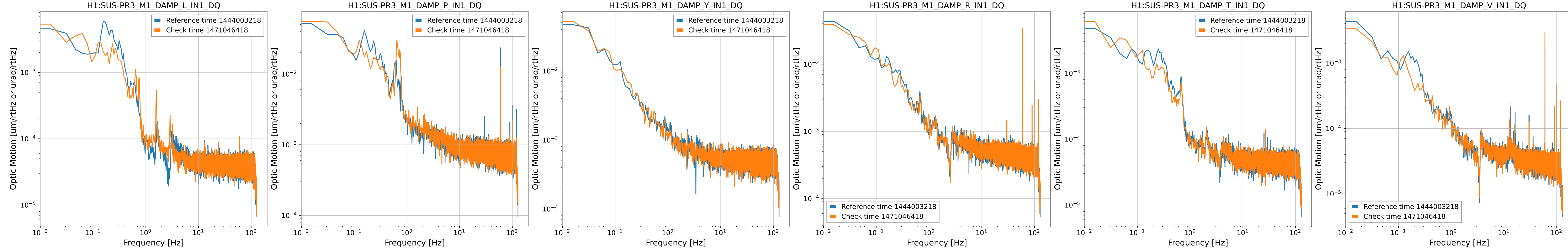
PRM



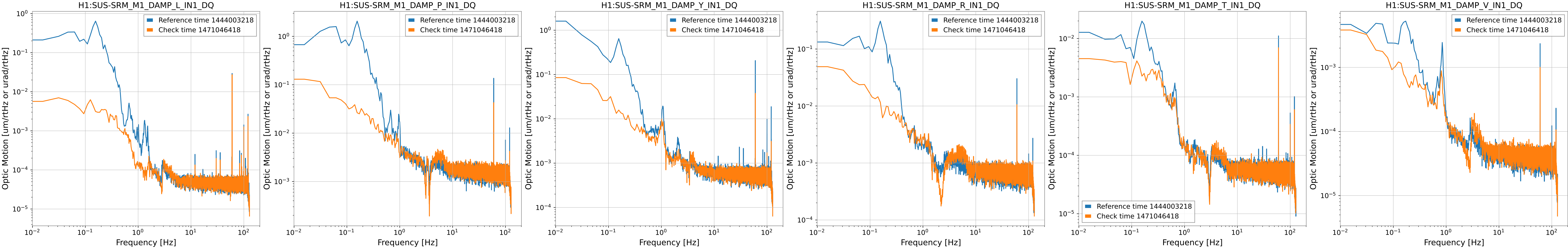
PR2



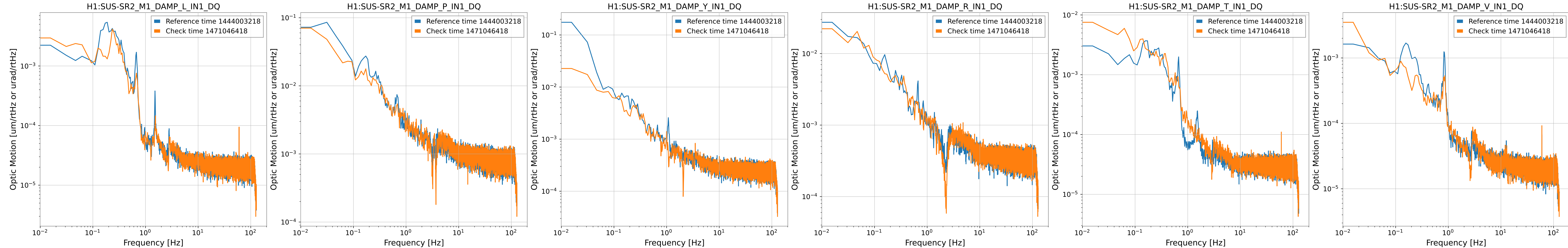
PR3



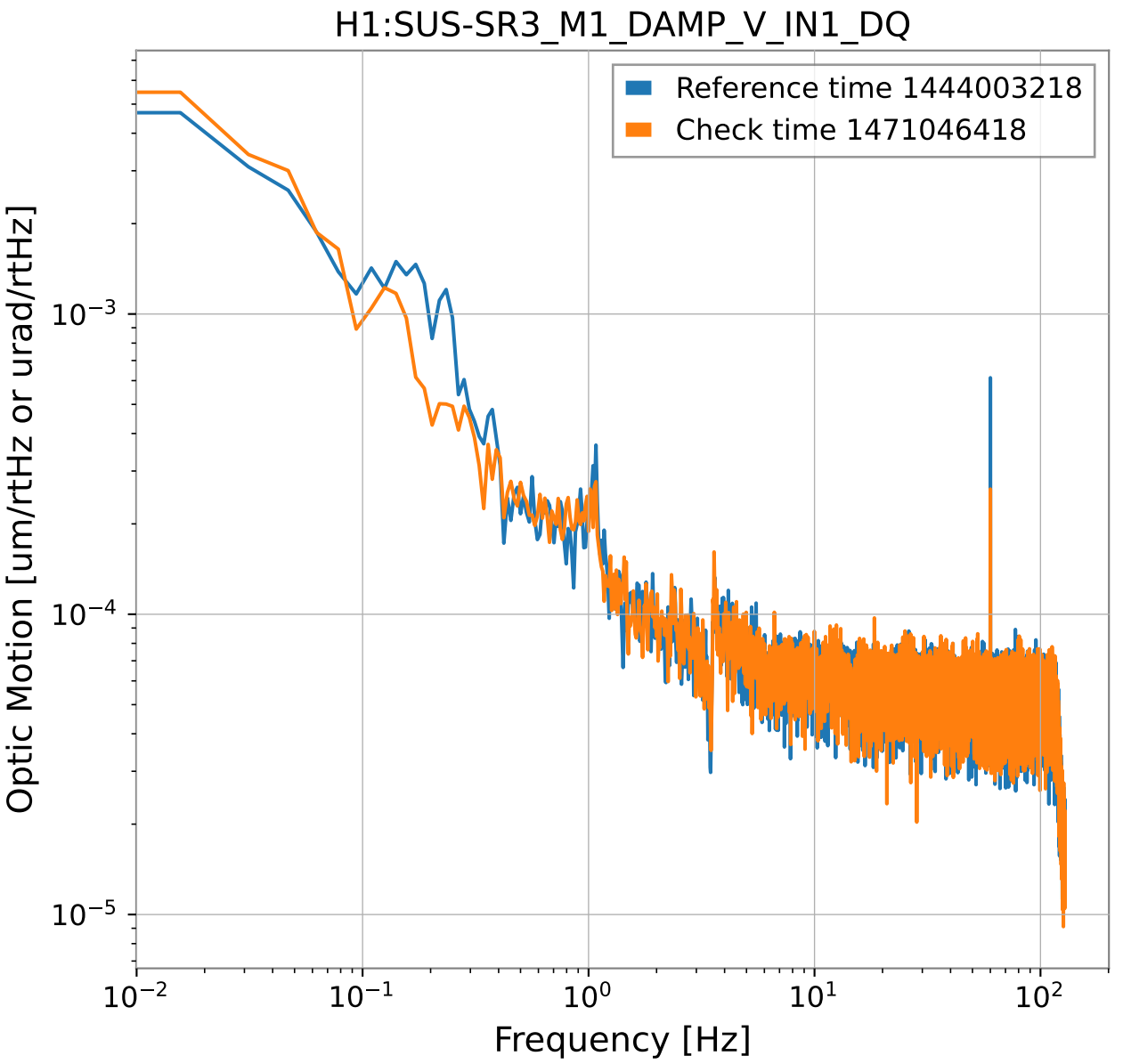
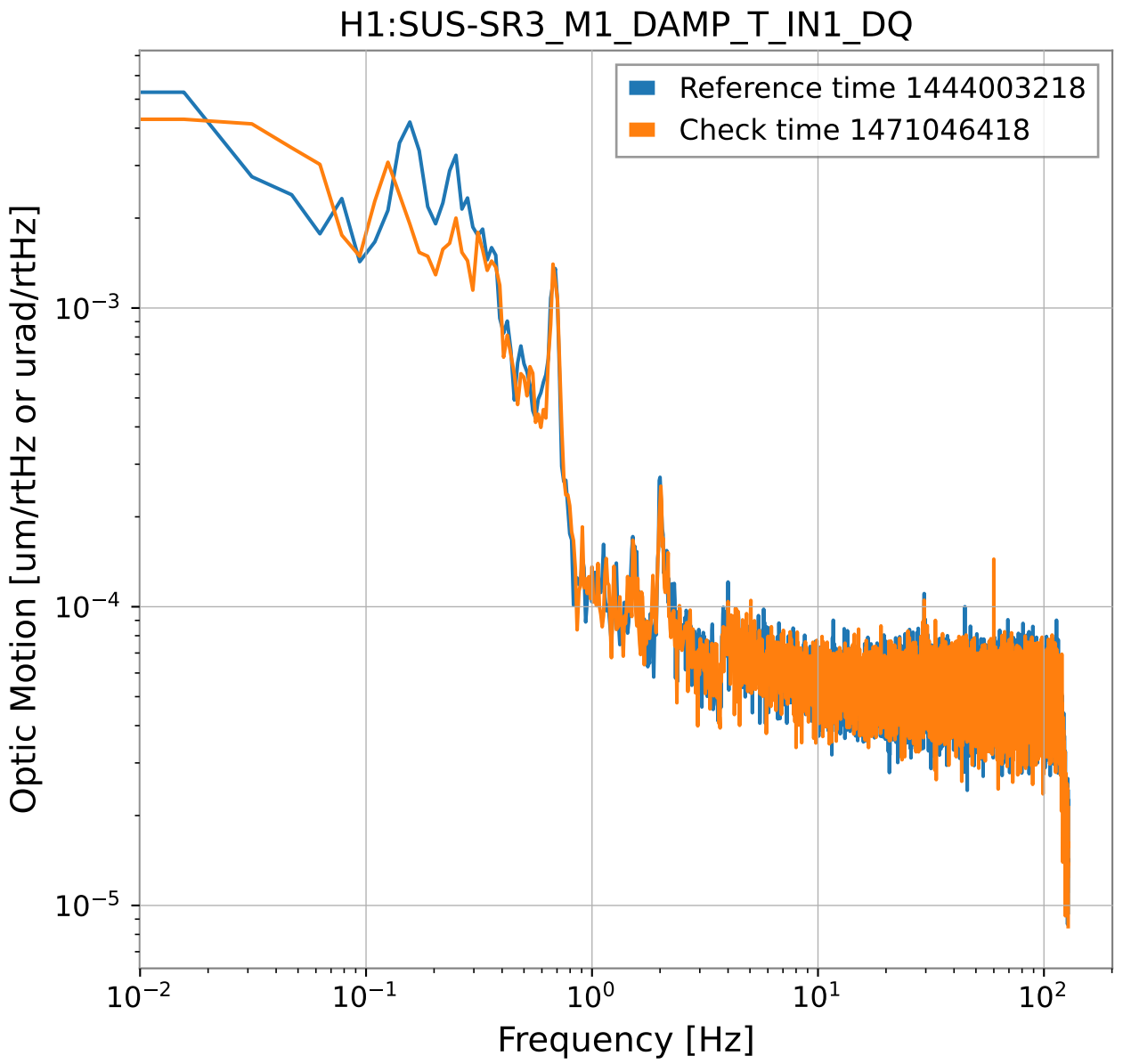
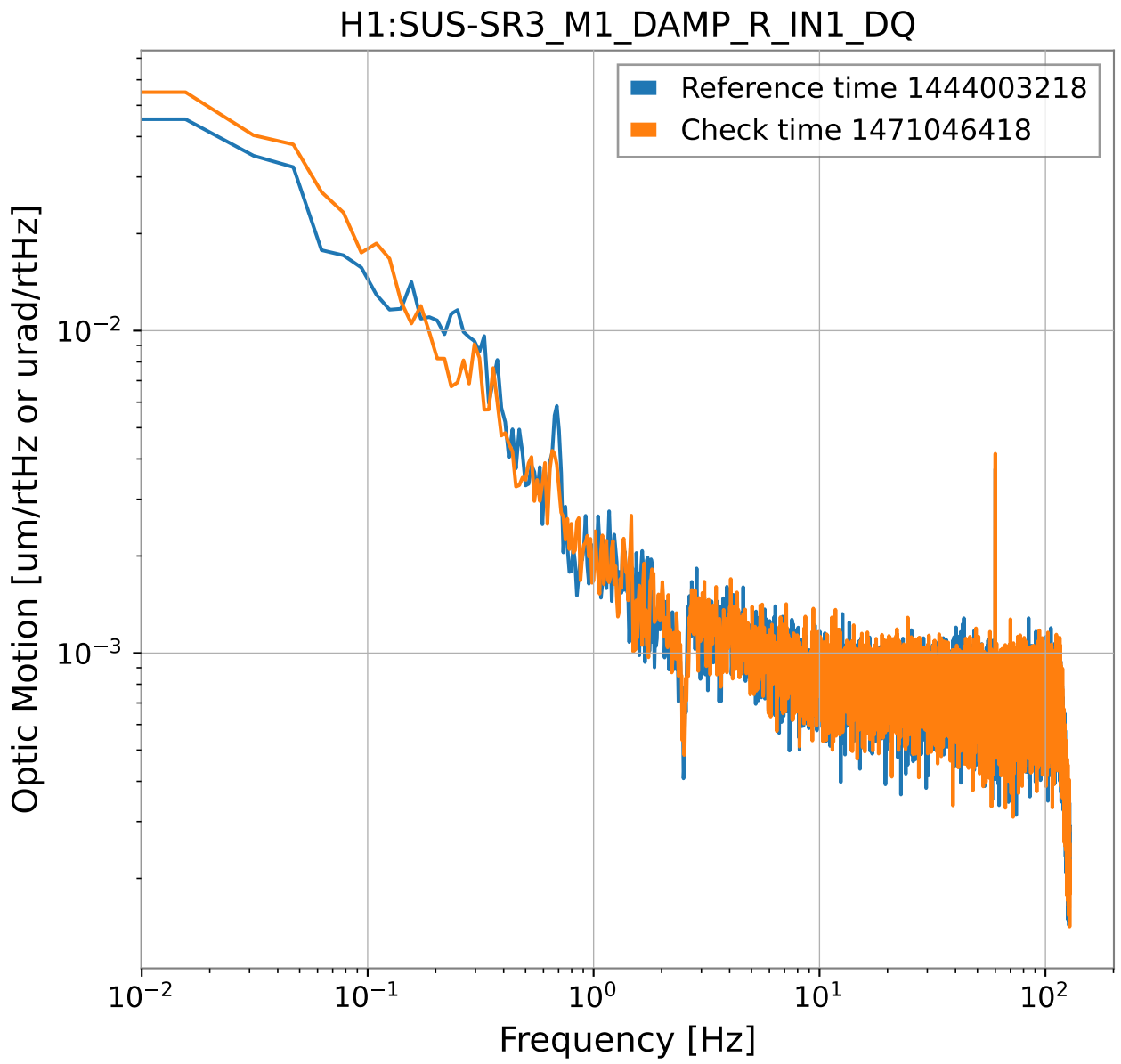
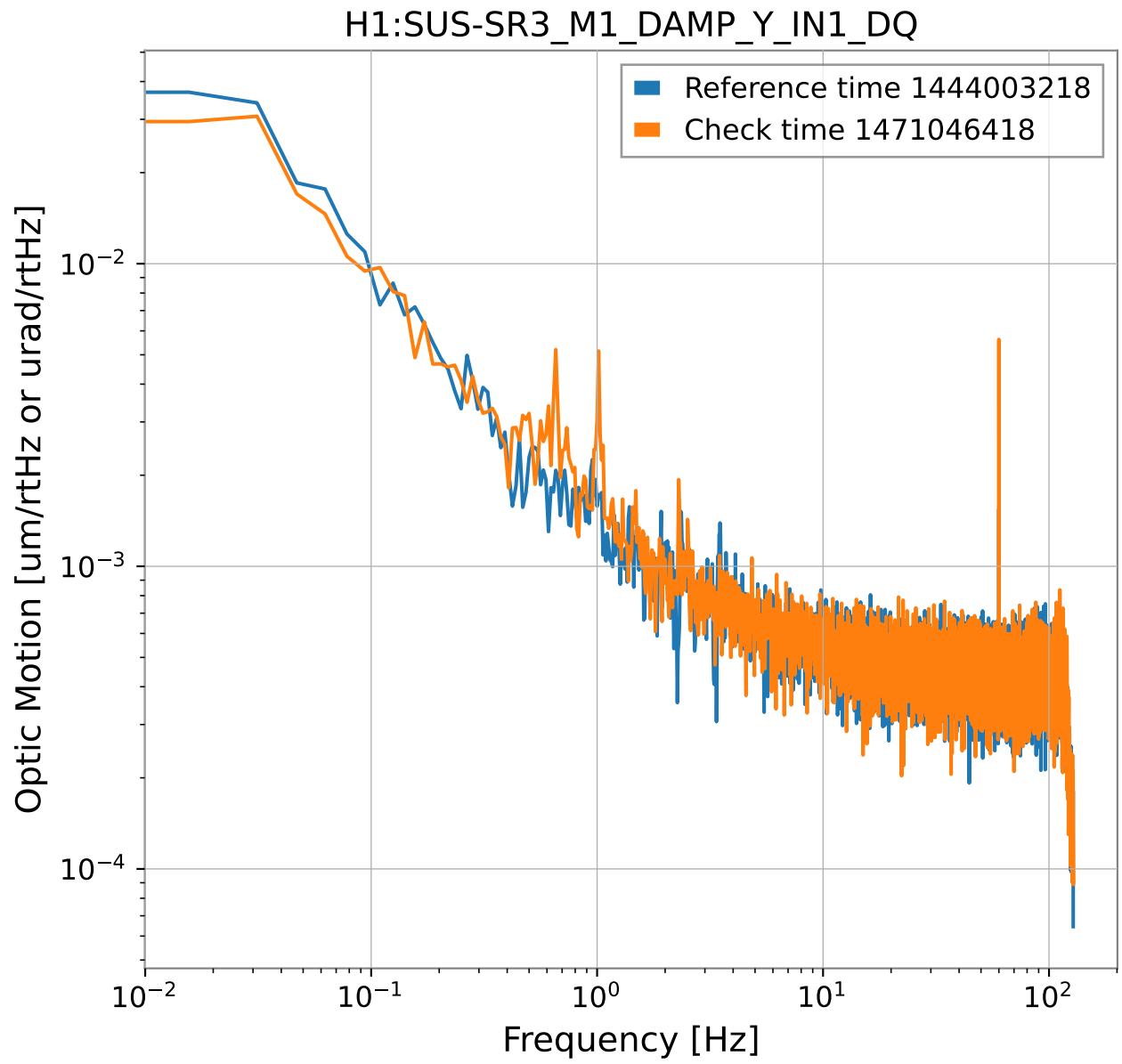
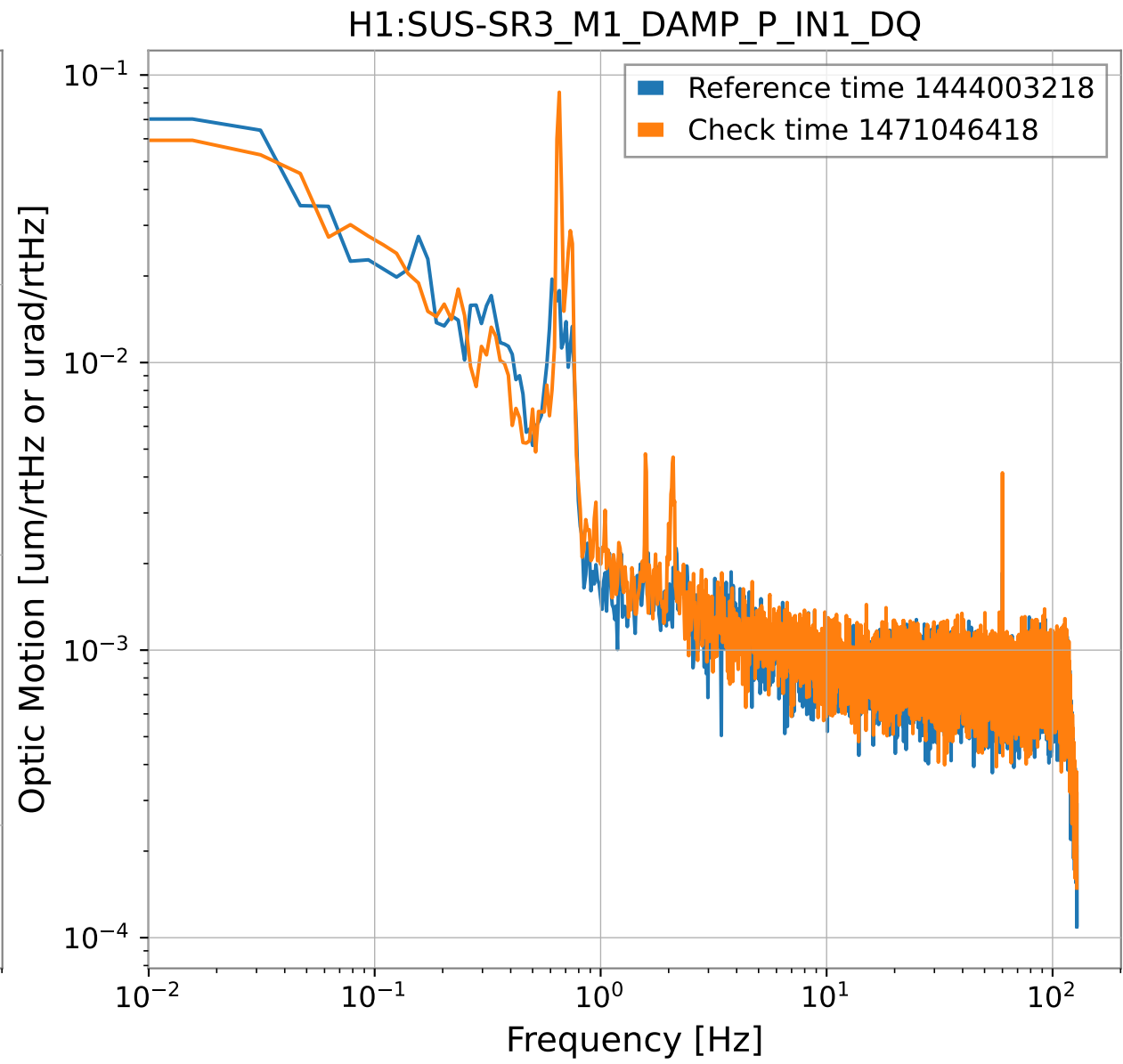
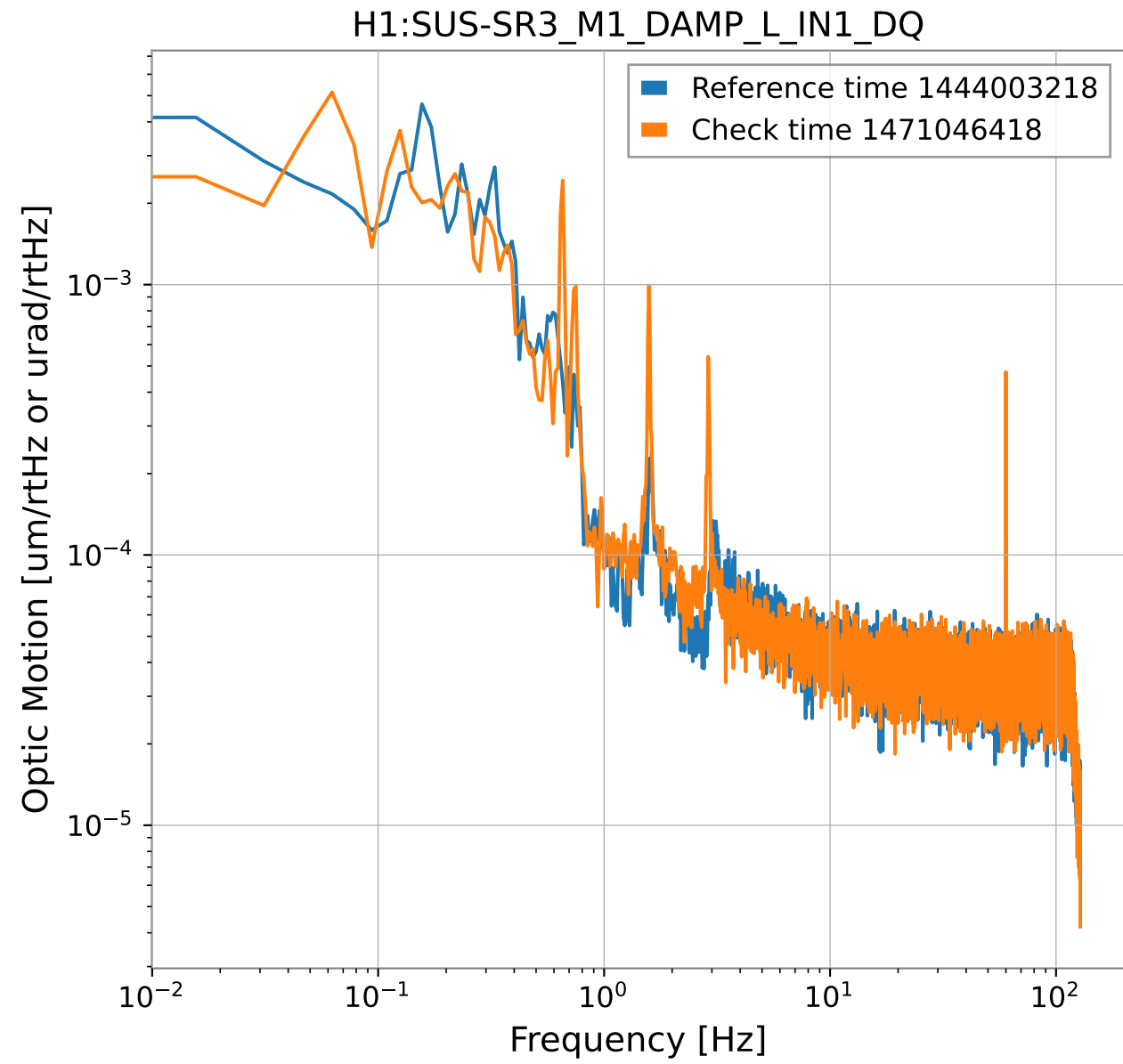
SRM



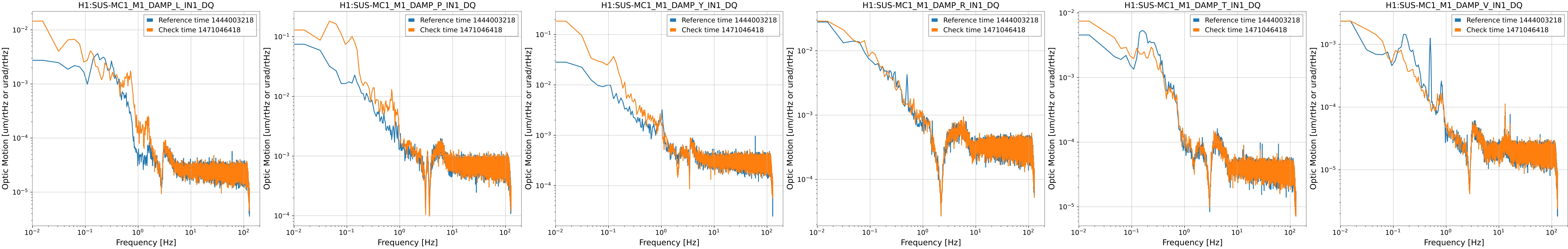
SR2



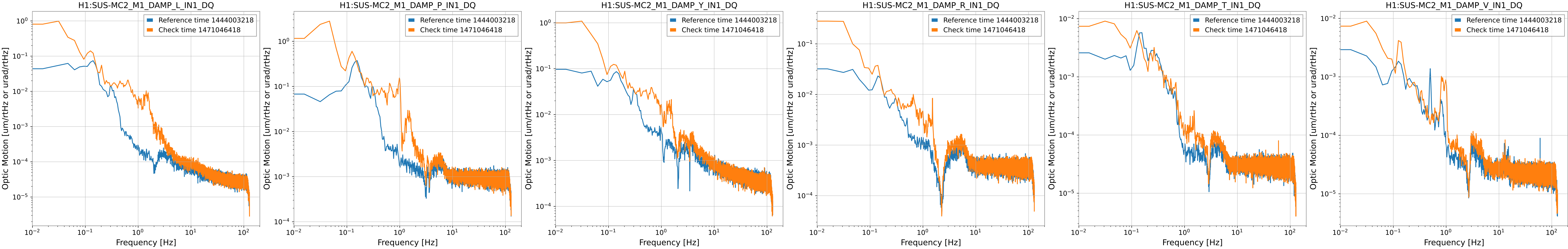
SR3



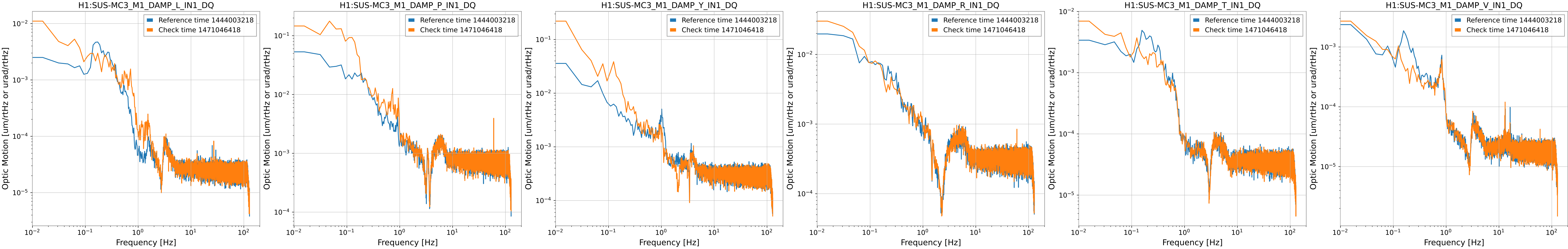
MC1



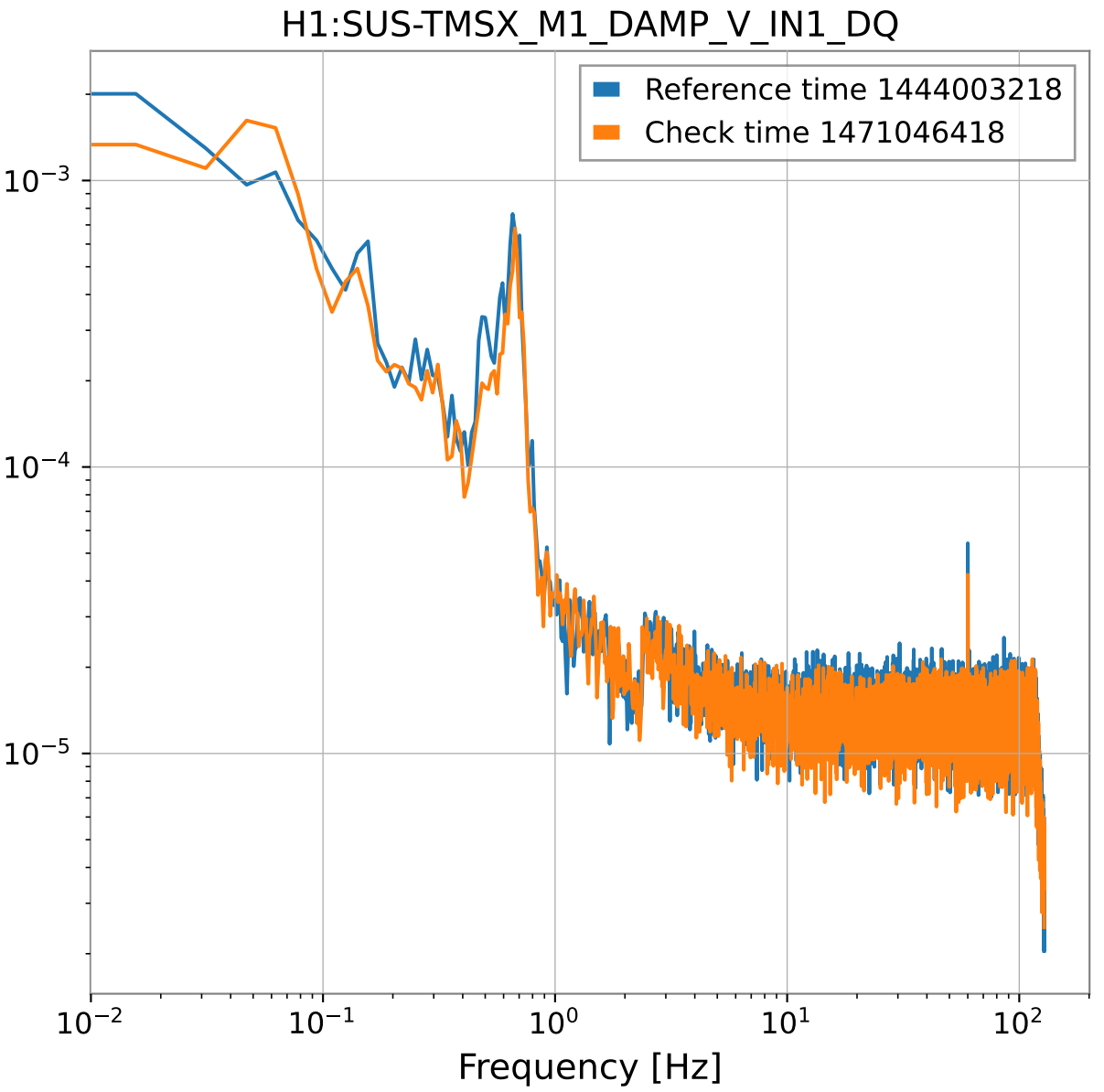
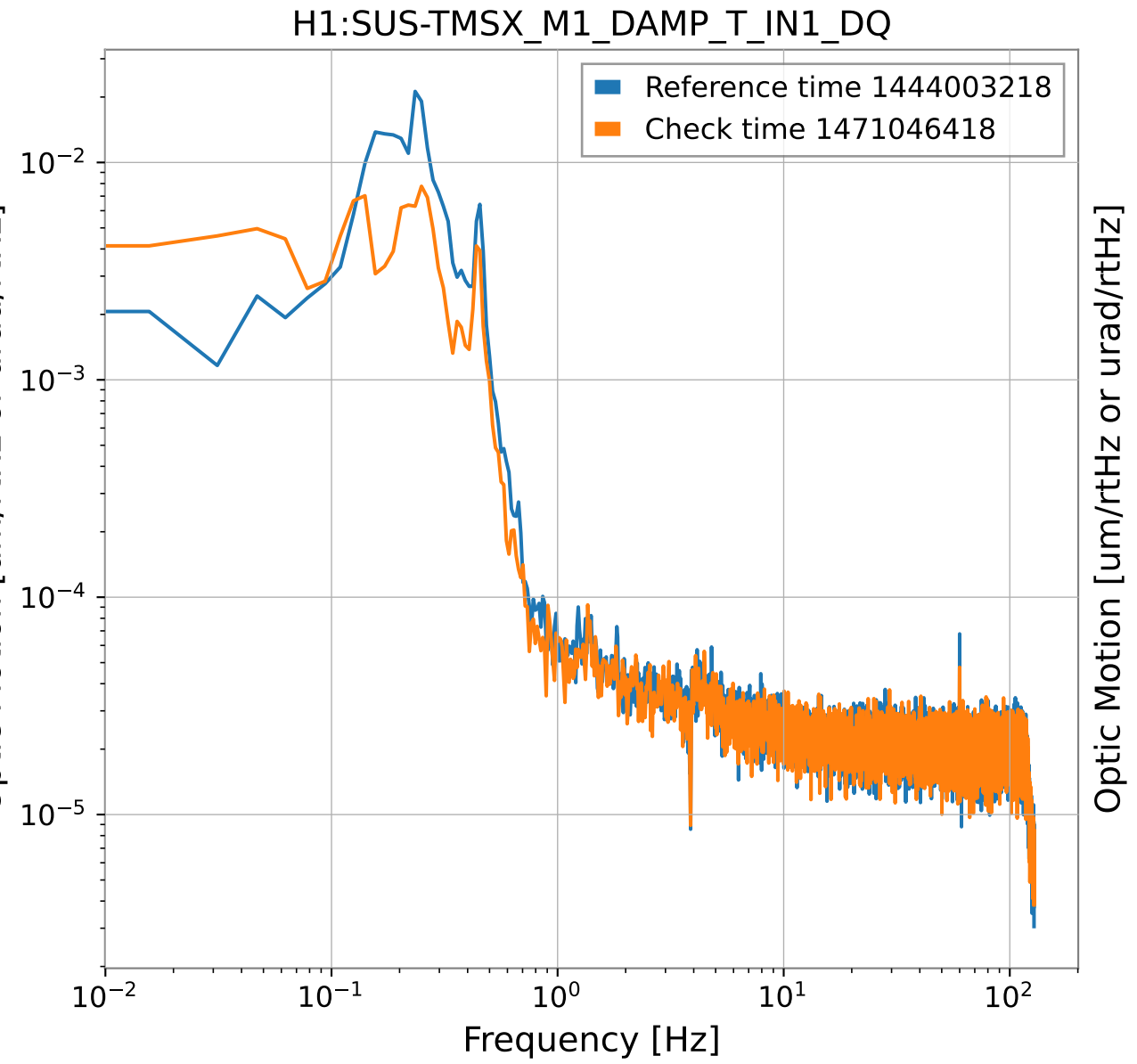
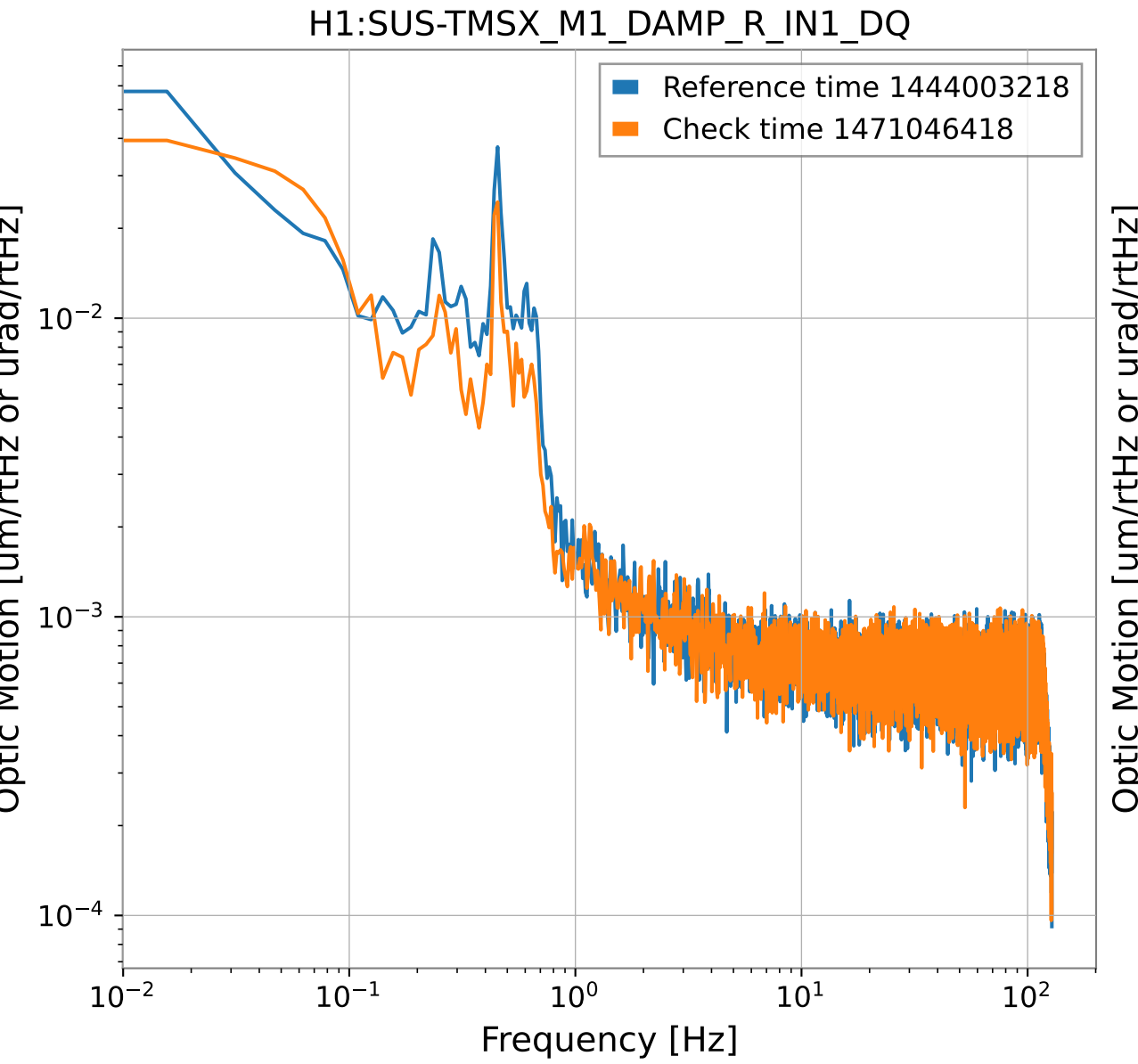
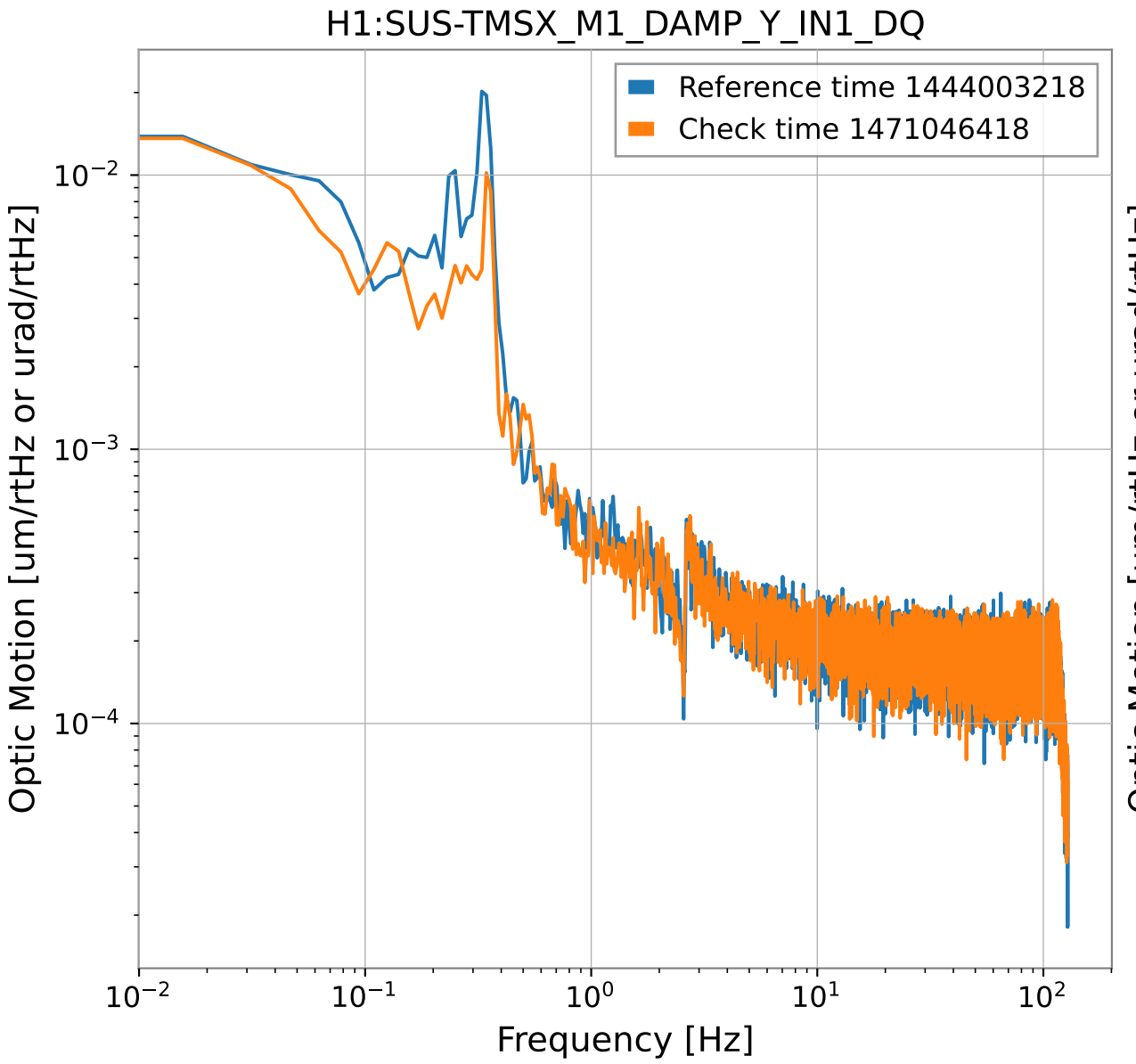
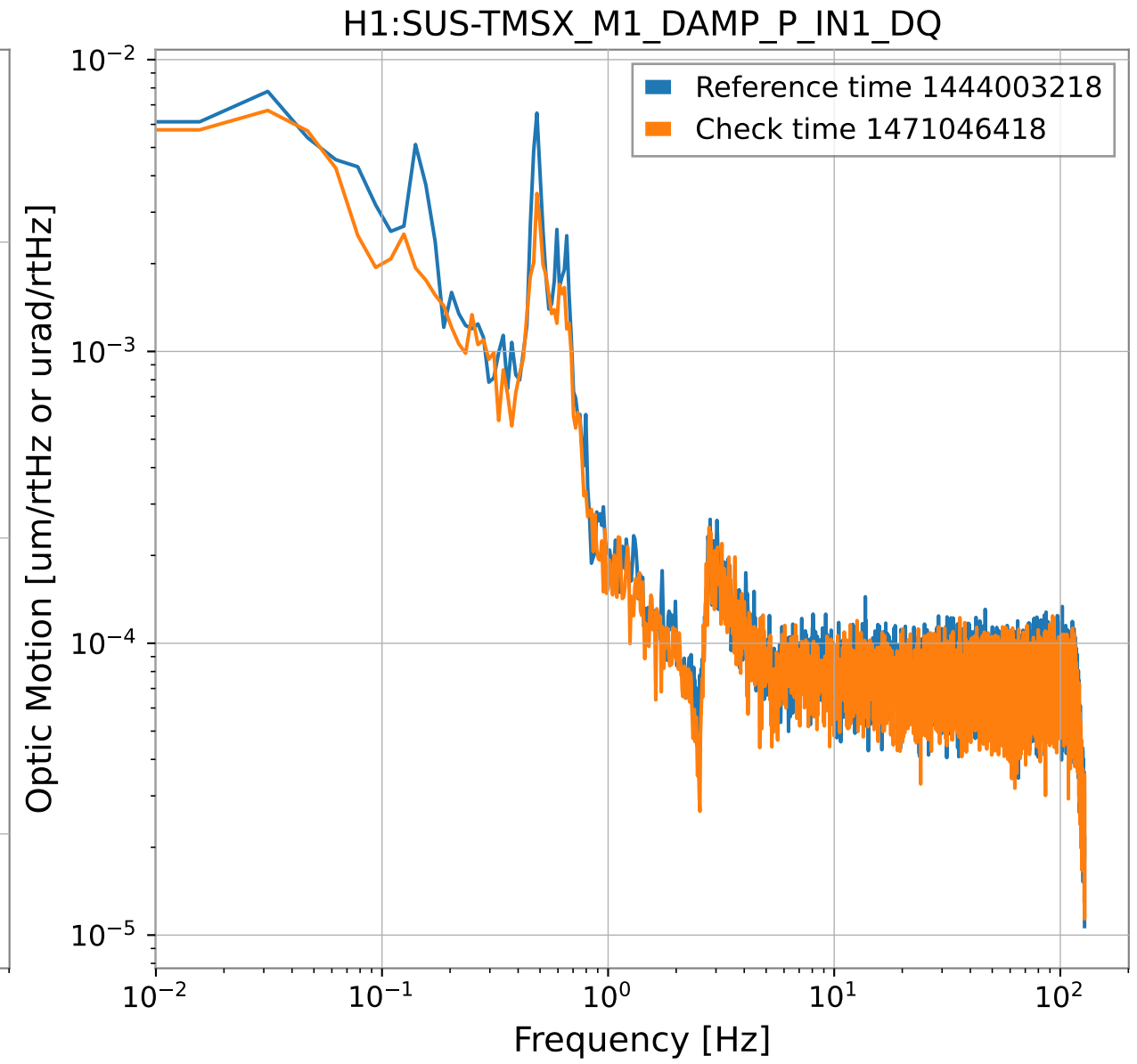
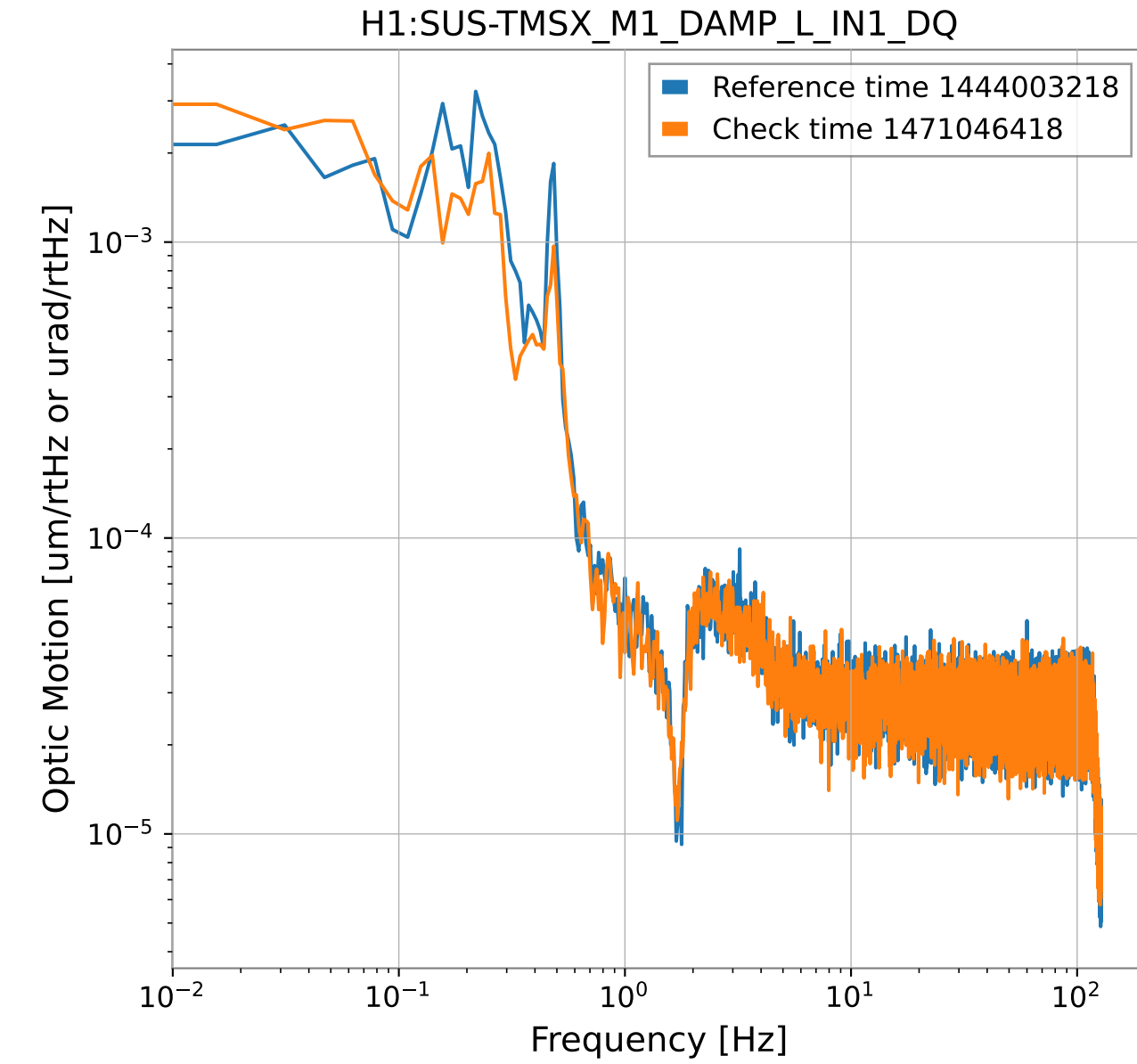
MC2



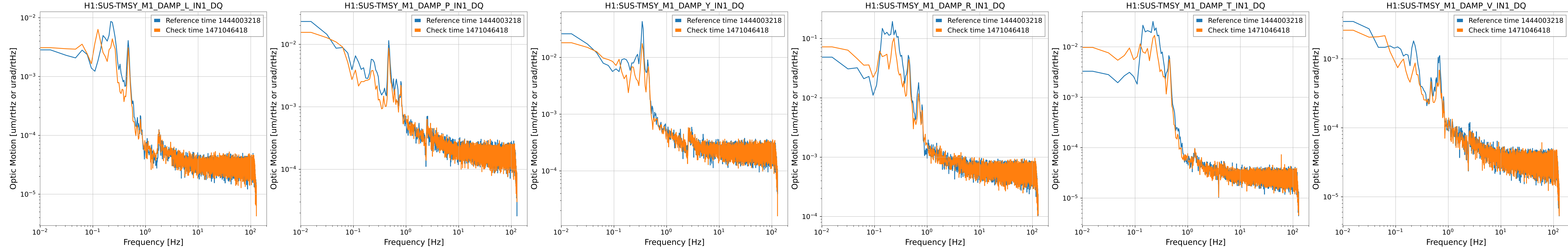
MC3



TMSX

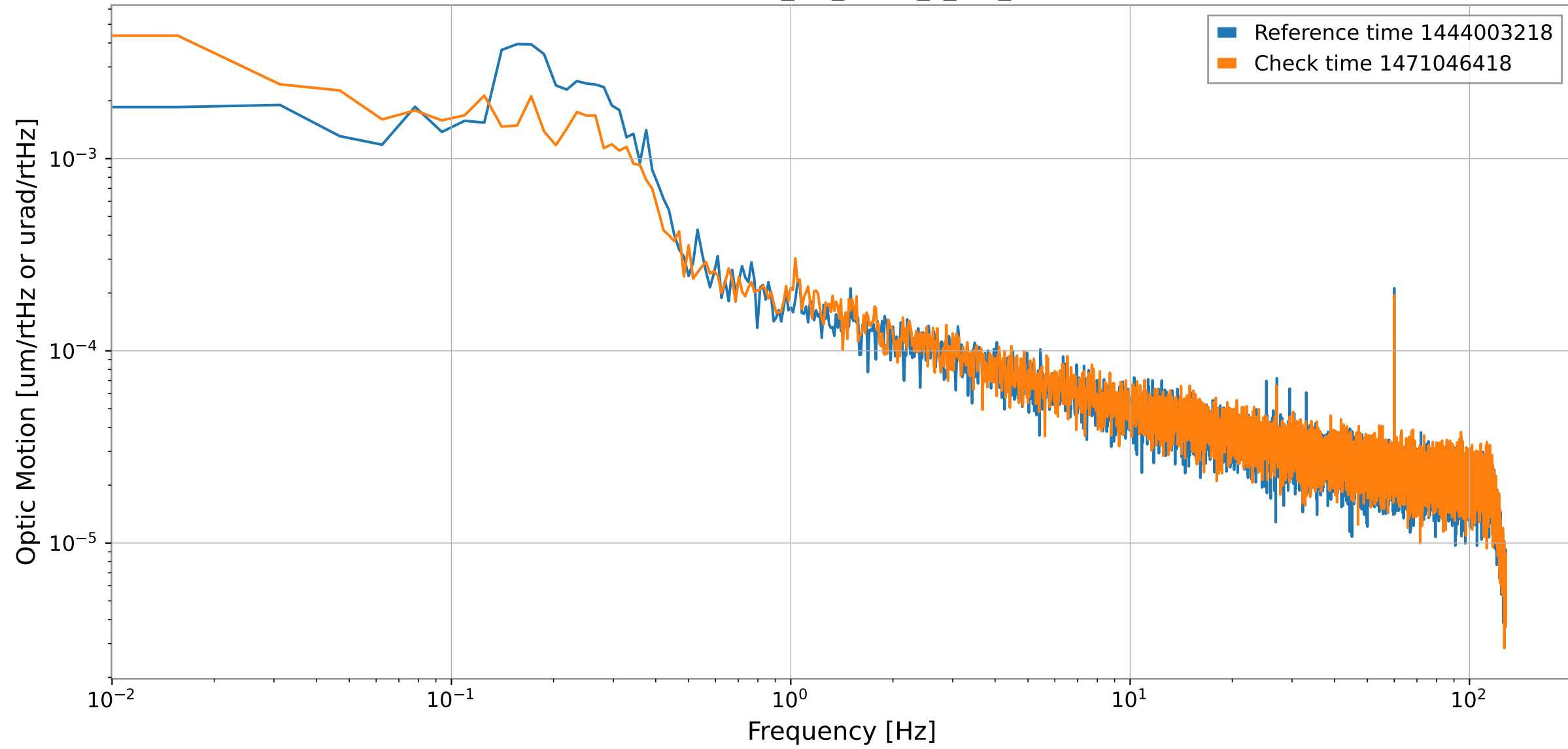


TMSY

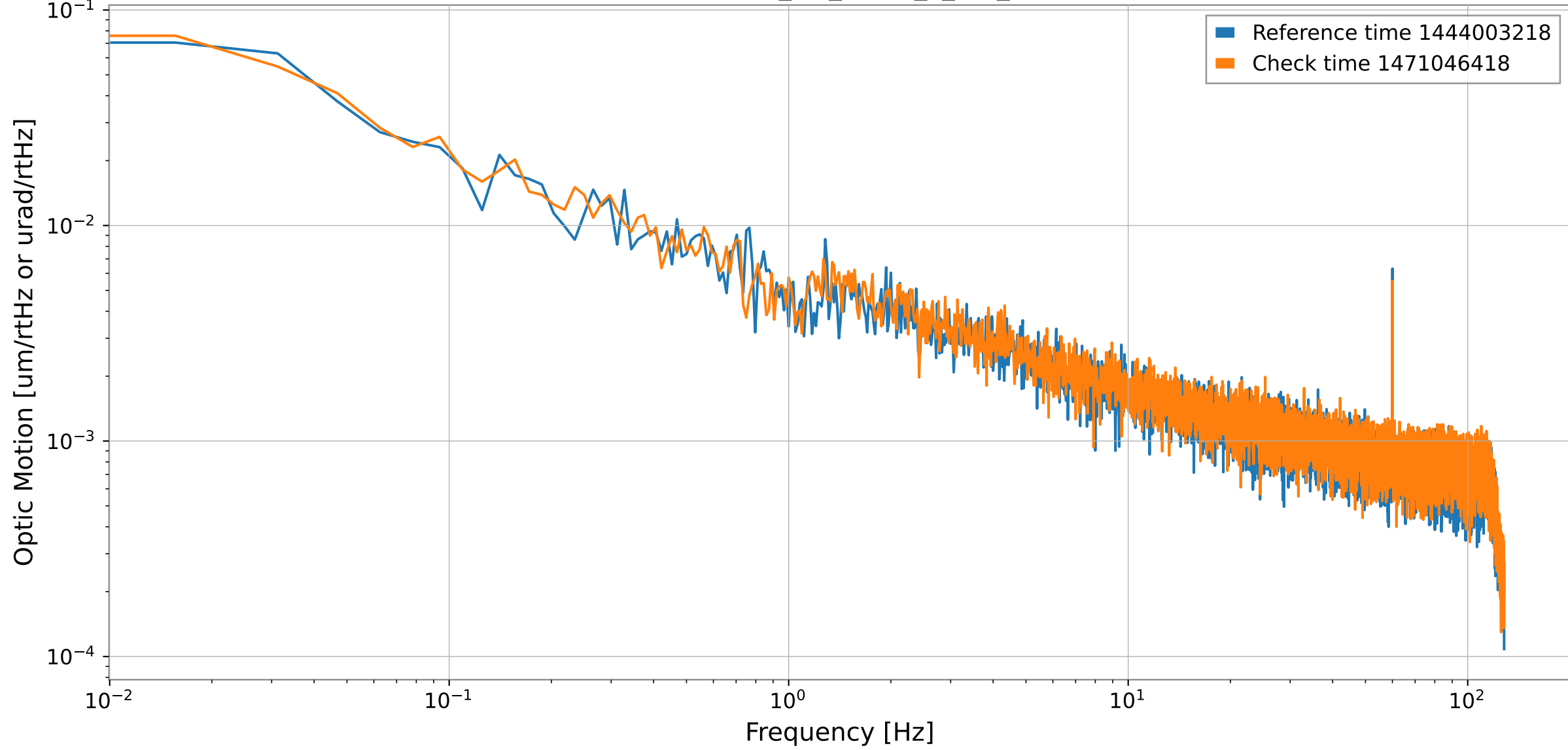


IM1

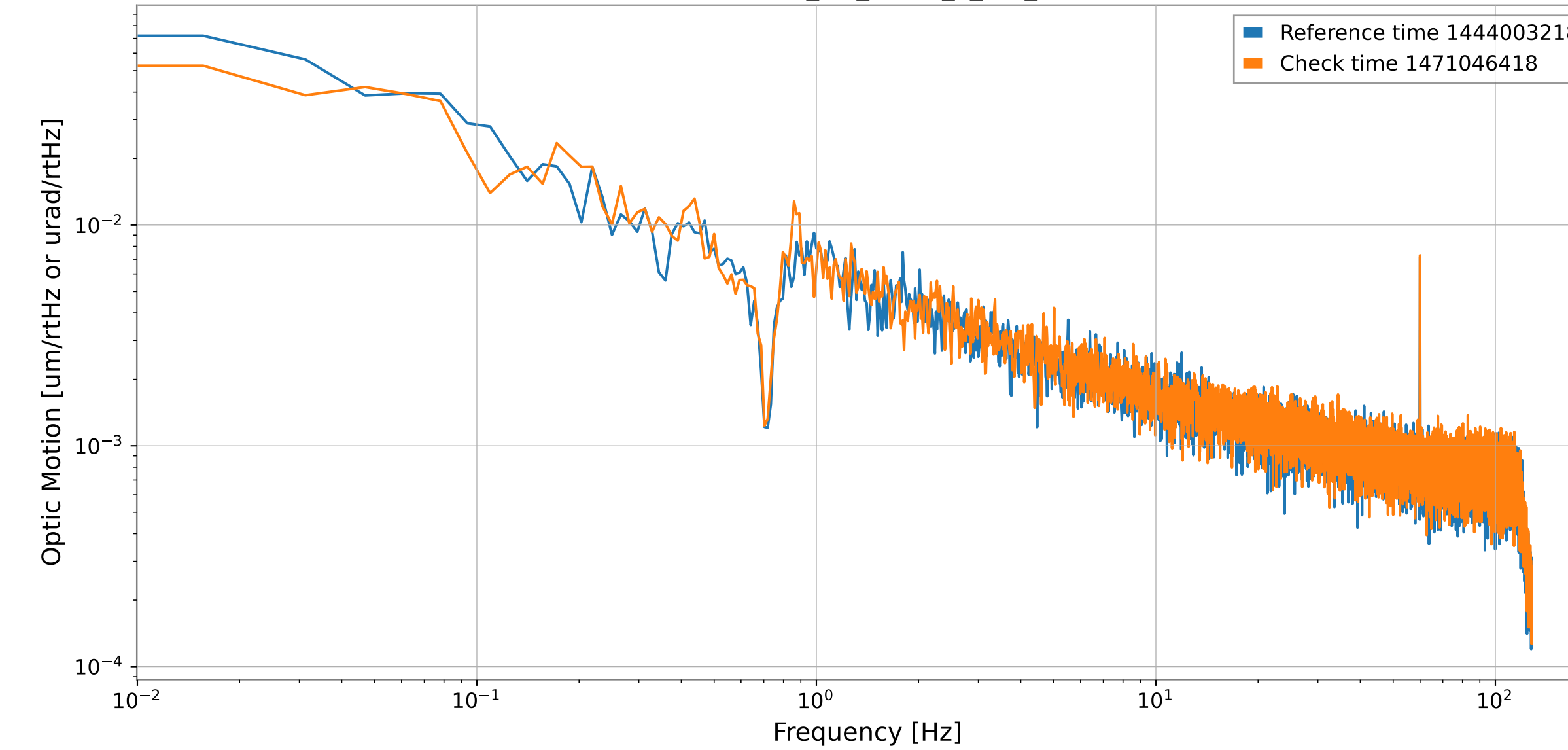
H1:SUS-IM1_M1_DAMP_L_IN1_DQ



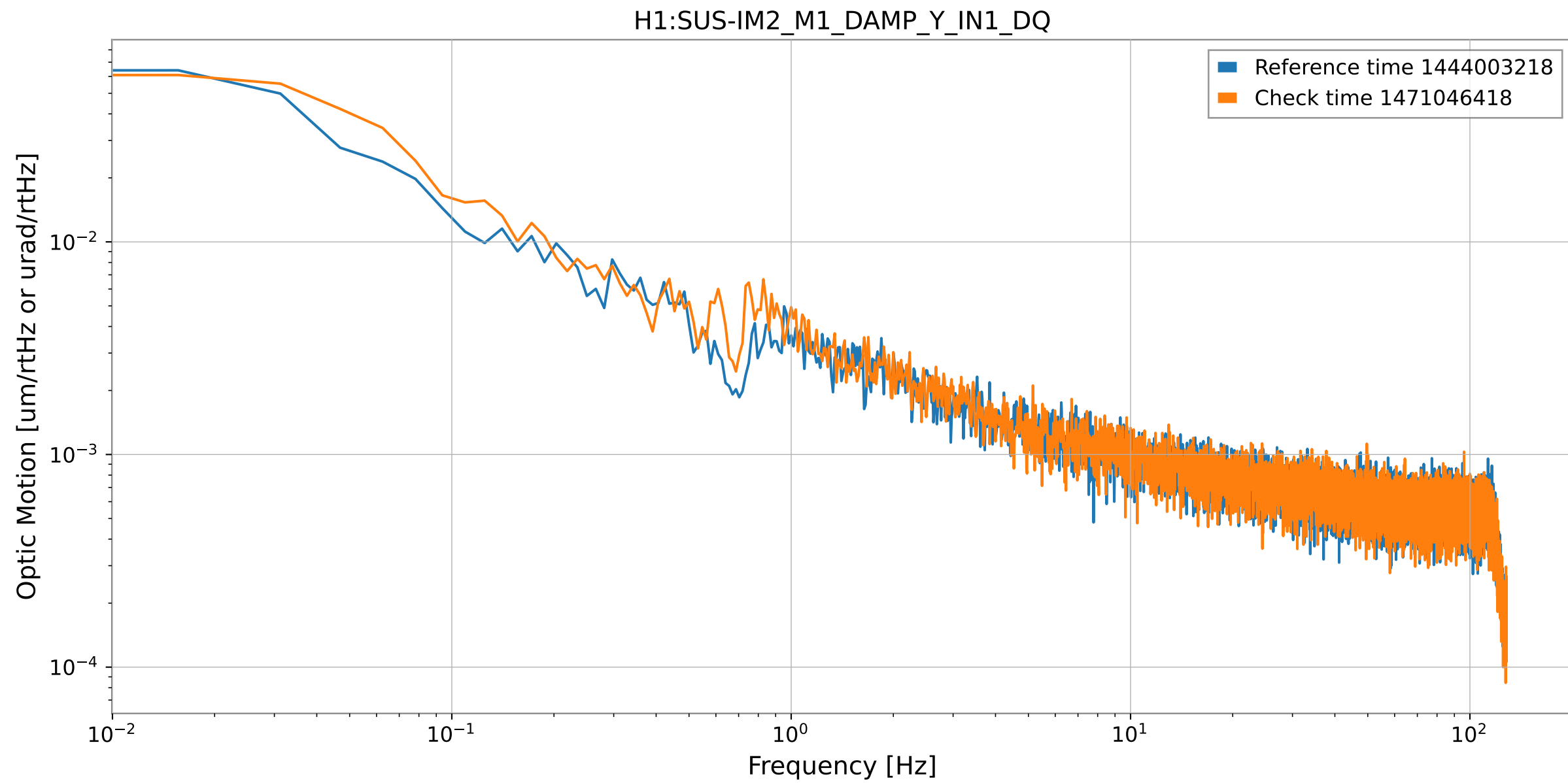
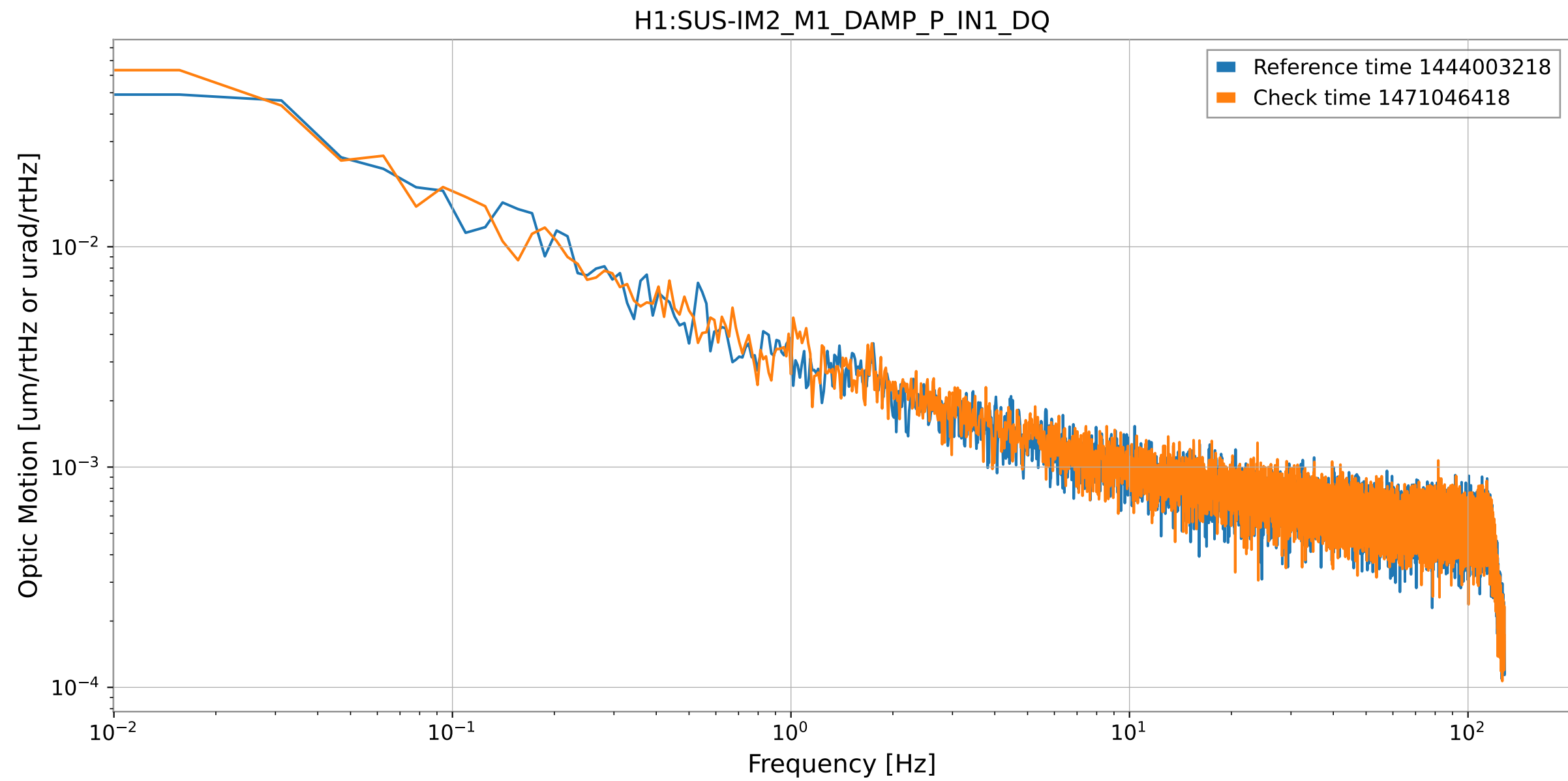
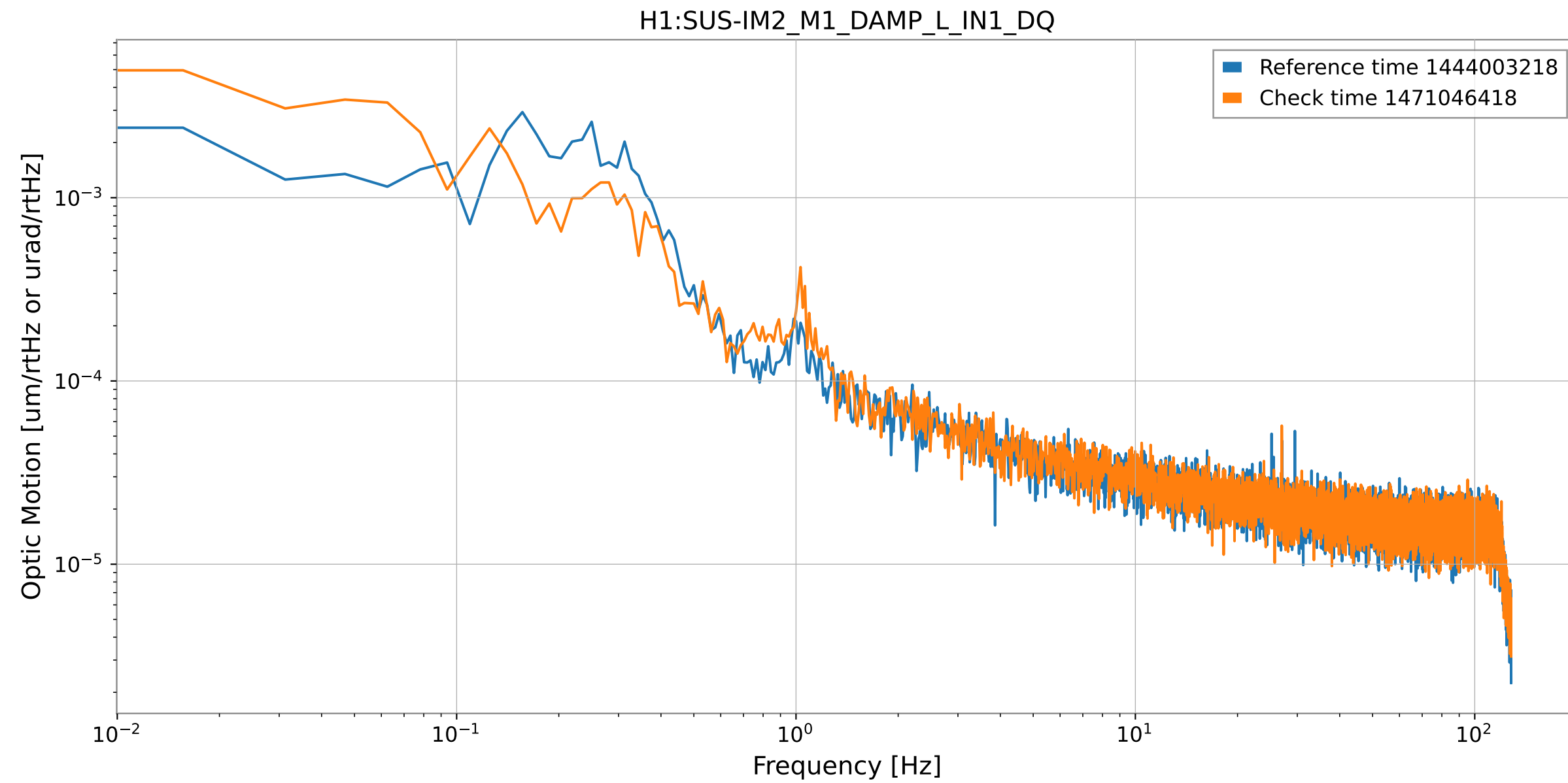
H1:SUS-IM1_M1_DAMP_P_IN1_DQ



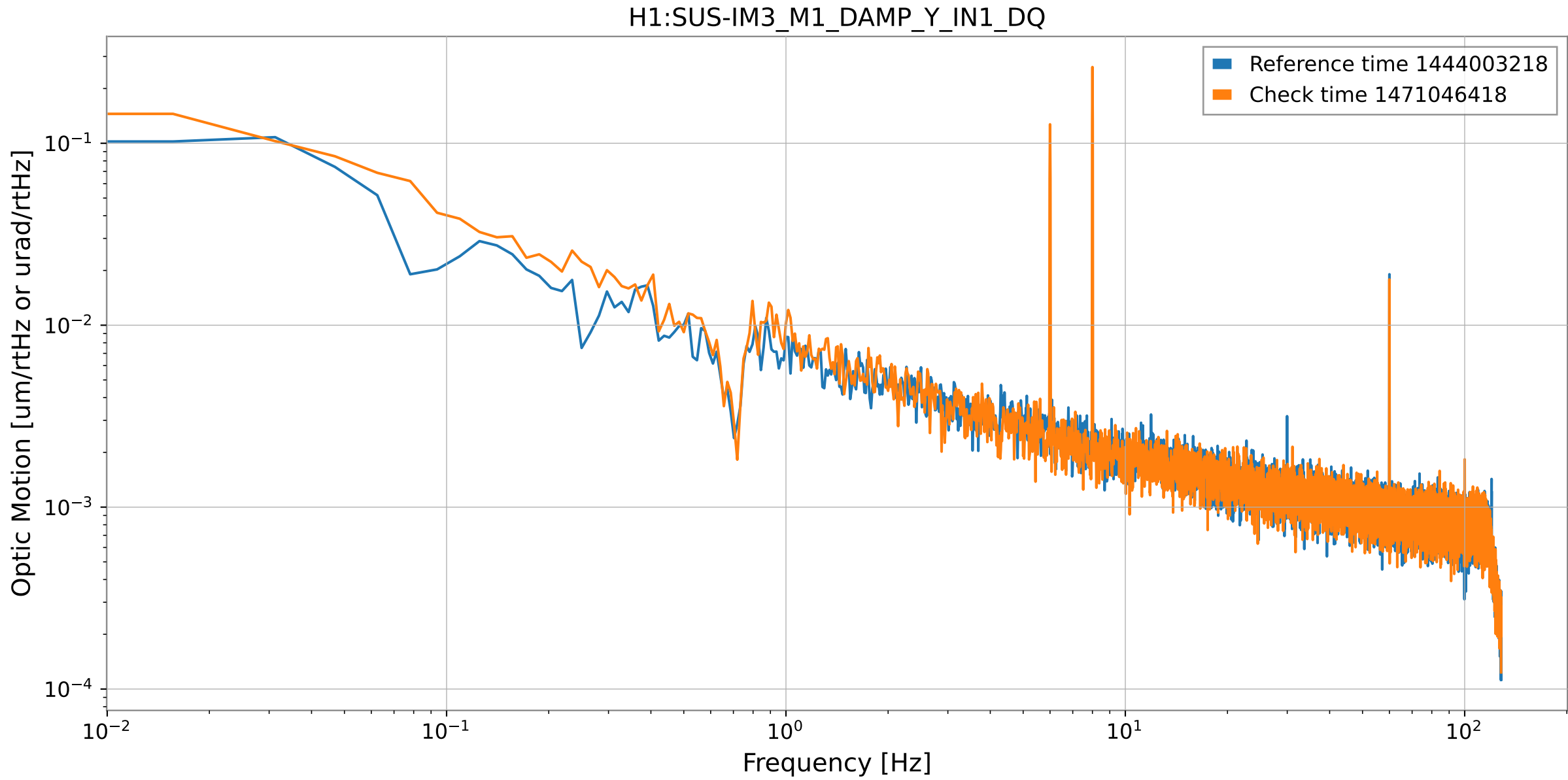
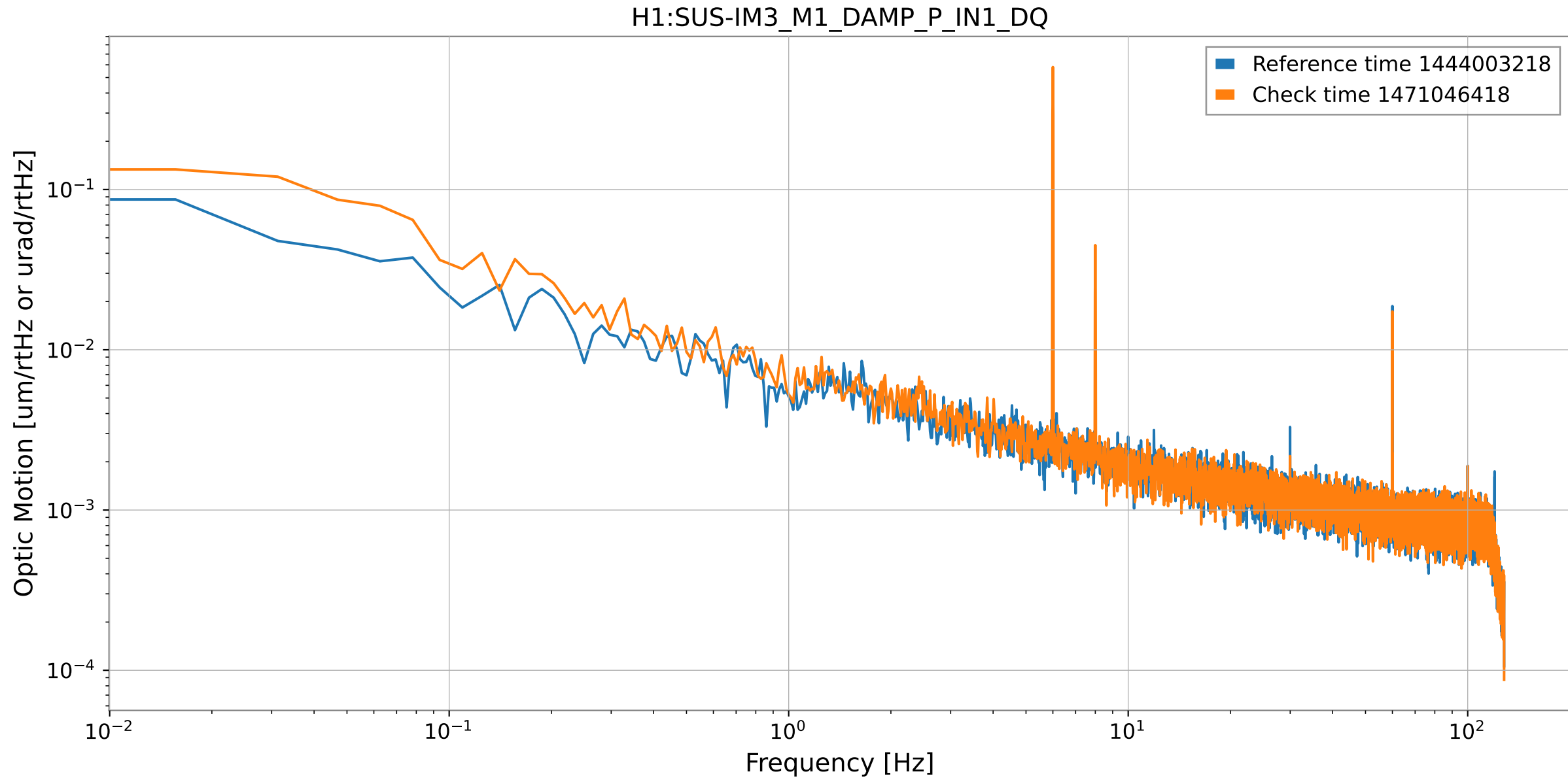
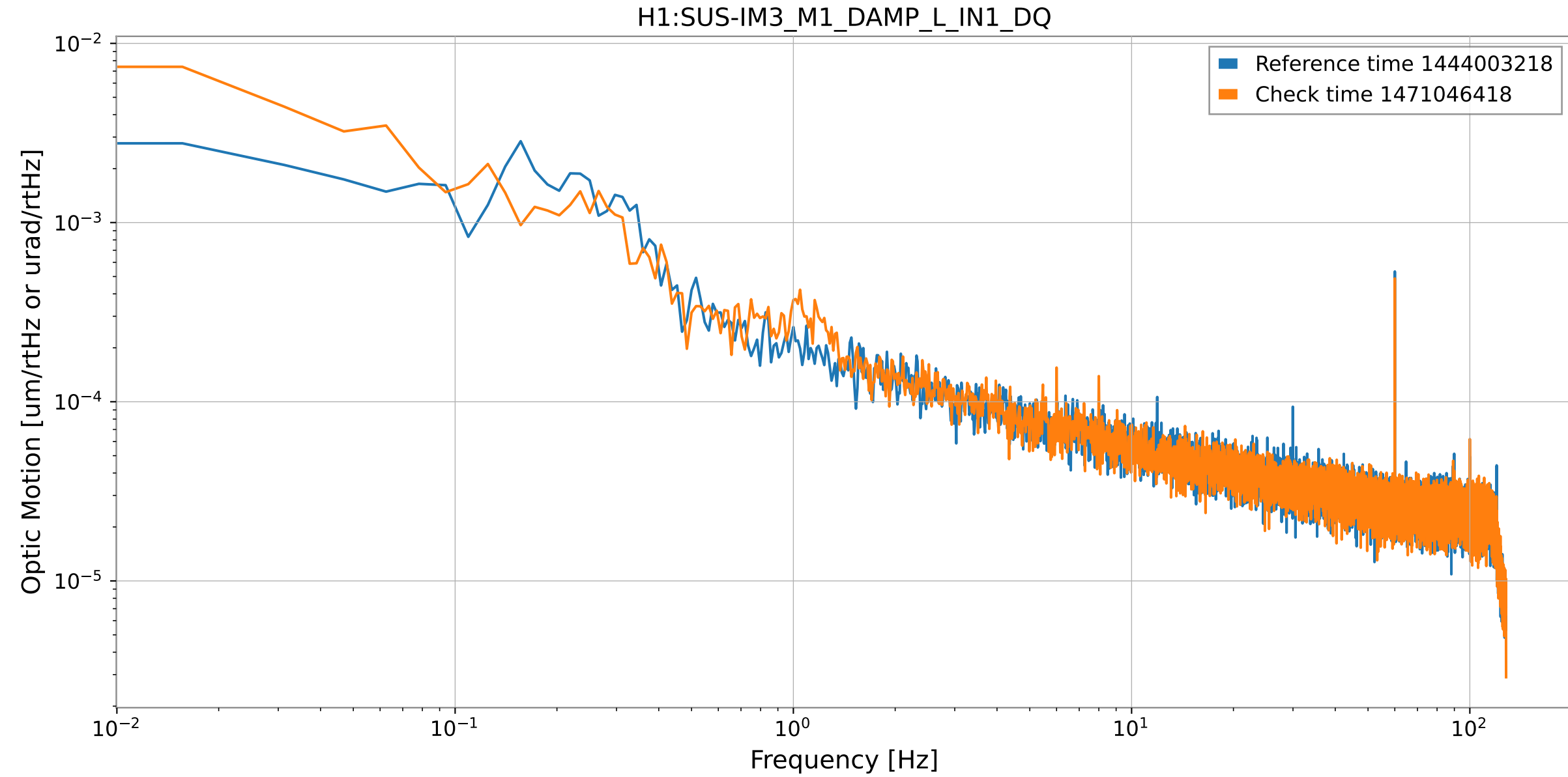
H1:SUS-IM1_M1_DAMP_Y_IN1_DQ



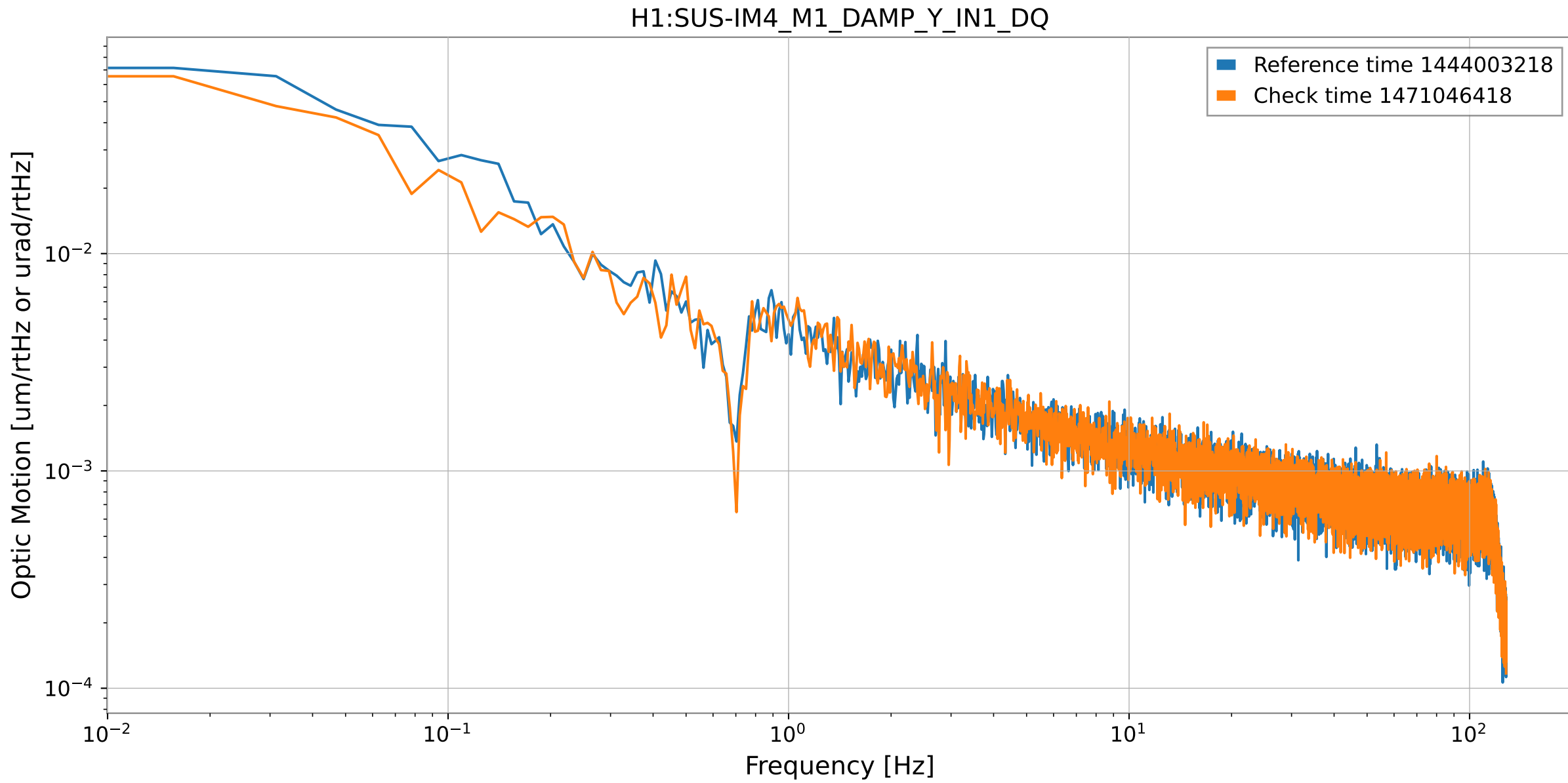
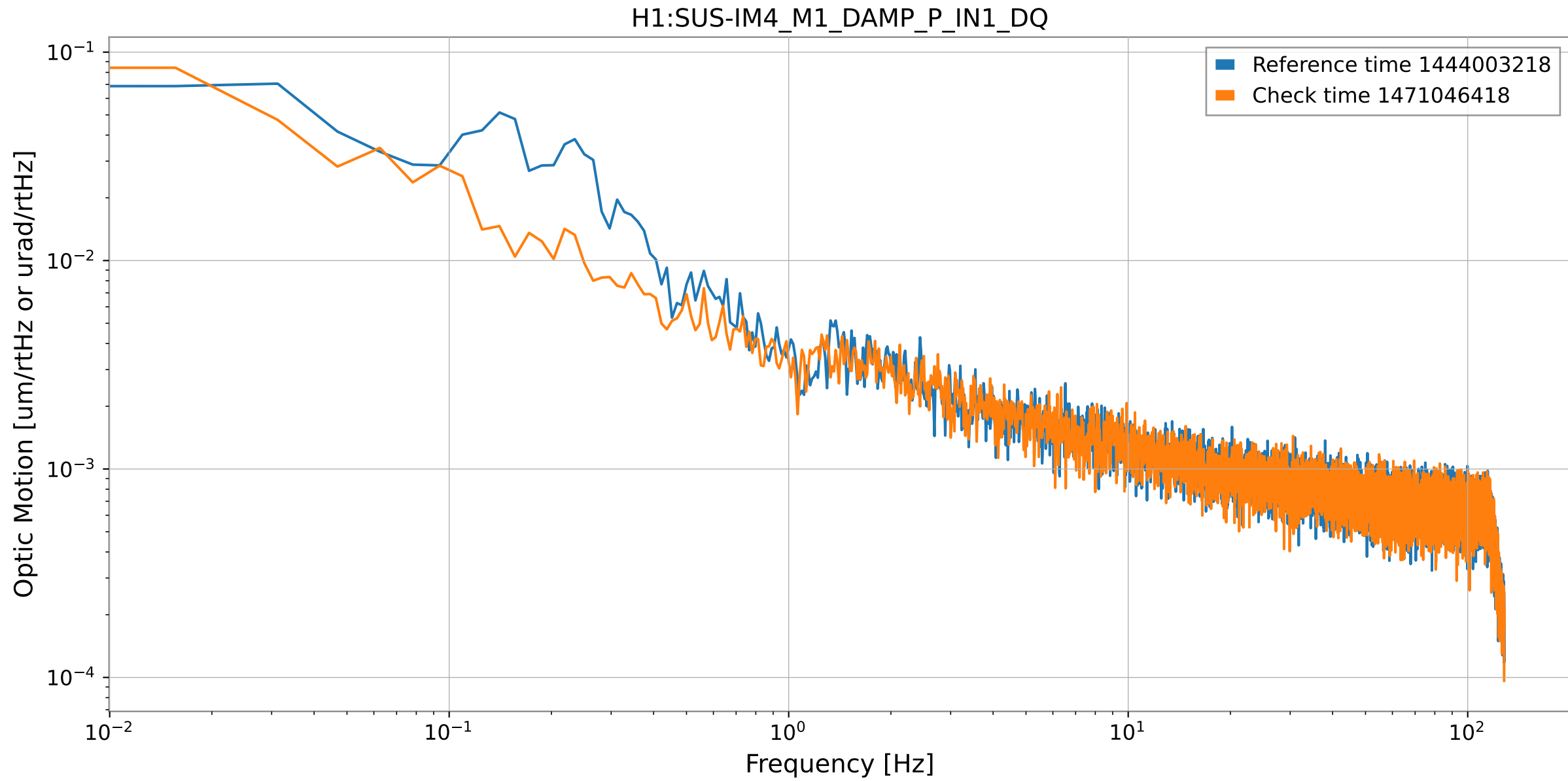
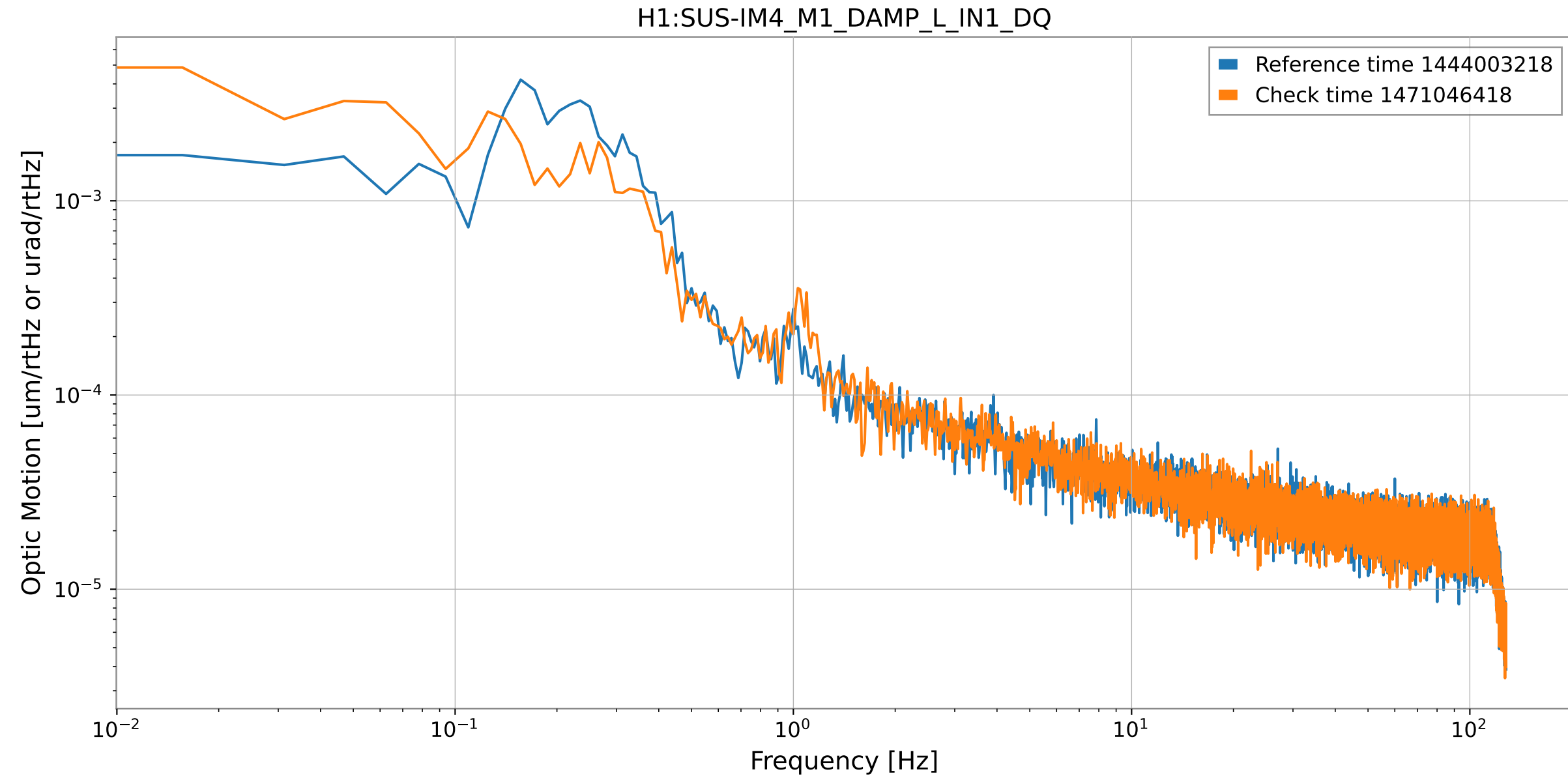
IM2



IM3

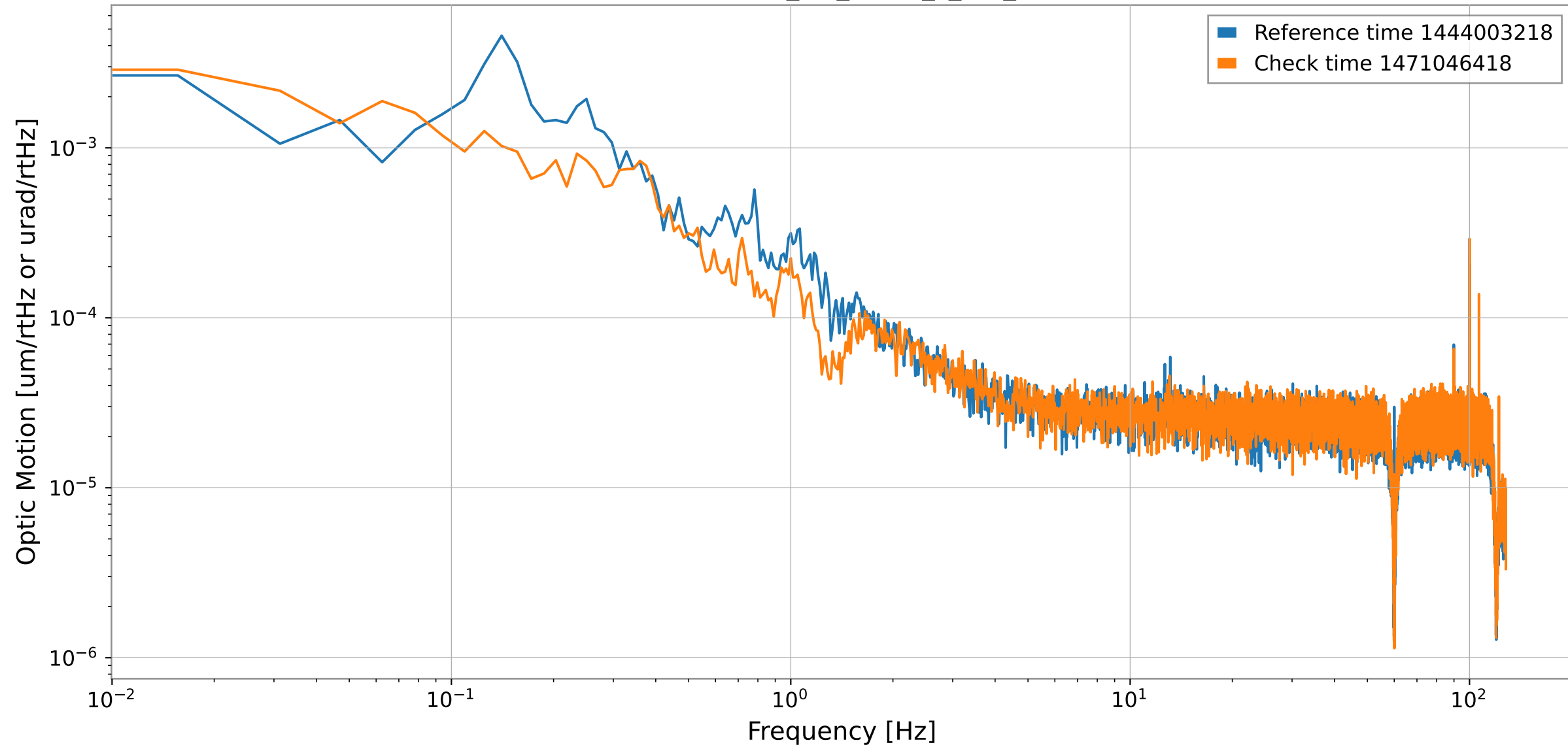


IM4

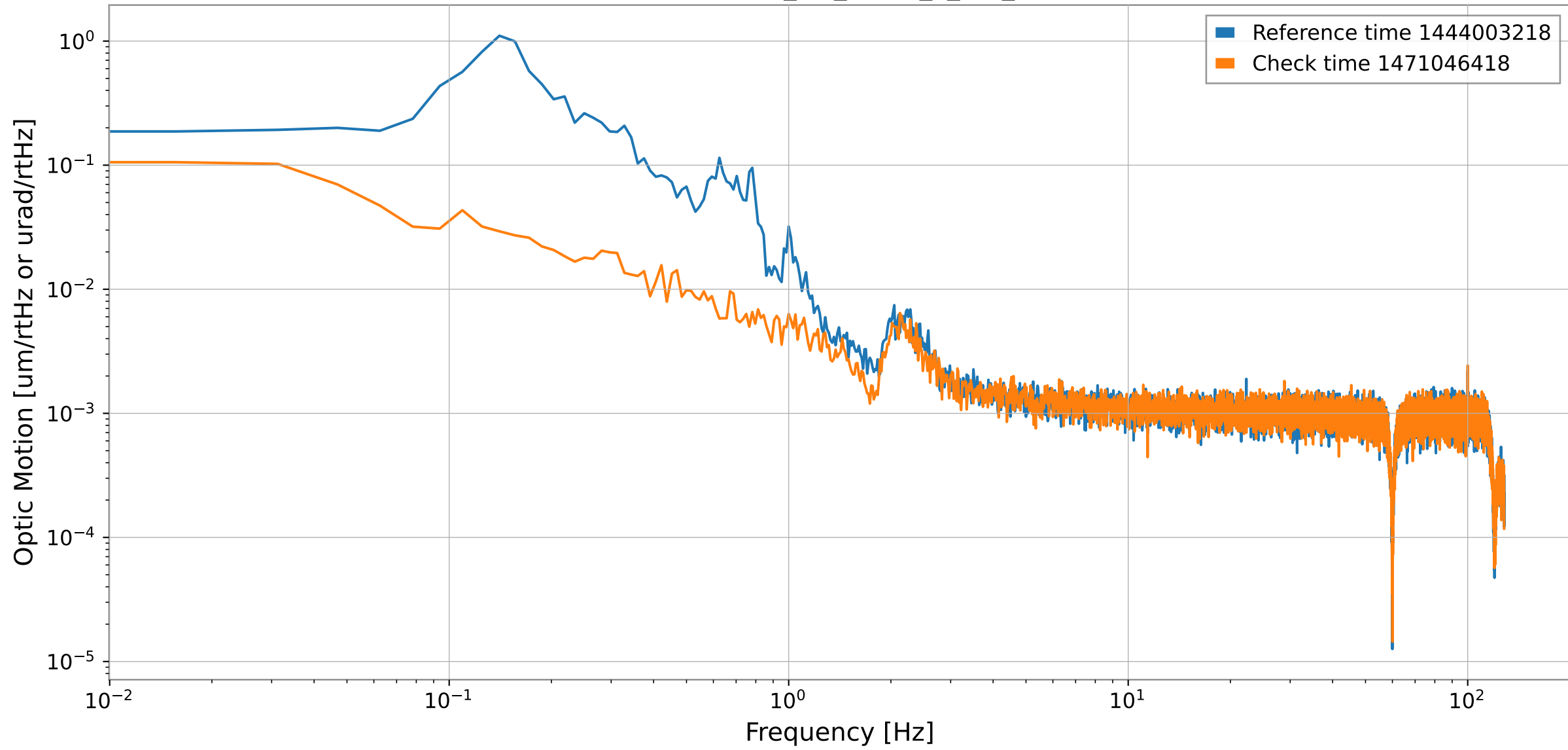


RM1

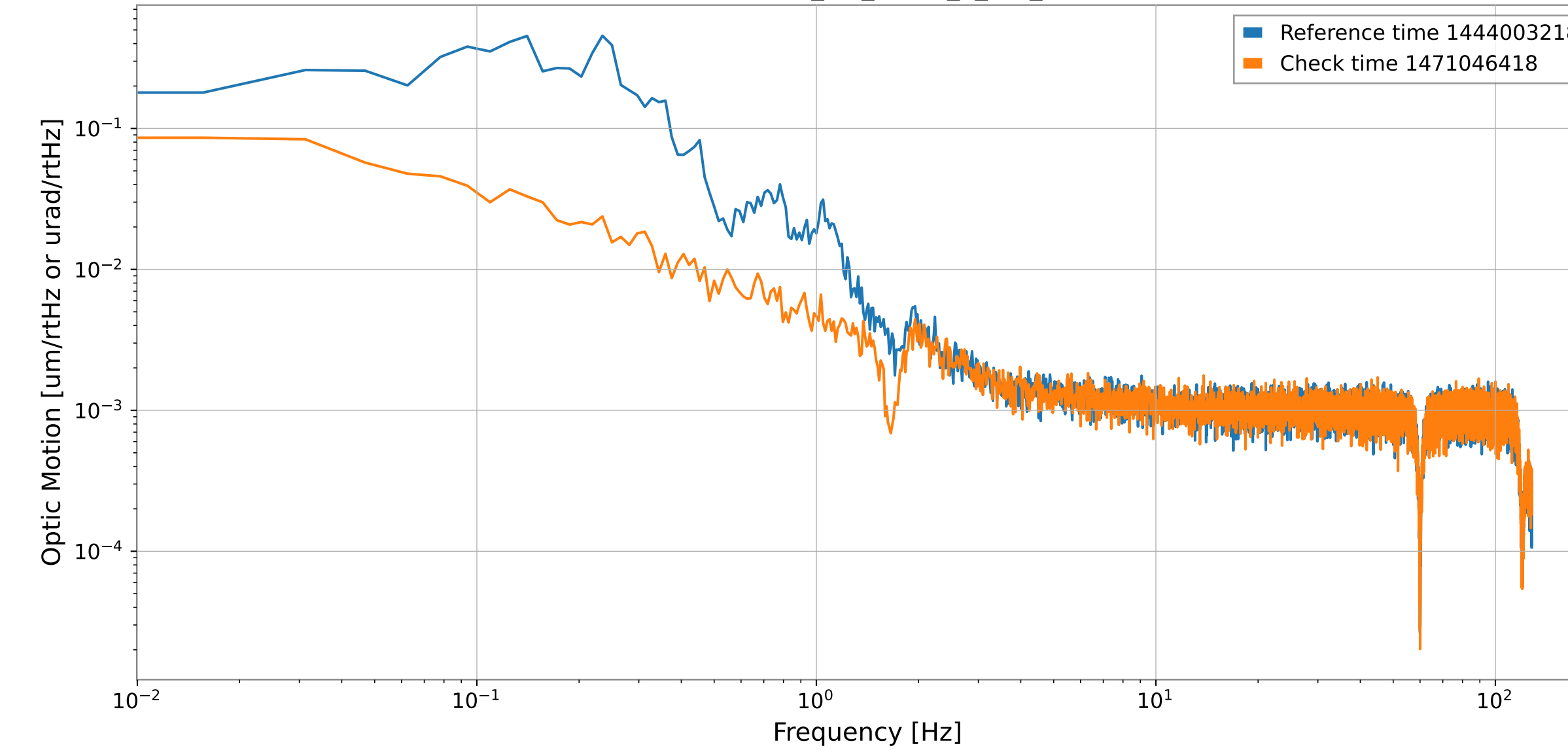
H1:SUS-RM1_M1_DAMP_L_IN1_DQ



H1:SUS-RM1_M1_DAMP_P_IN1_DQ

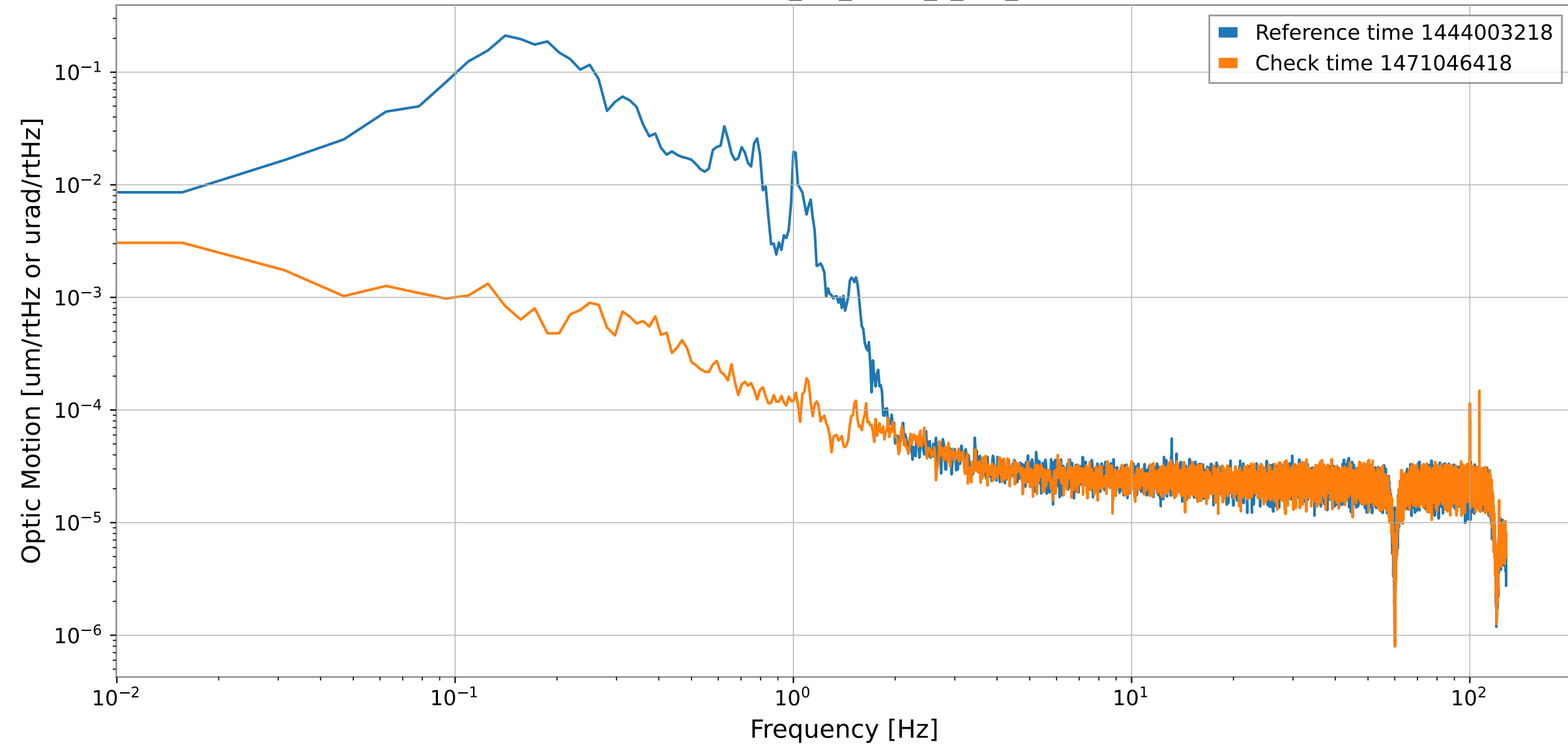


H1:SUS-RM1_M1_DAMP_Y_IN1_DQ

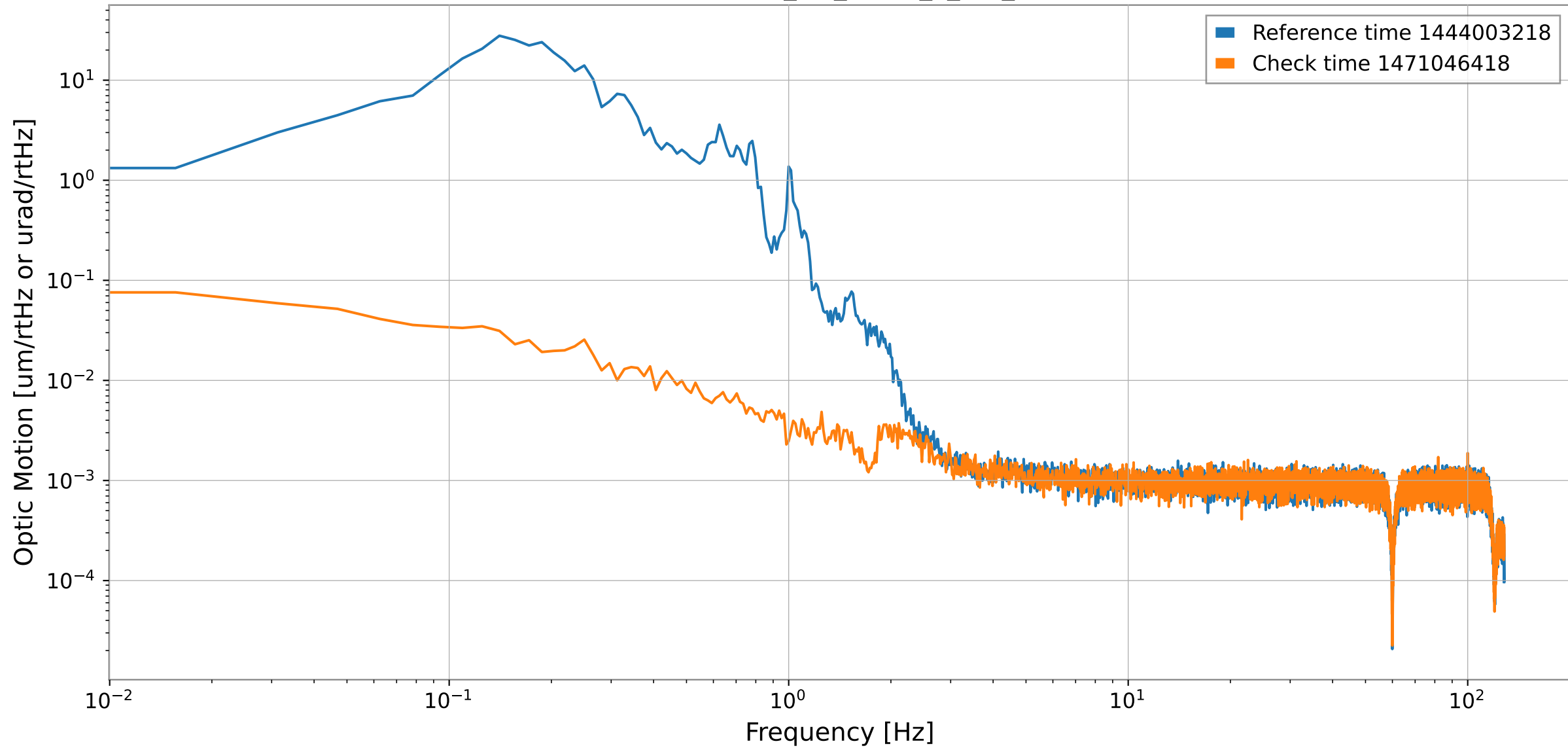


RM2

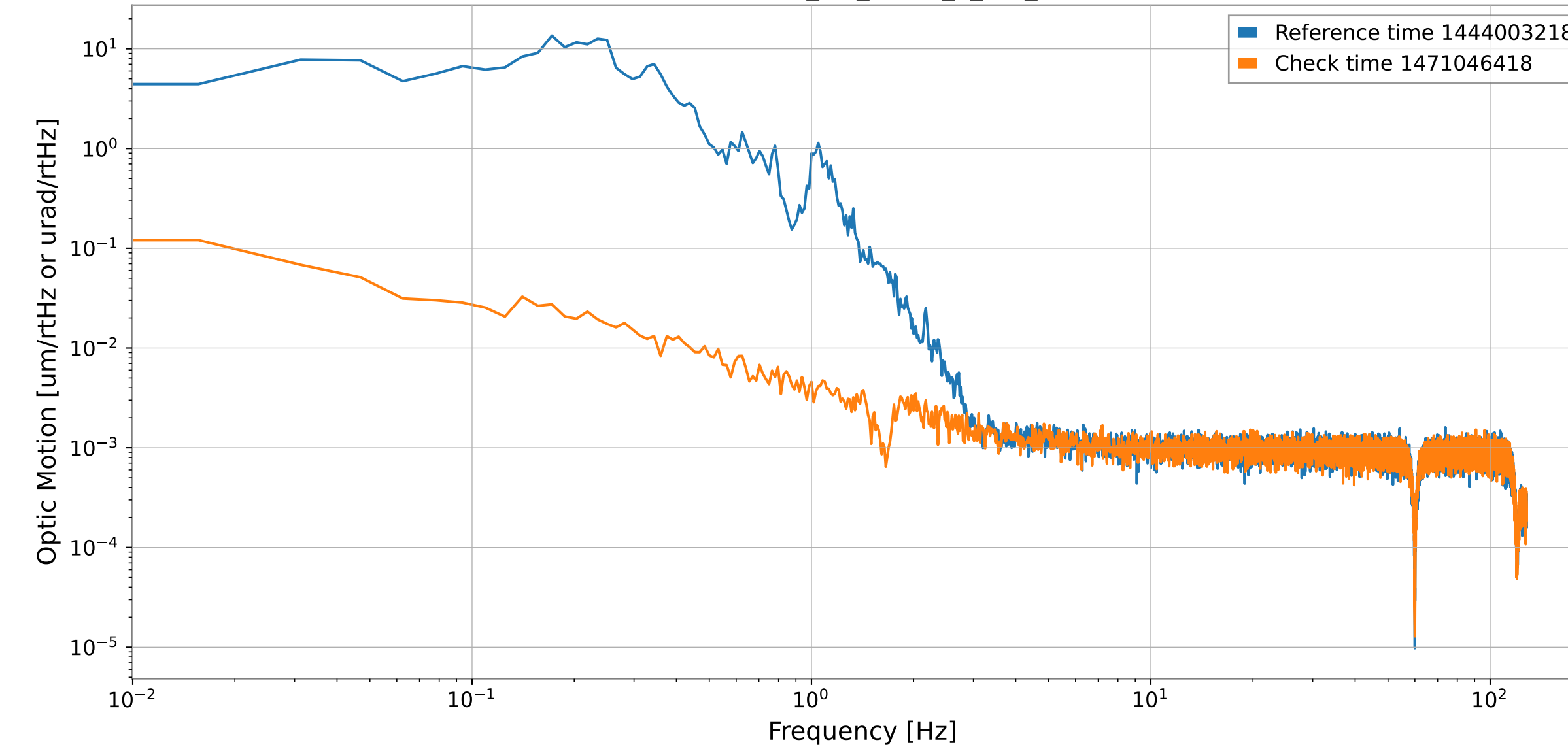
H1:SUS-RM2_M1_DAMP_L_IN1_DQ



H1:SUS-RM2_M1_DAMP_P_IN1_DQ

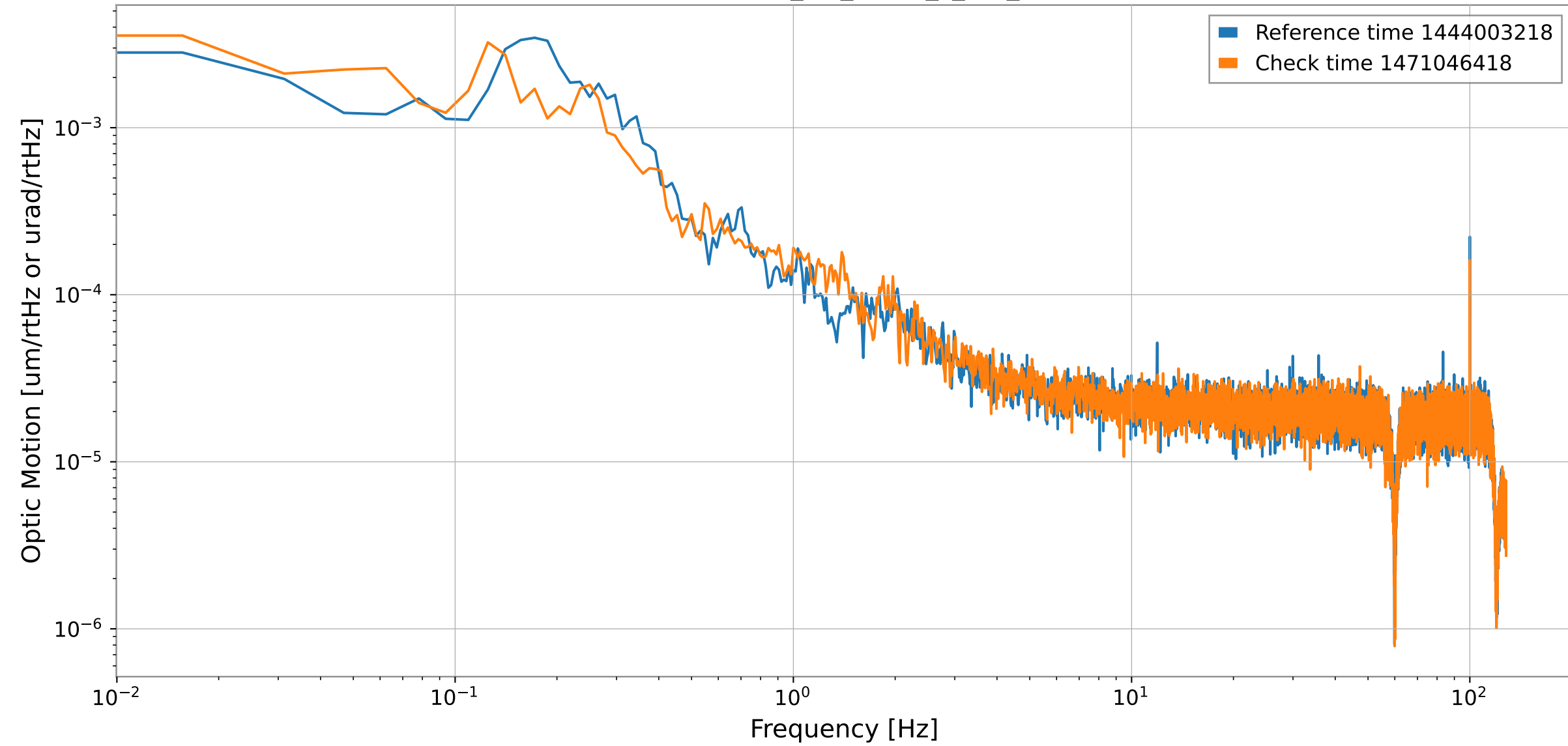


H1:SUS-RM2_M1_DAMP_Y_IN1_DQ

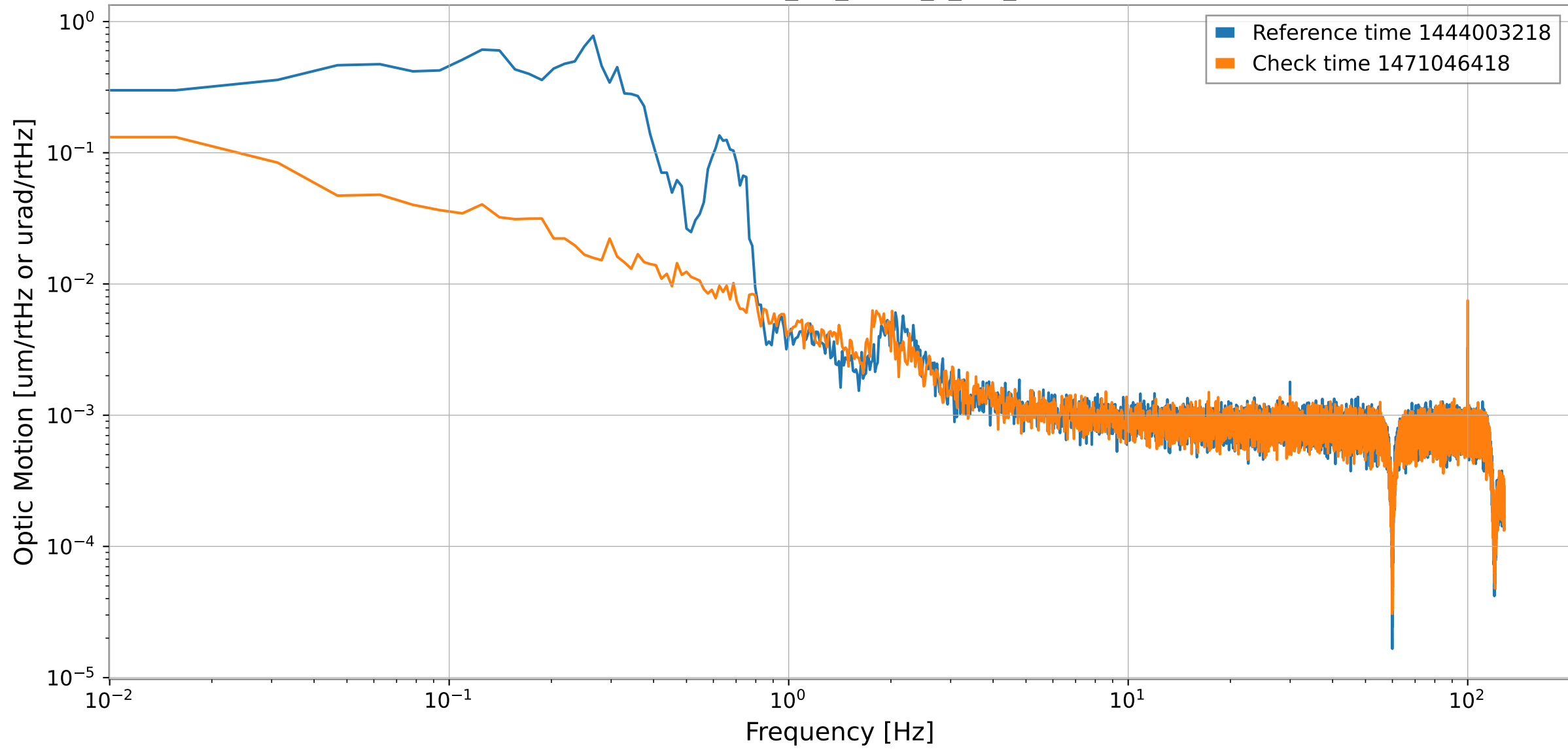


OM1

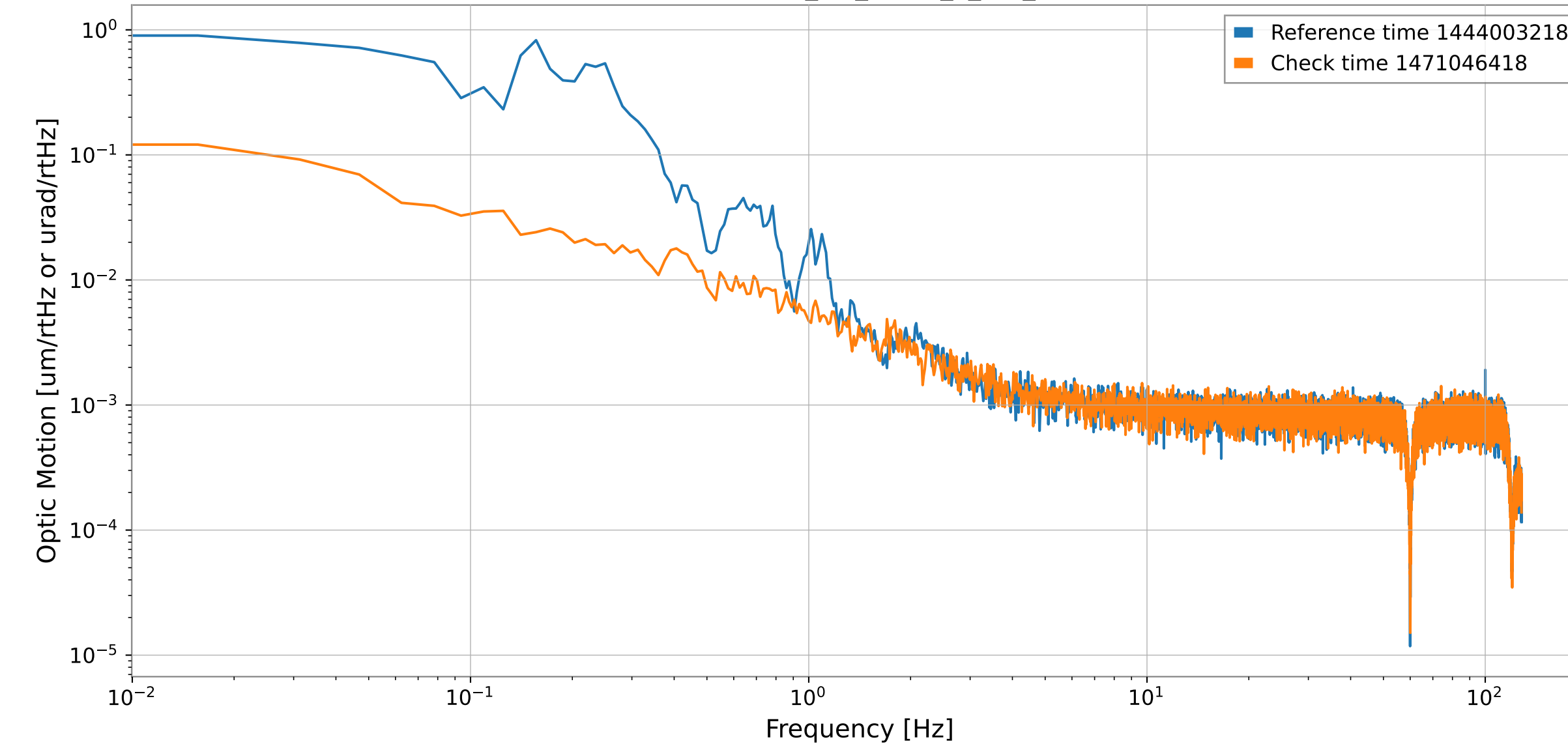
H1:SUS-OM1_M1_DAMP_L_IN1_DQ



H1:SUS-OM1_M1_DAMP_P_IN1_DQ

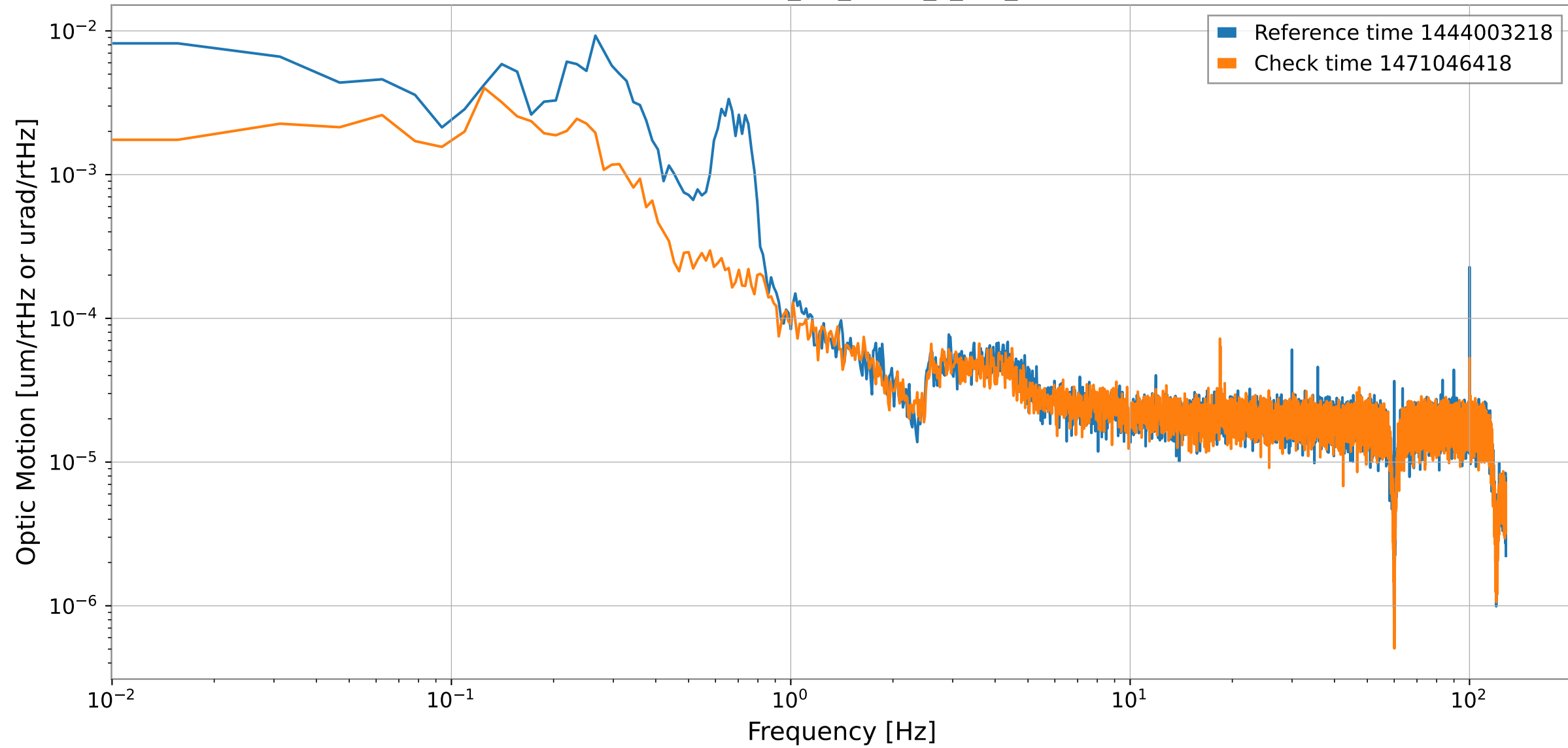


H1:SUS-OM1_M1_DAMP_Y_IN1_DQ

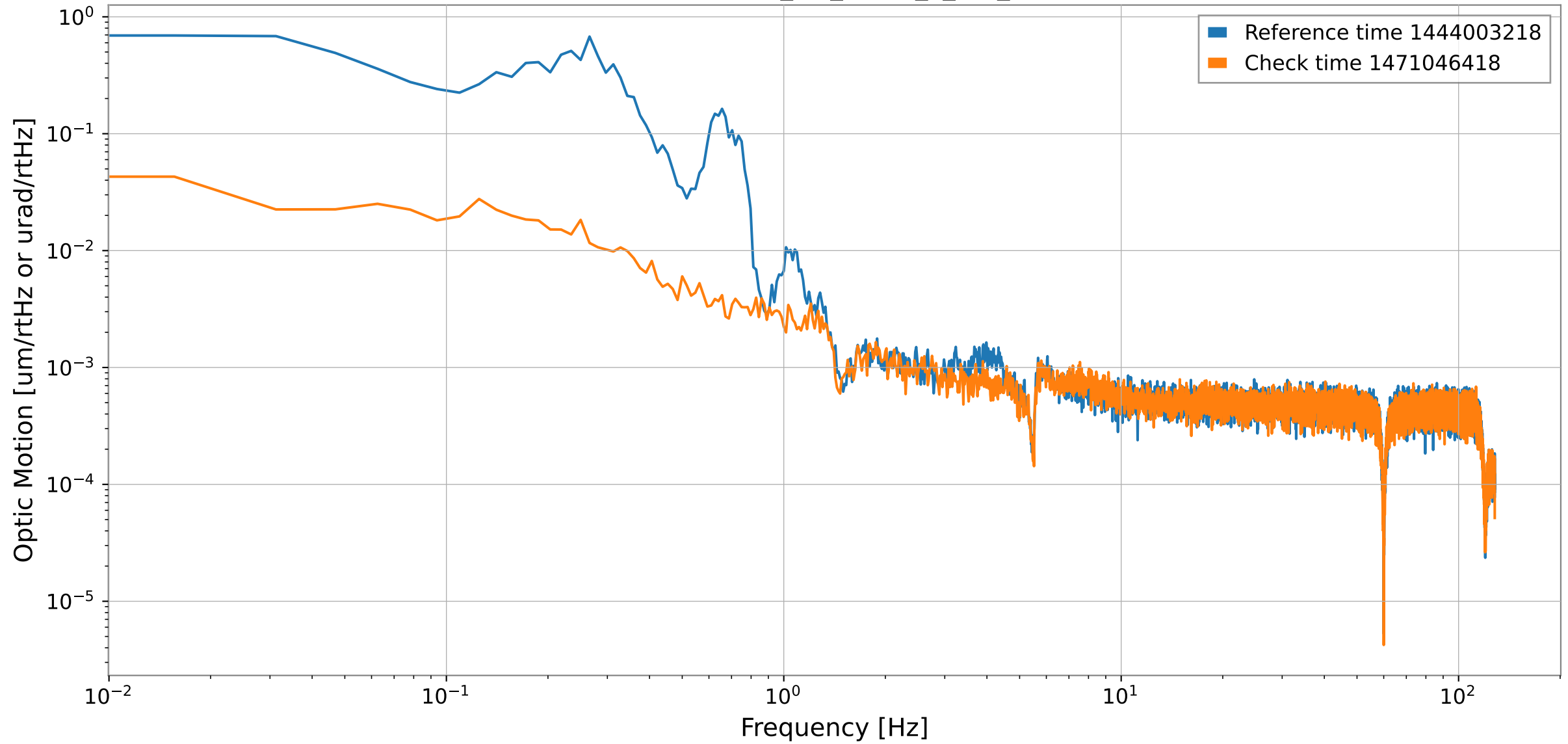


OM2

H1:SUS-OM2_M1_DAMP_L_IN1_DQ



H1:SUS-OM2_M1_DAMP_P_IN1_DQ

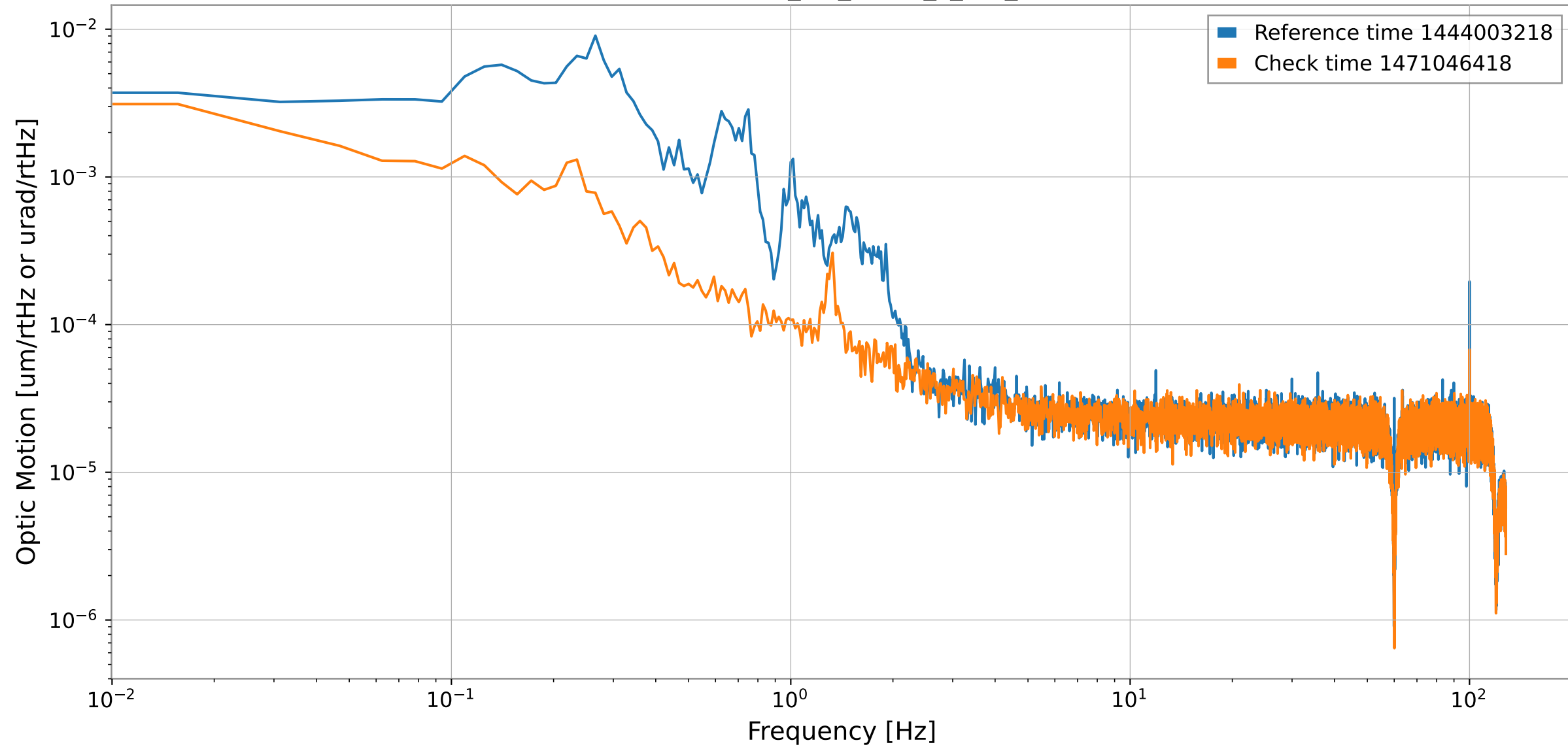


H1:SUS-OM2_M1_DAMP_Y_IN1_DQ

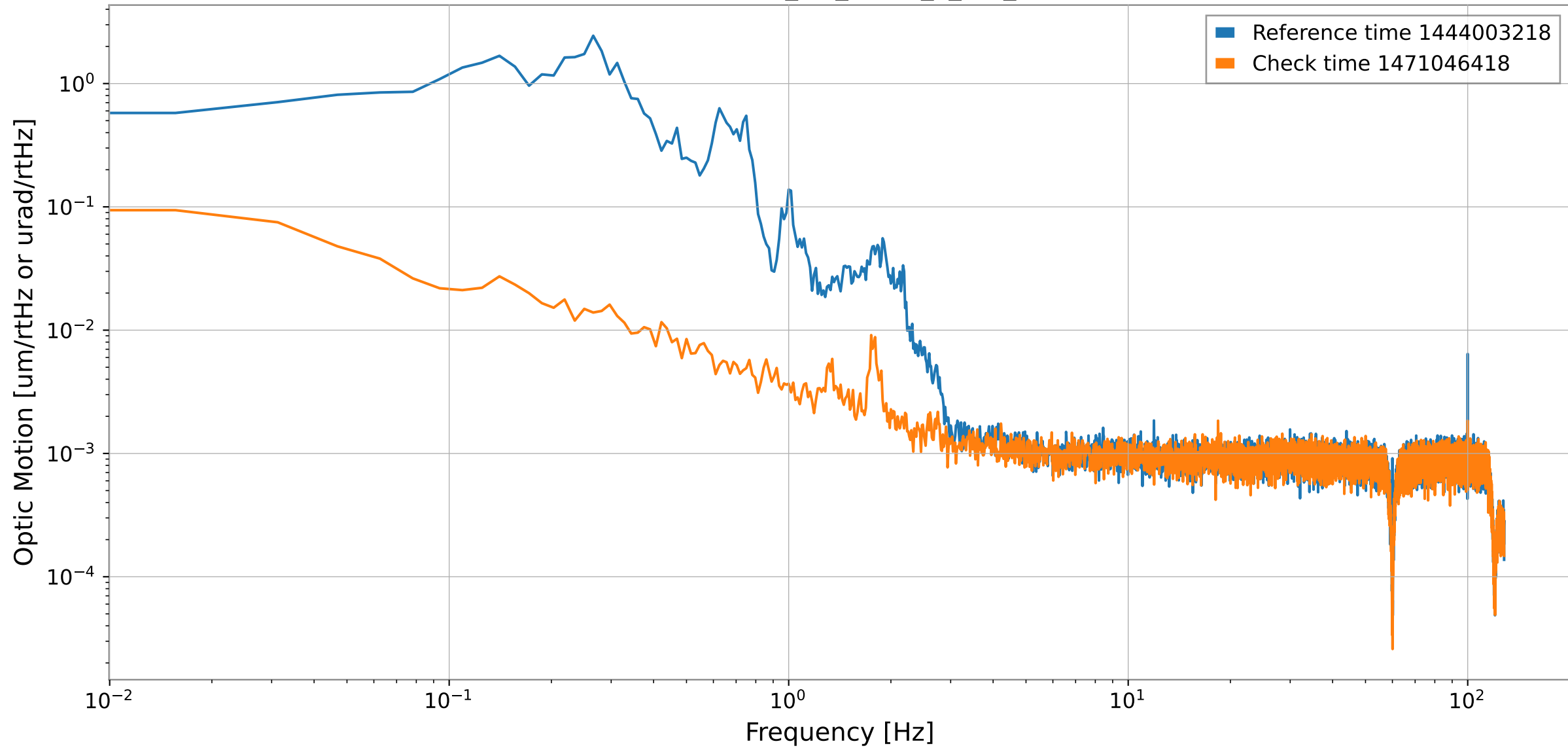


OM3

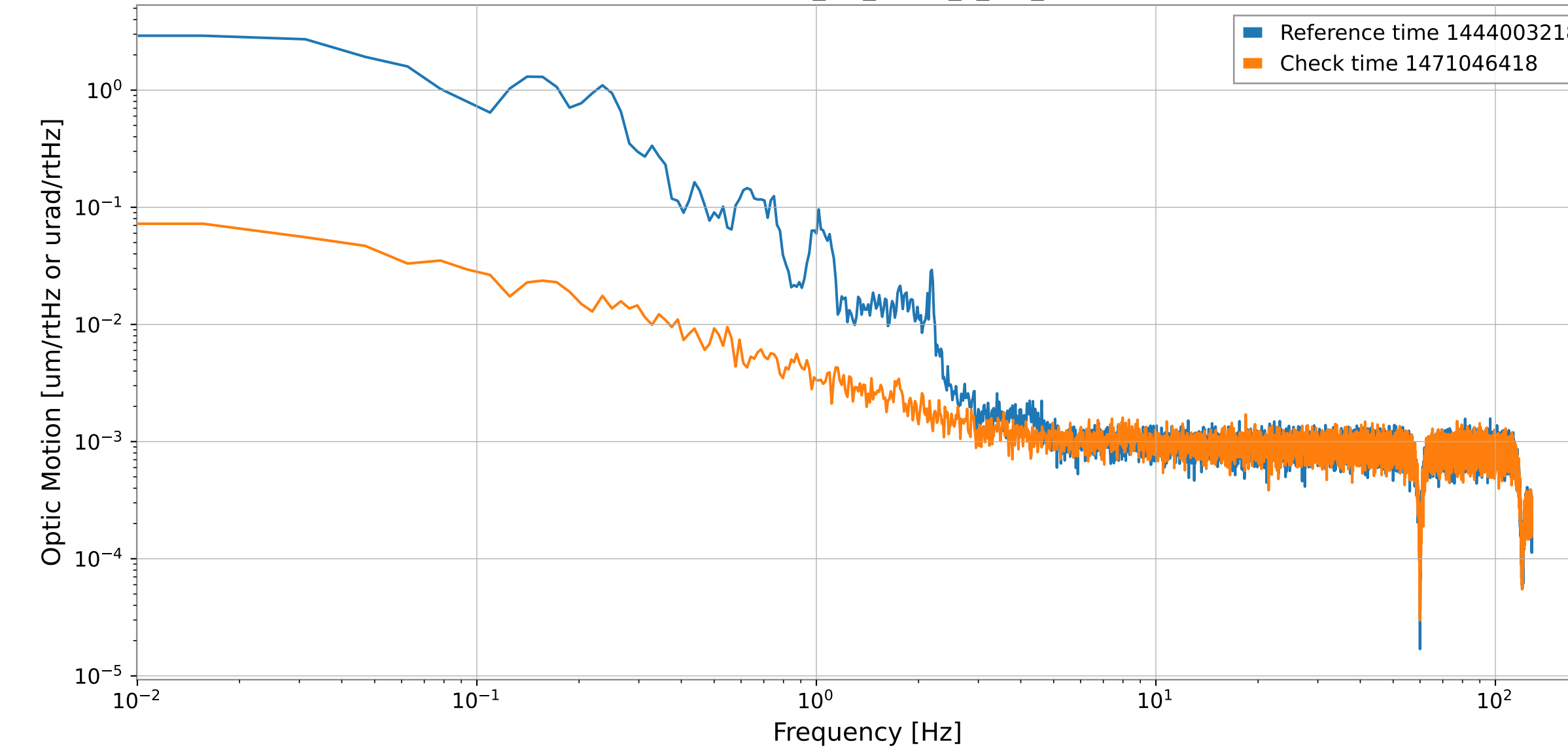
H1:SUS-OM3_M1_DAMP_L_IN1_DQ



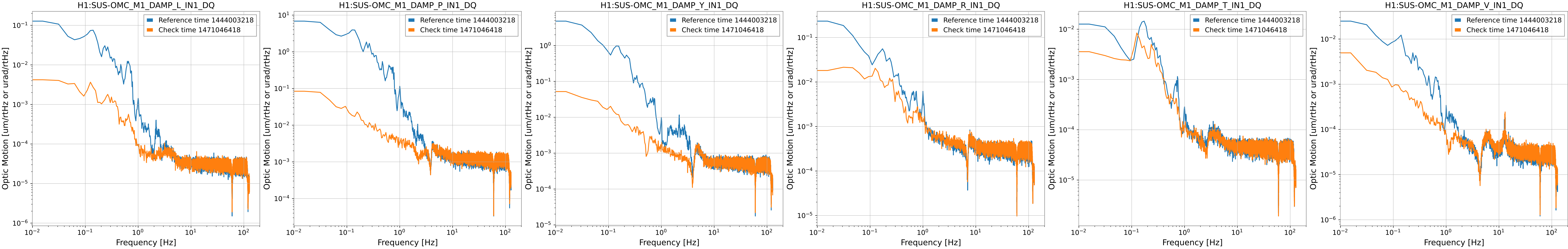
H1:SUS-OM3_M1_DAMP_P_IN1_DQ



H1:SUS-OM3_M1_DAMP_Y_IN1_DQ



OMC



OFI

