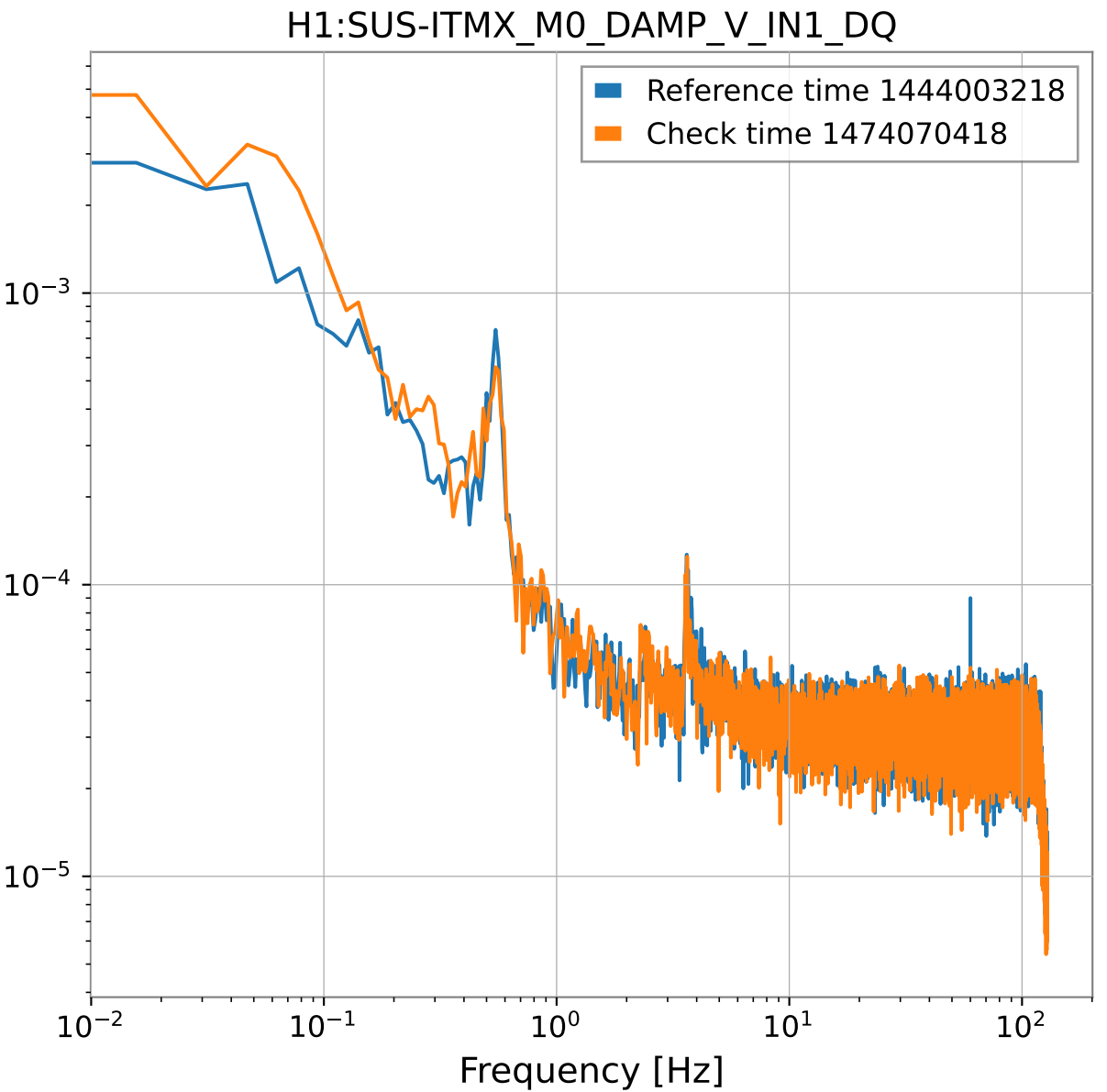
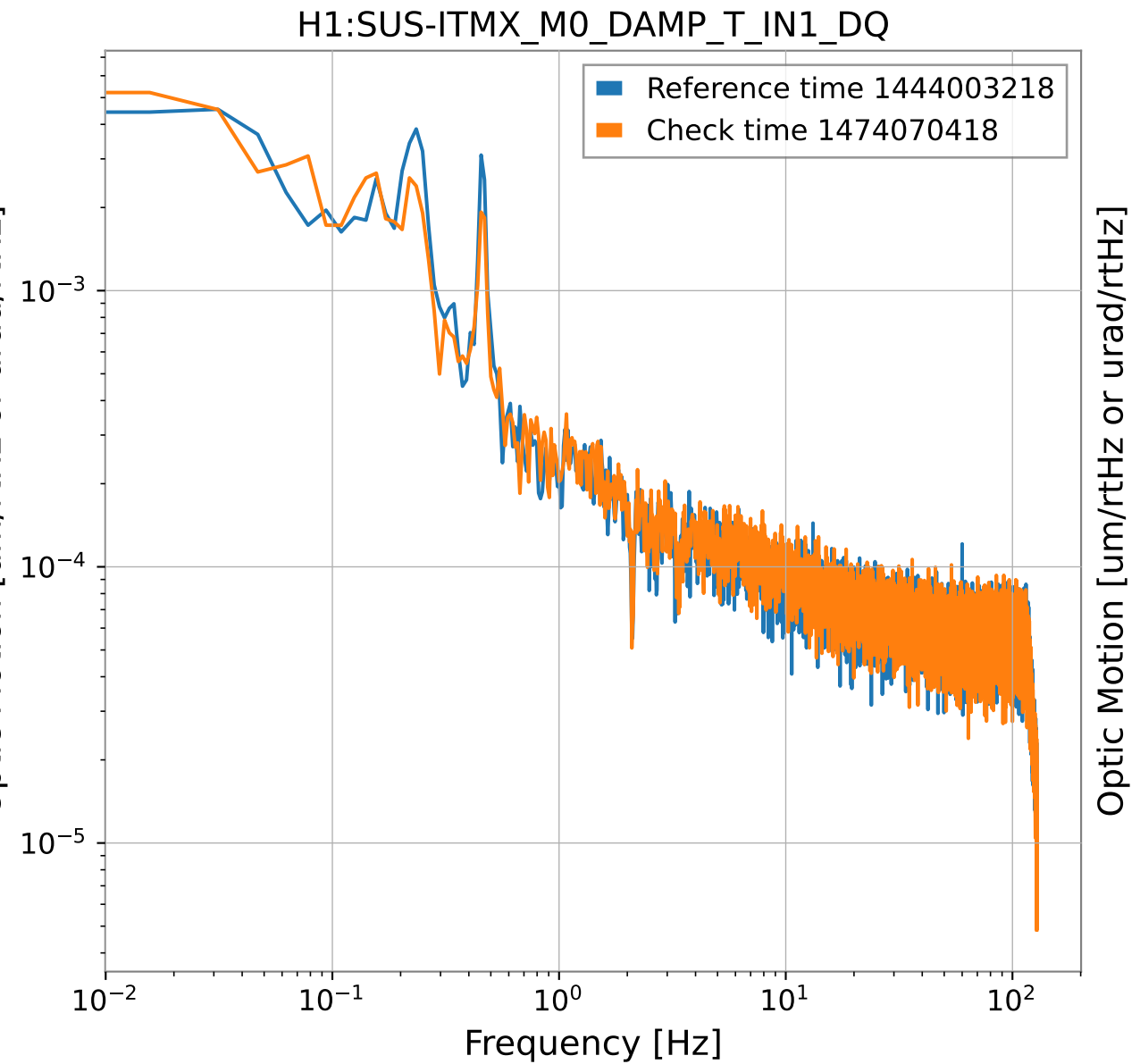
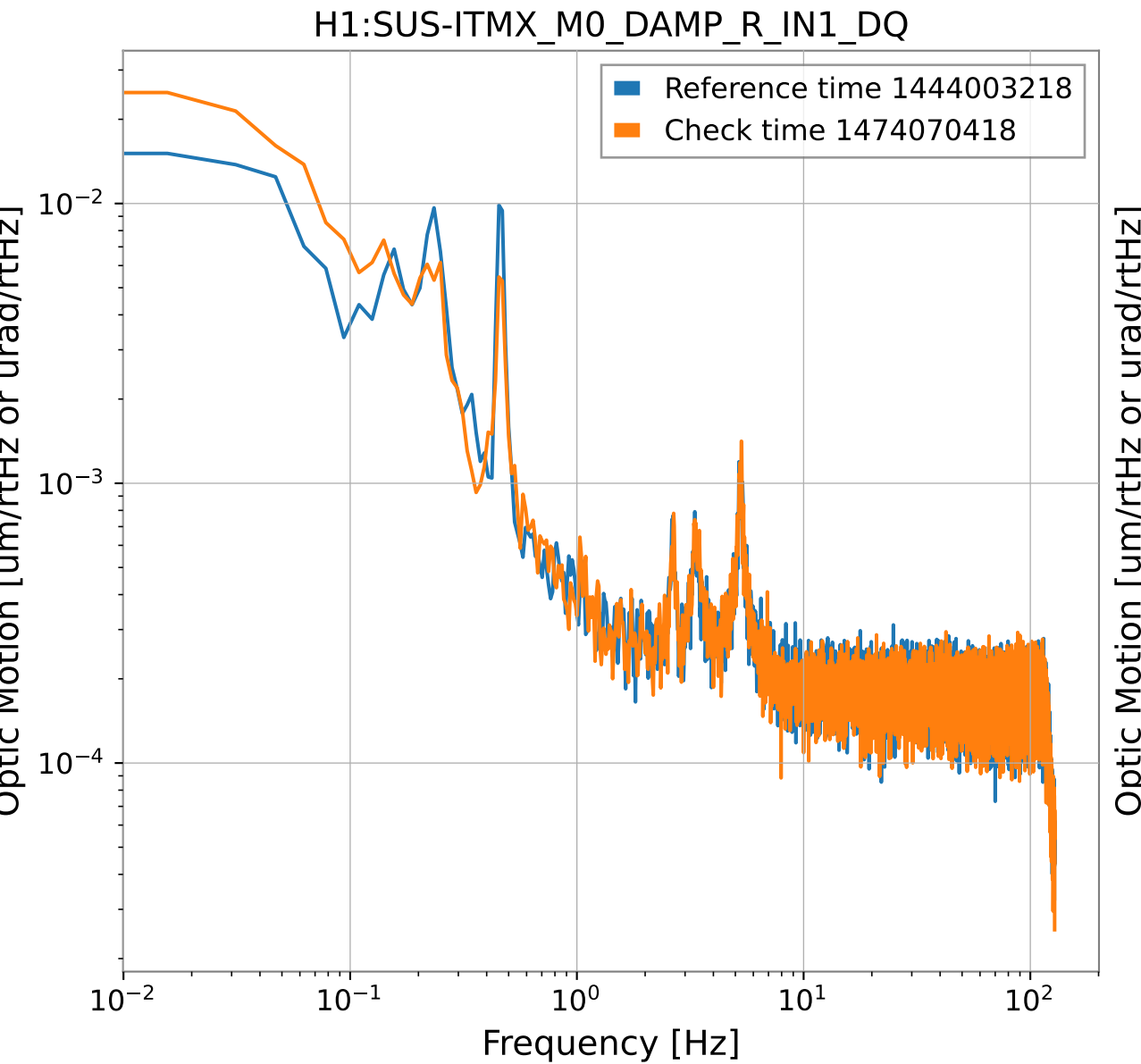
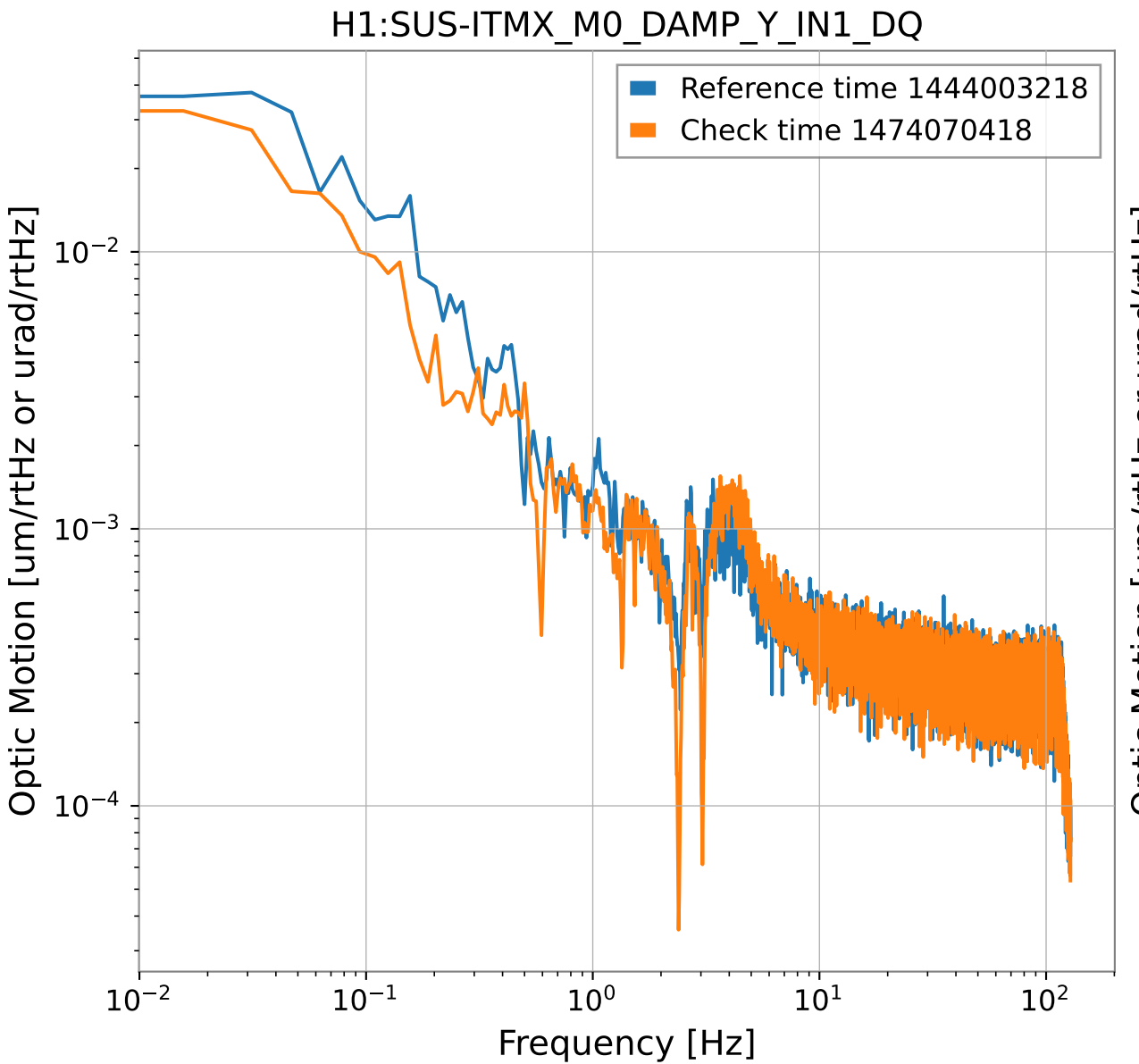
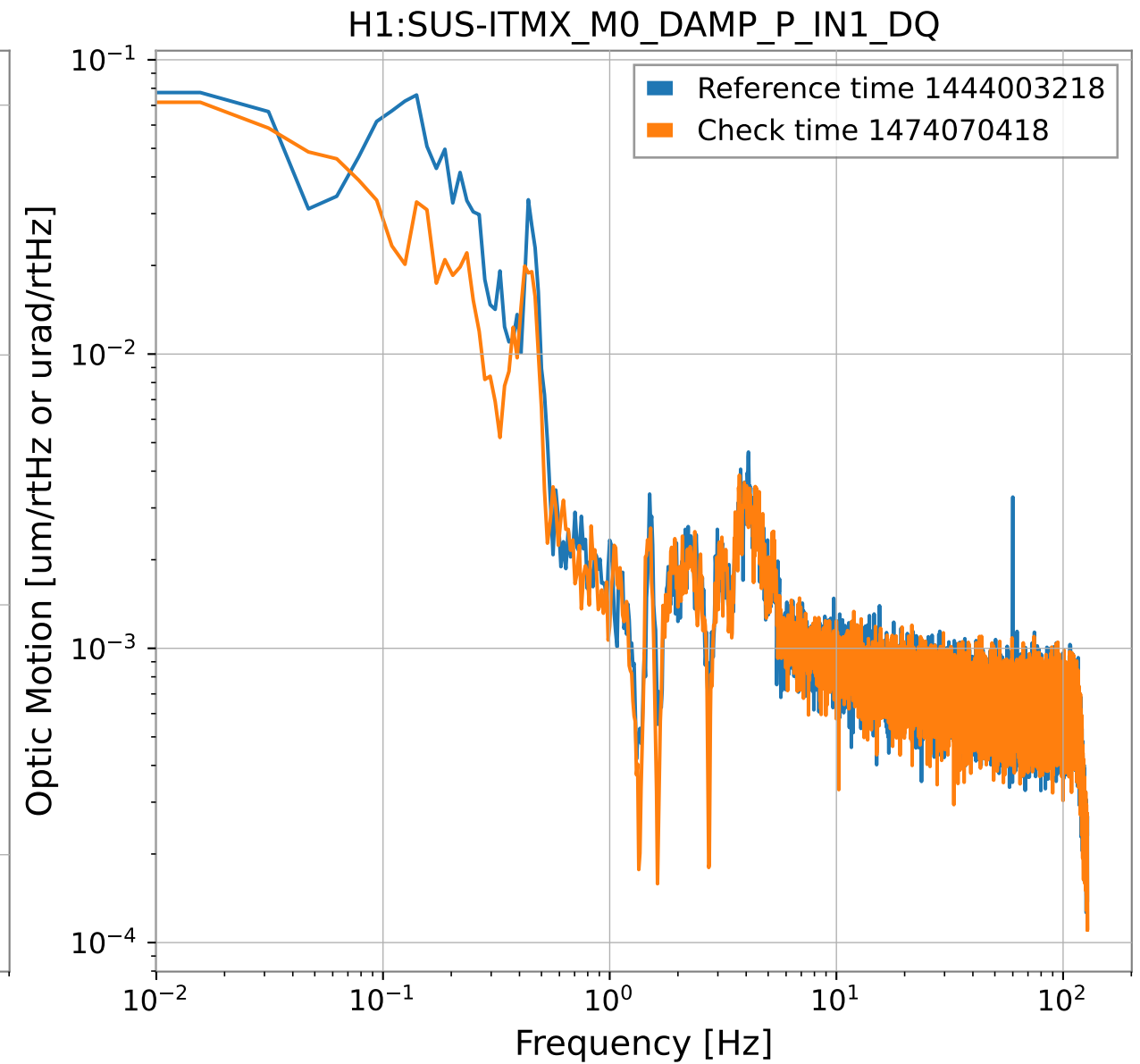
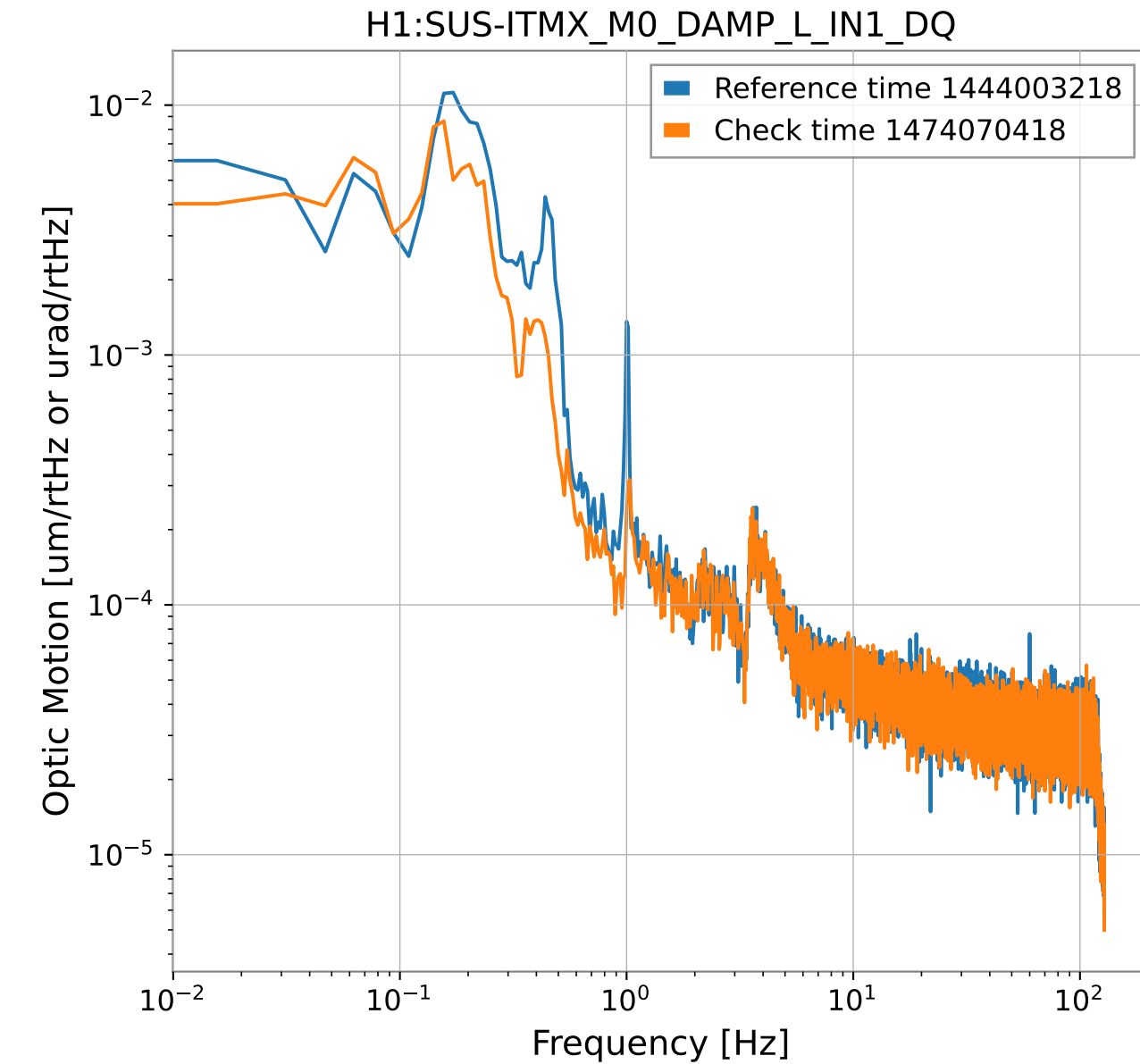
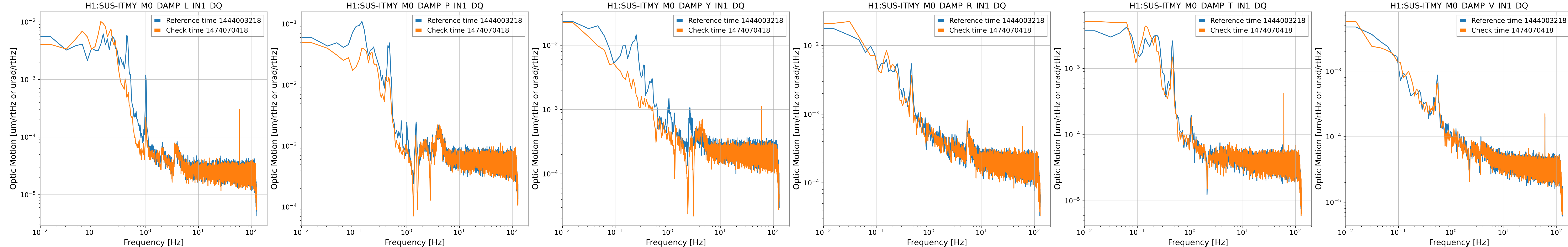


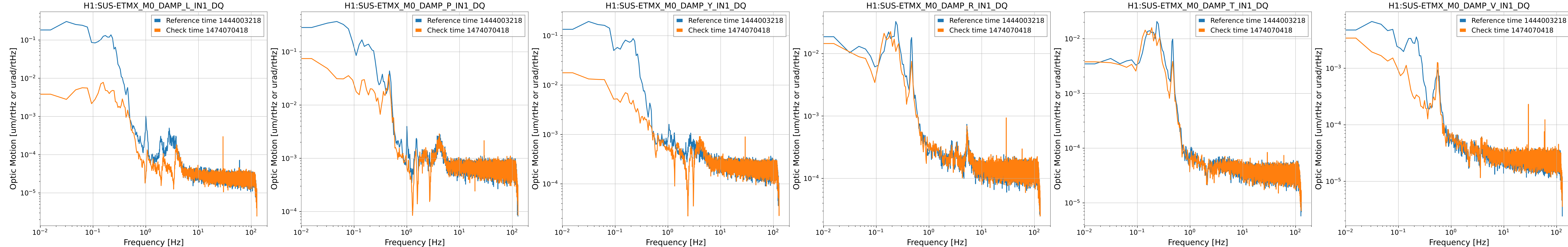
ITMX



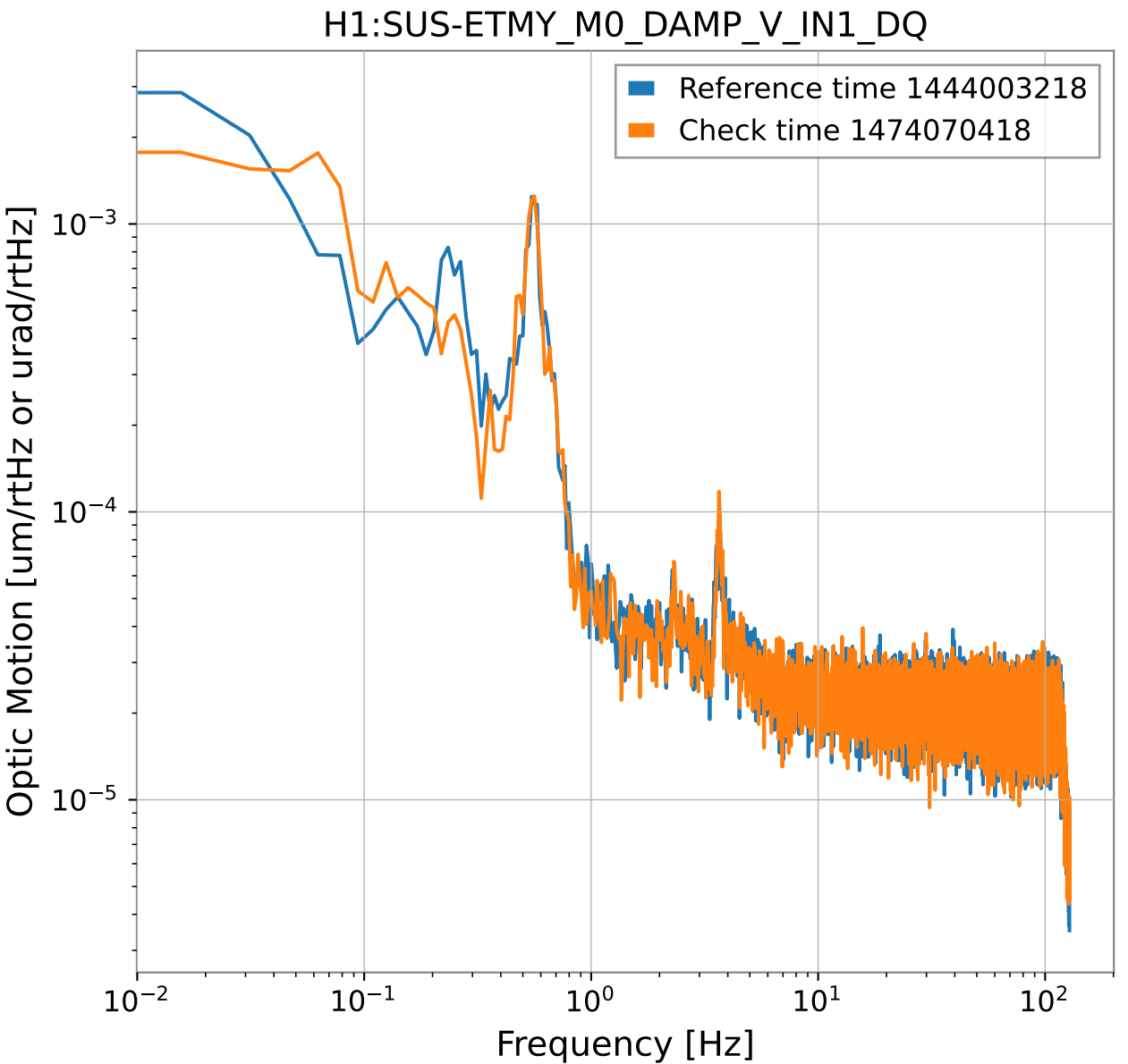
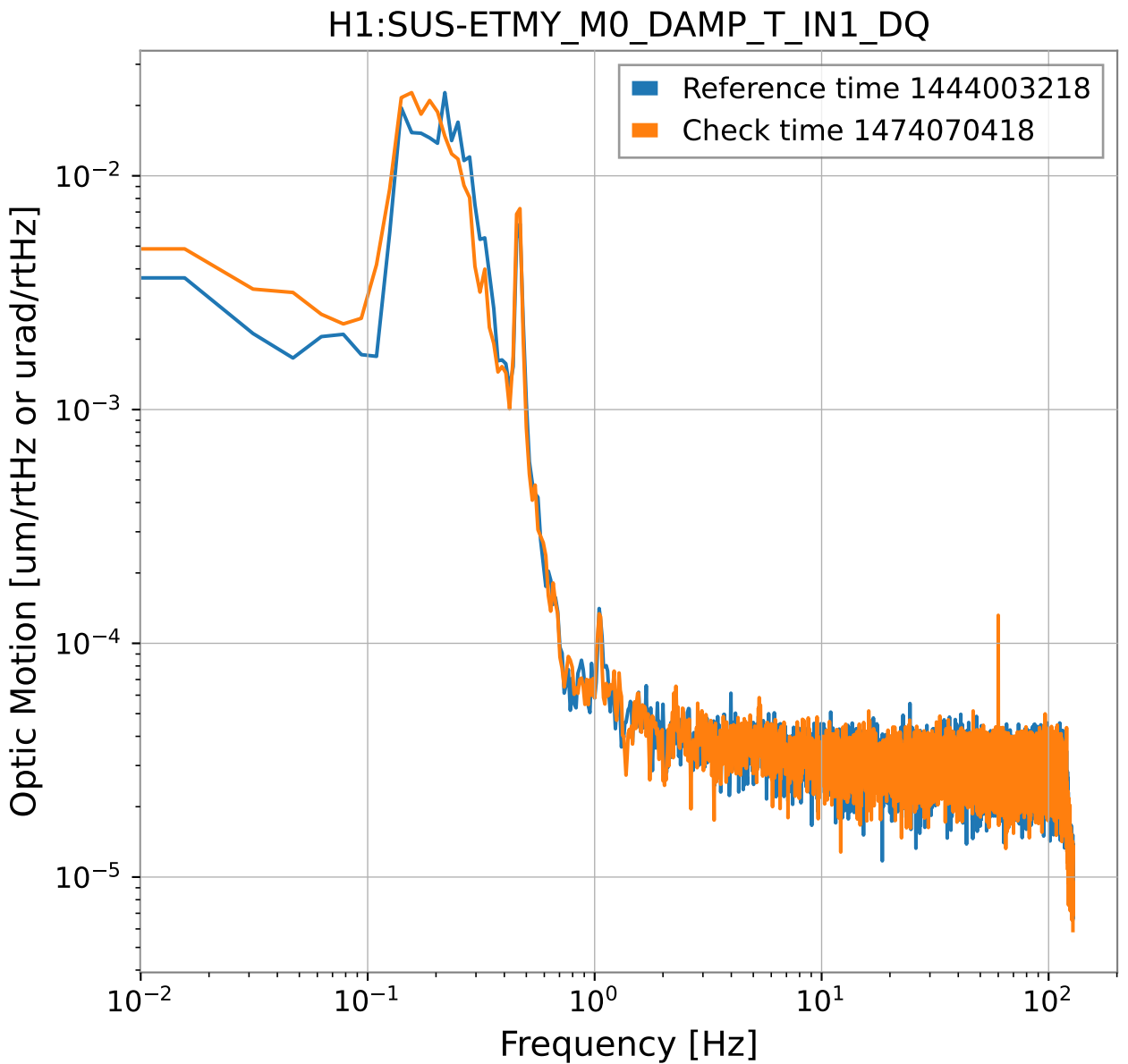
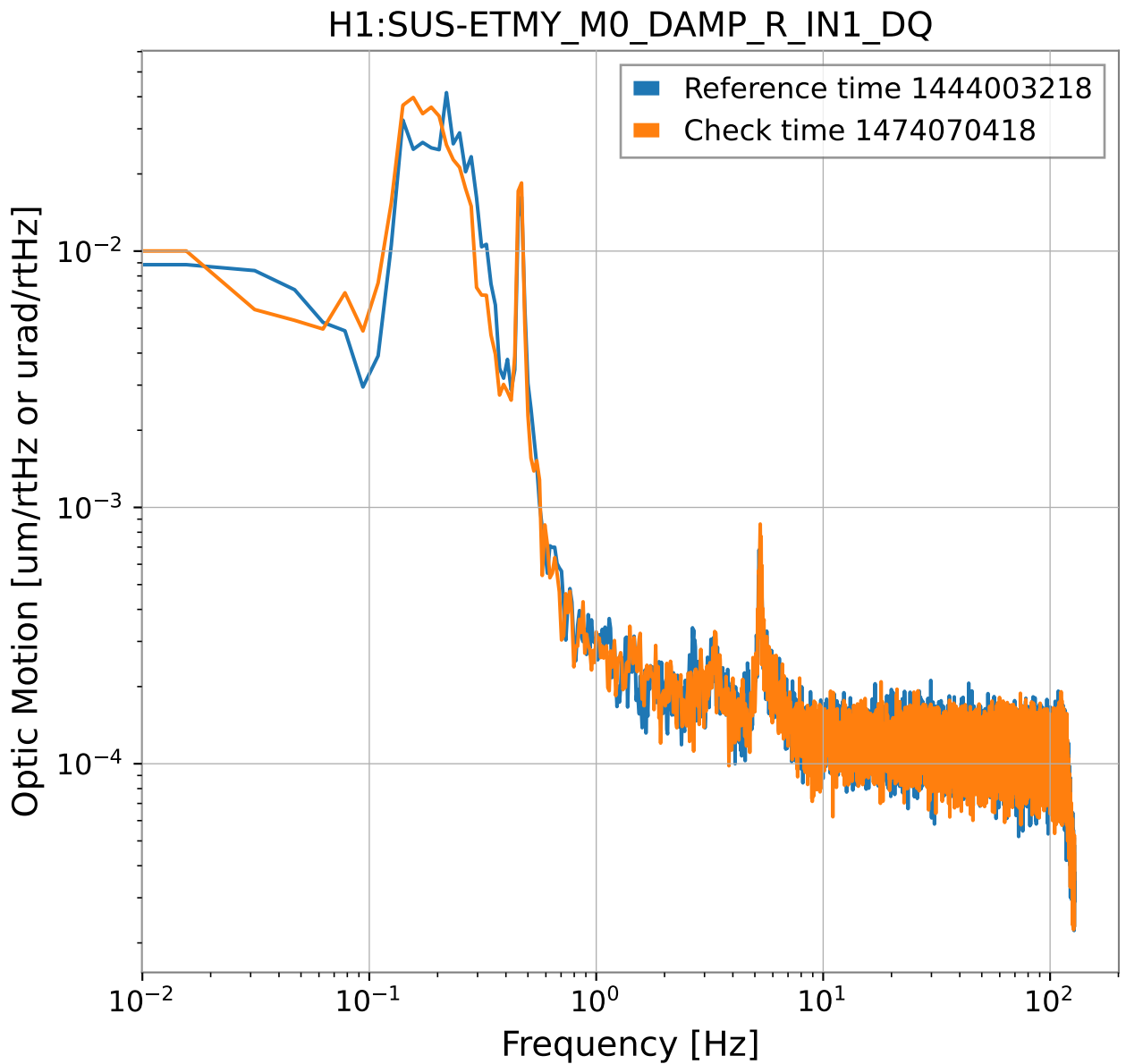
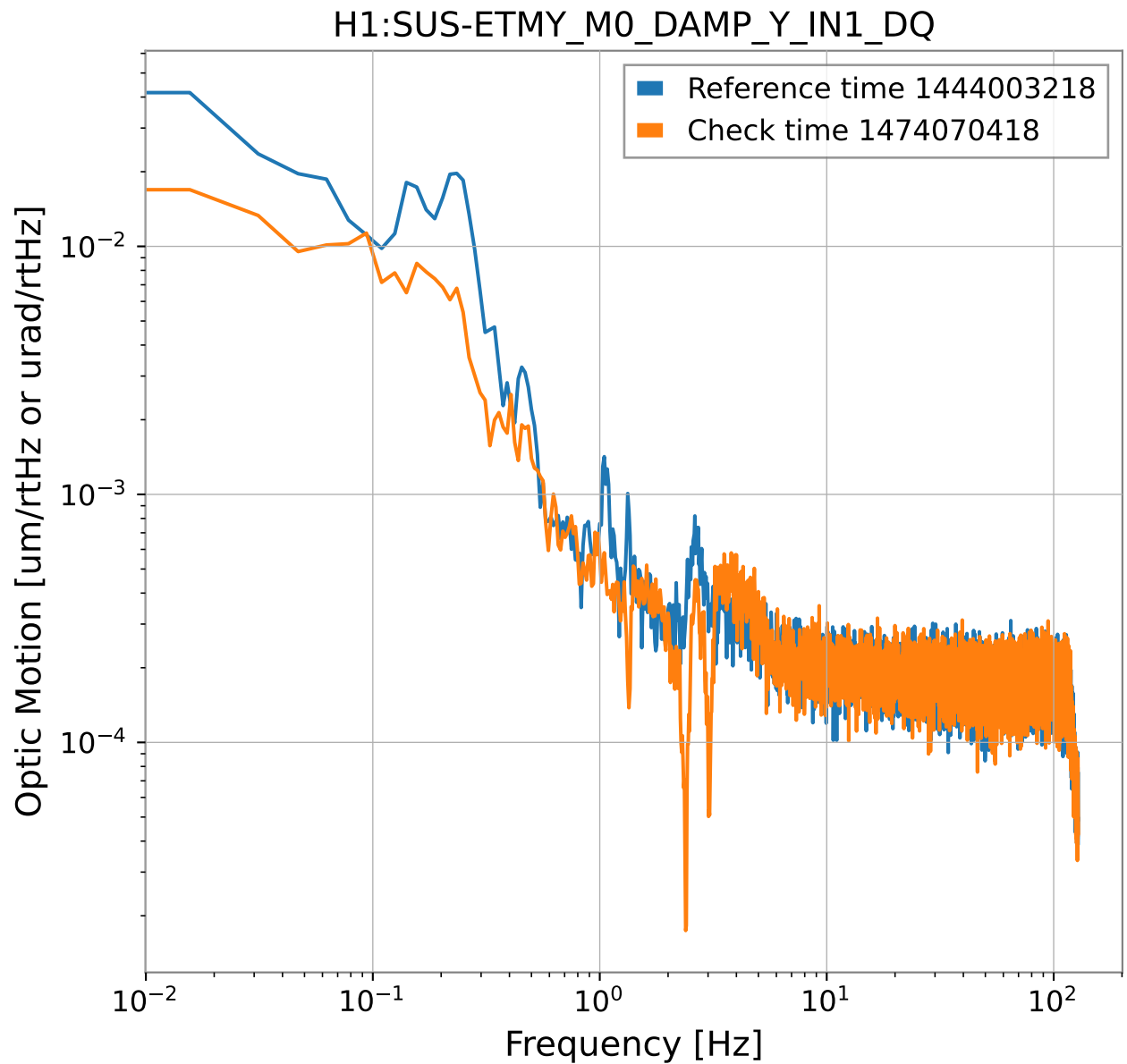
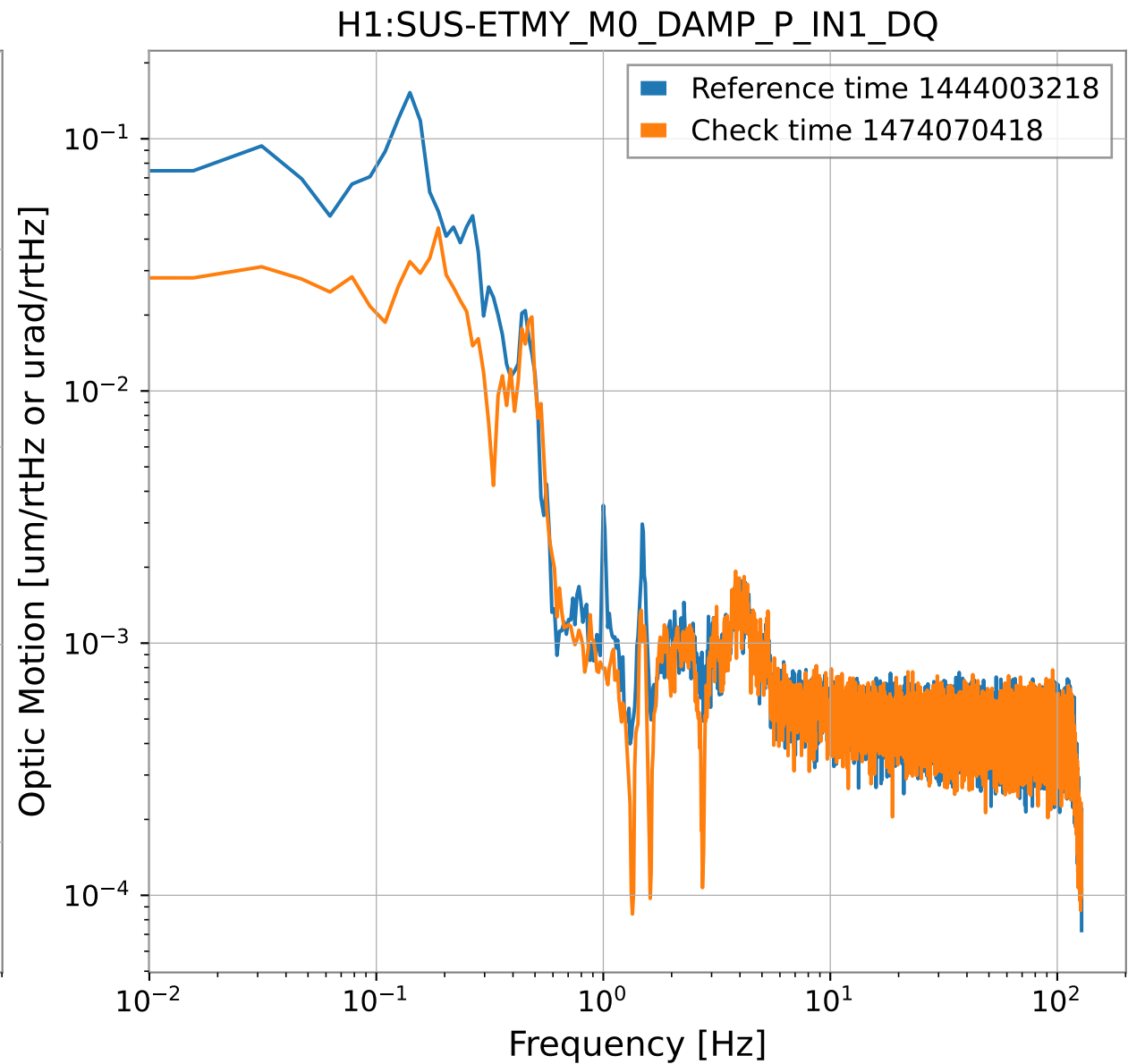
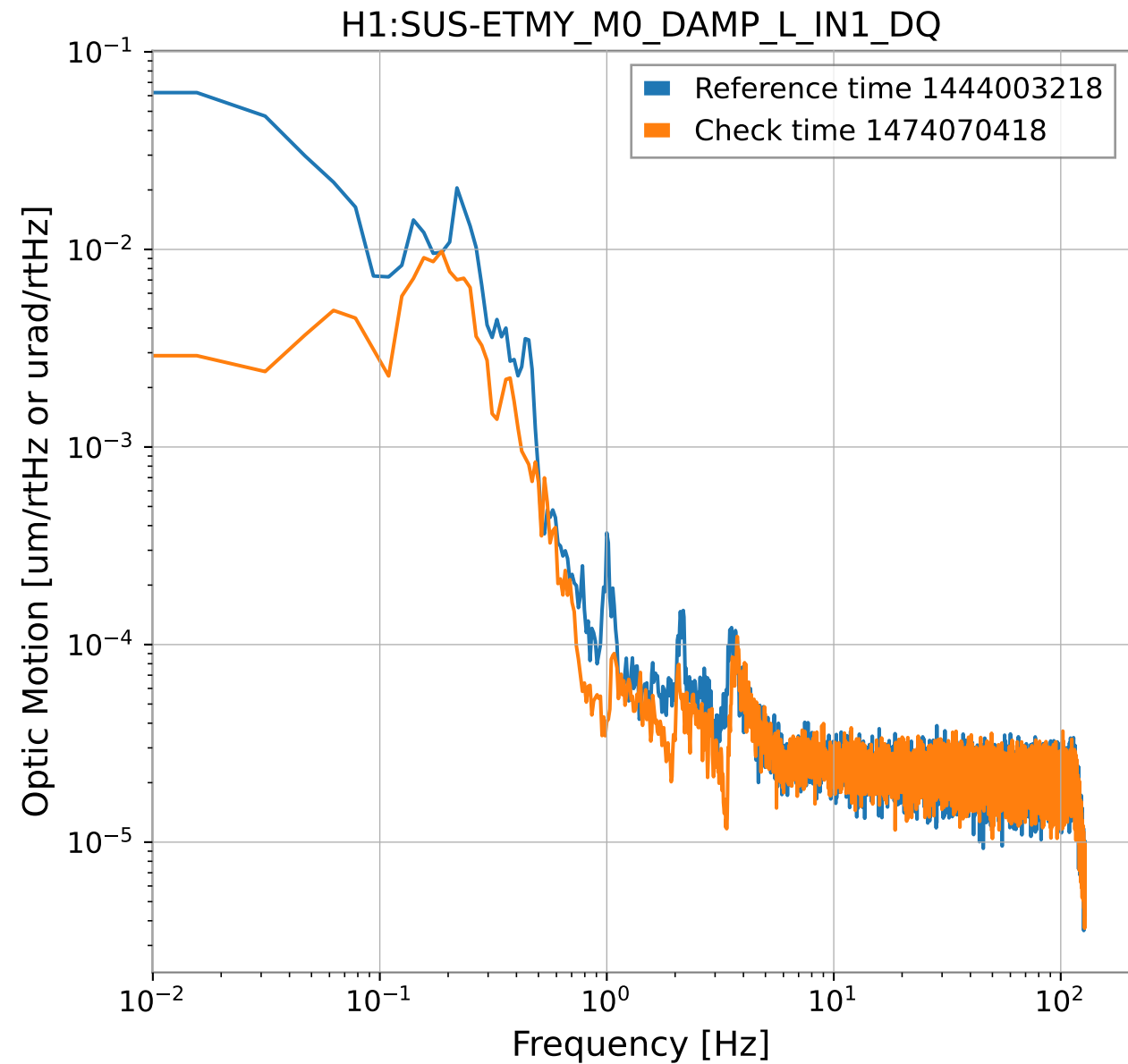
ITMY



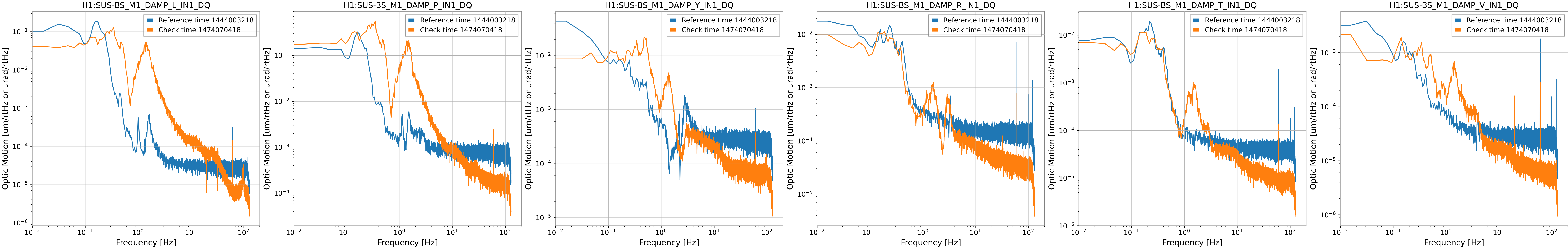
ETMX



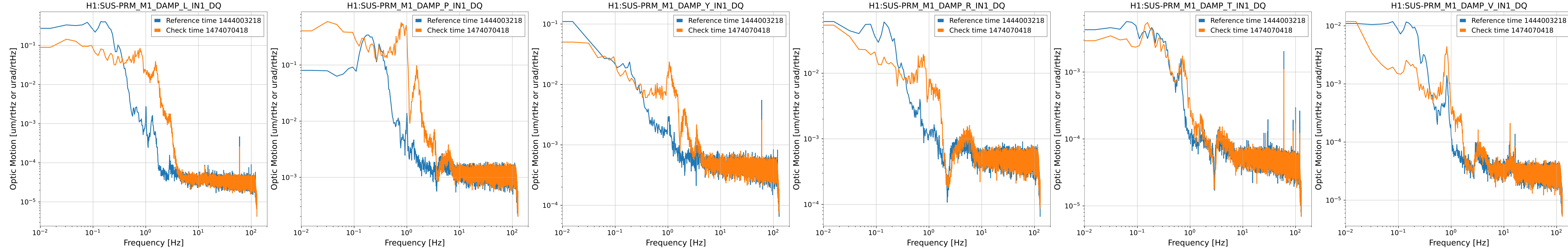
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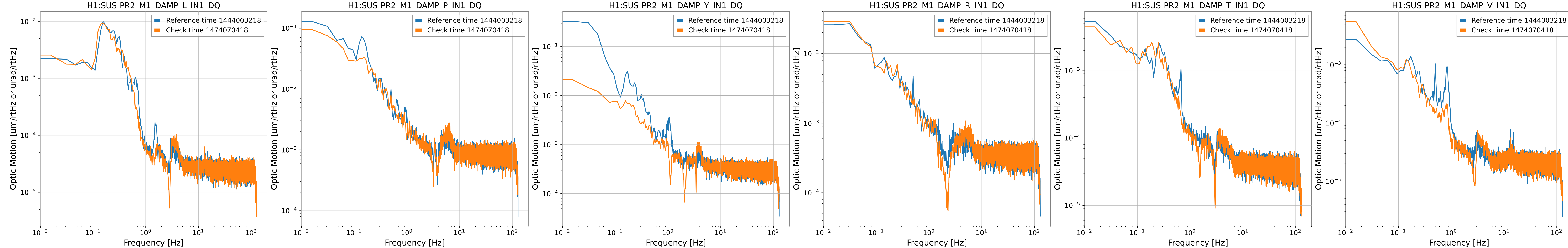
BS



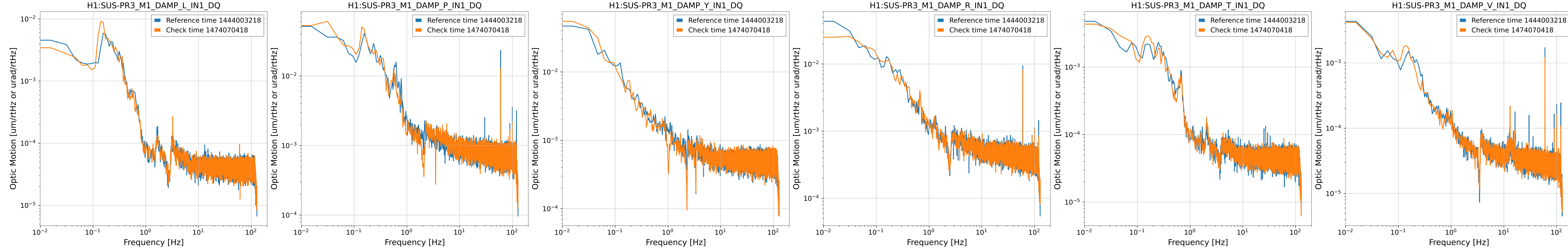
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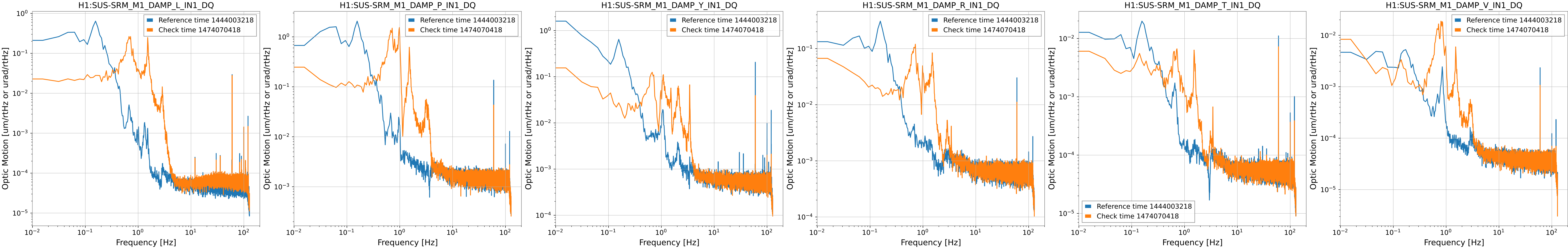
PR2



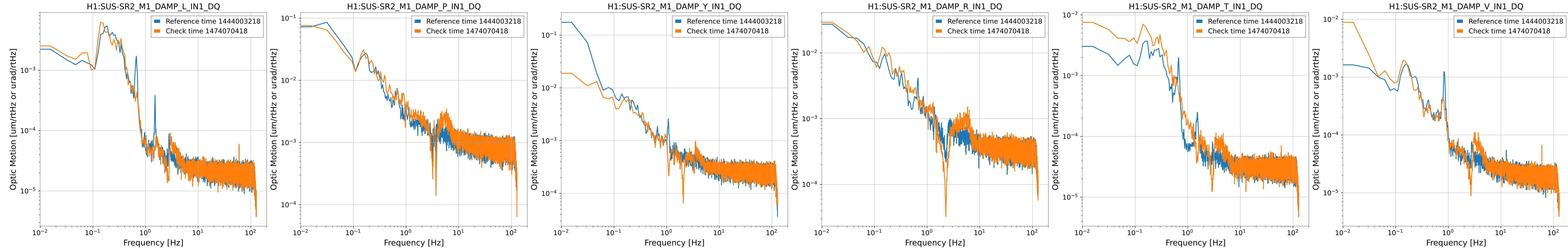
PR3



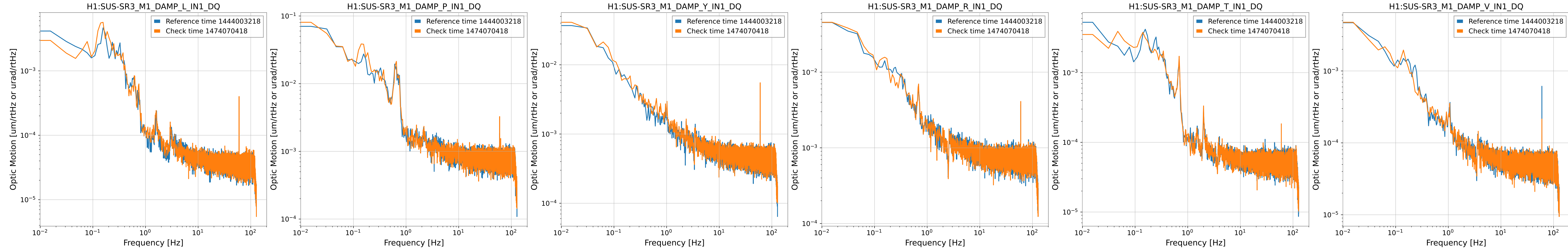
SRM



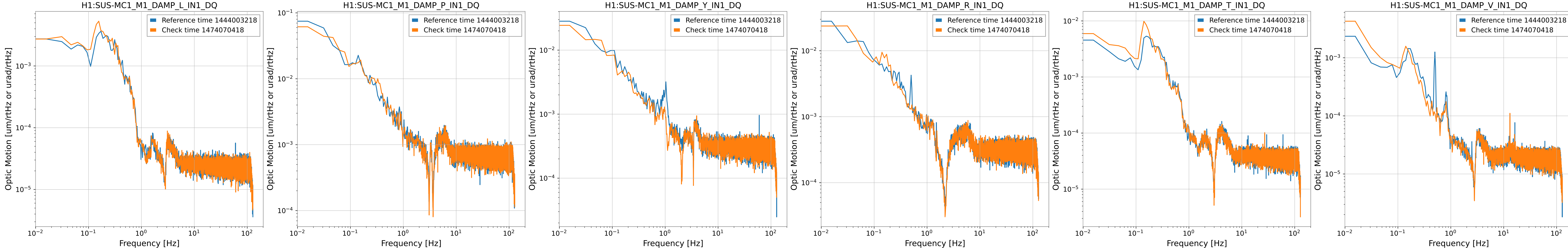
SR2



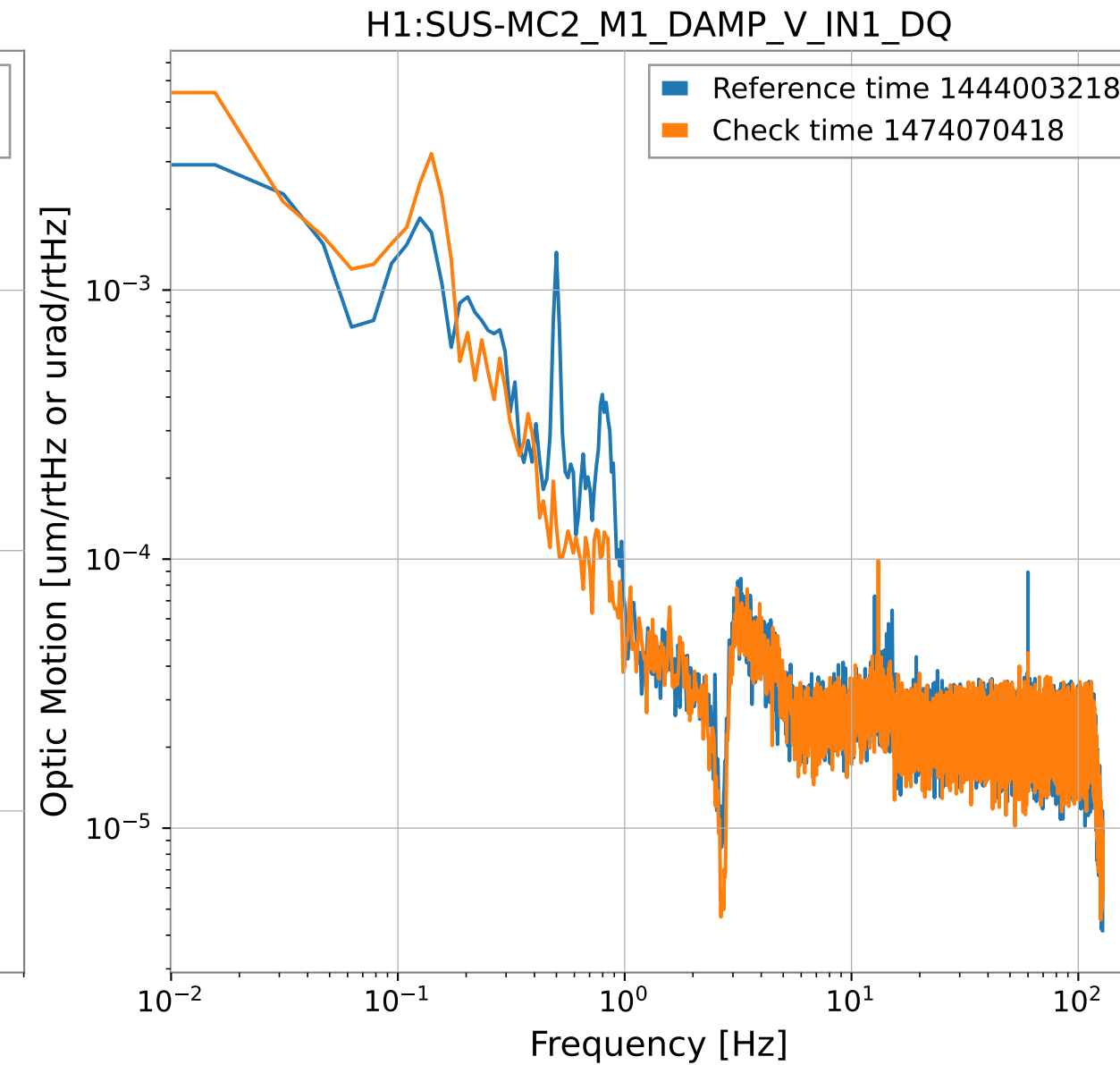
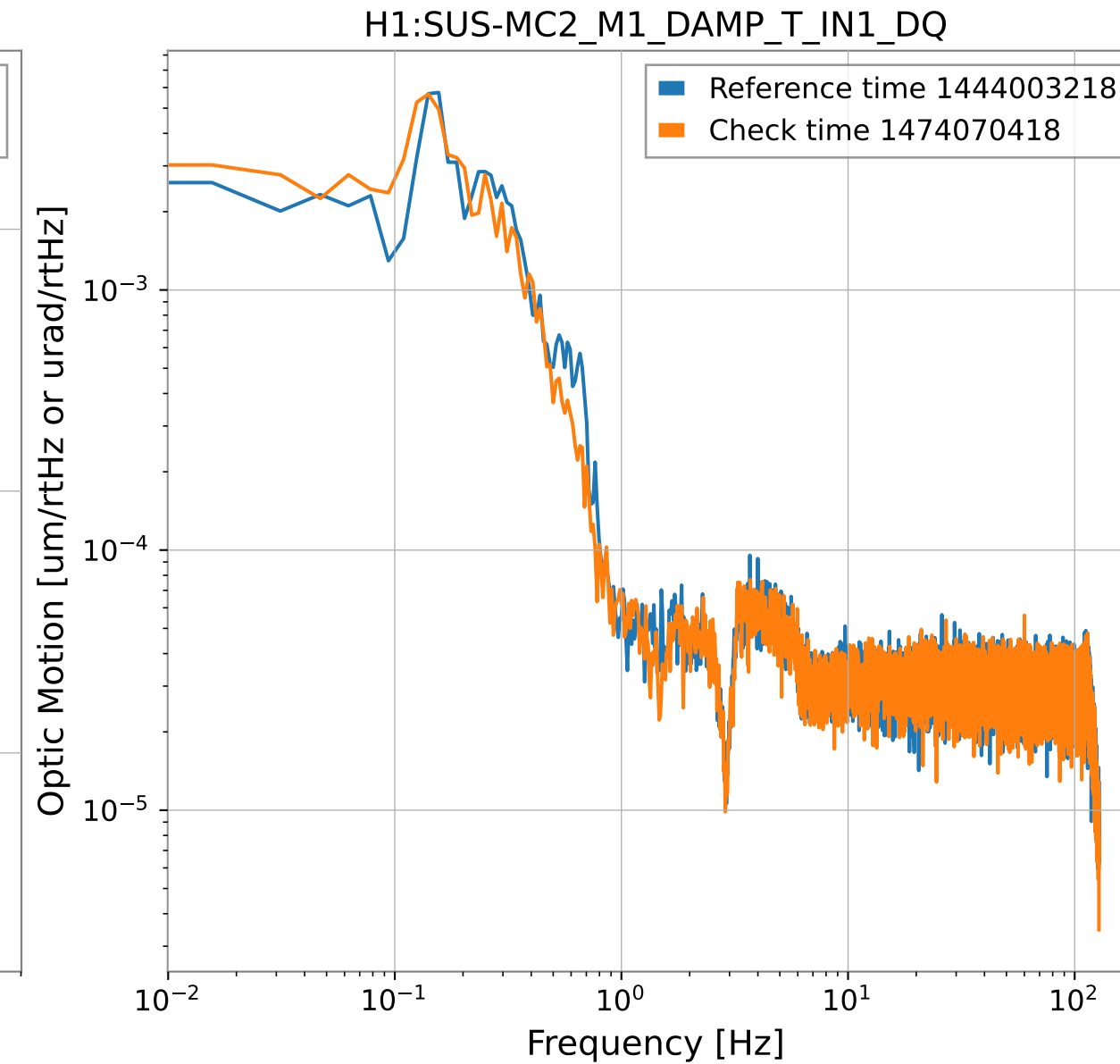
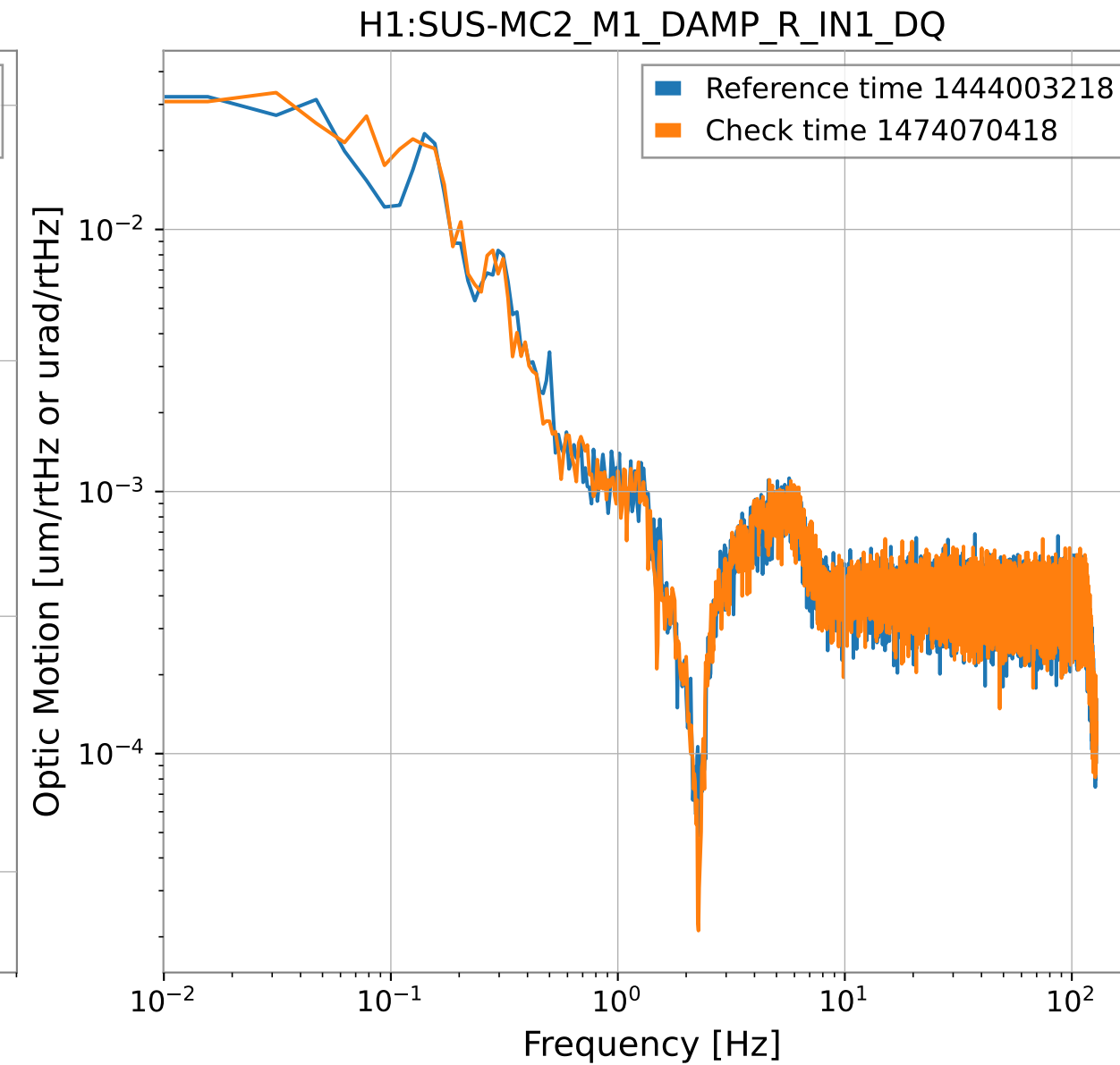
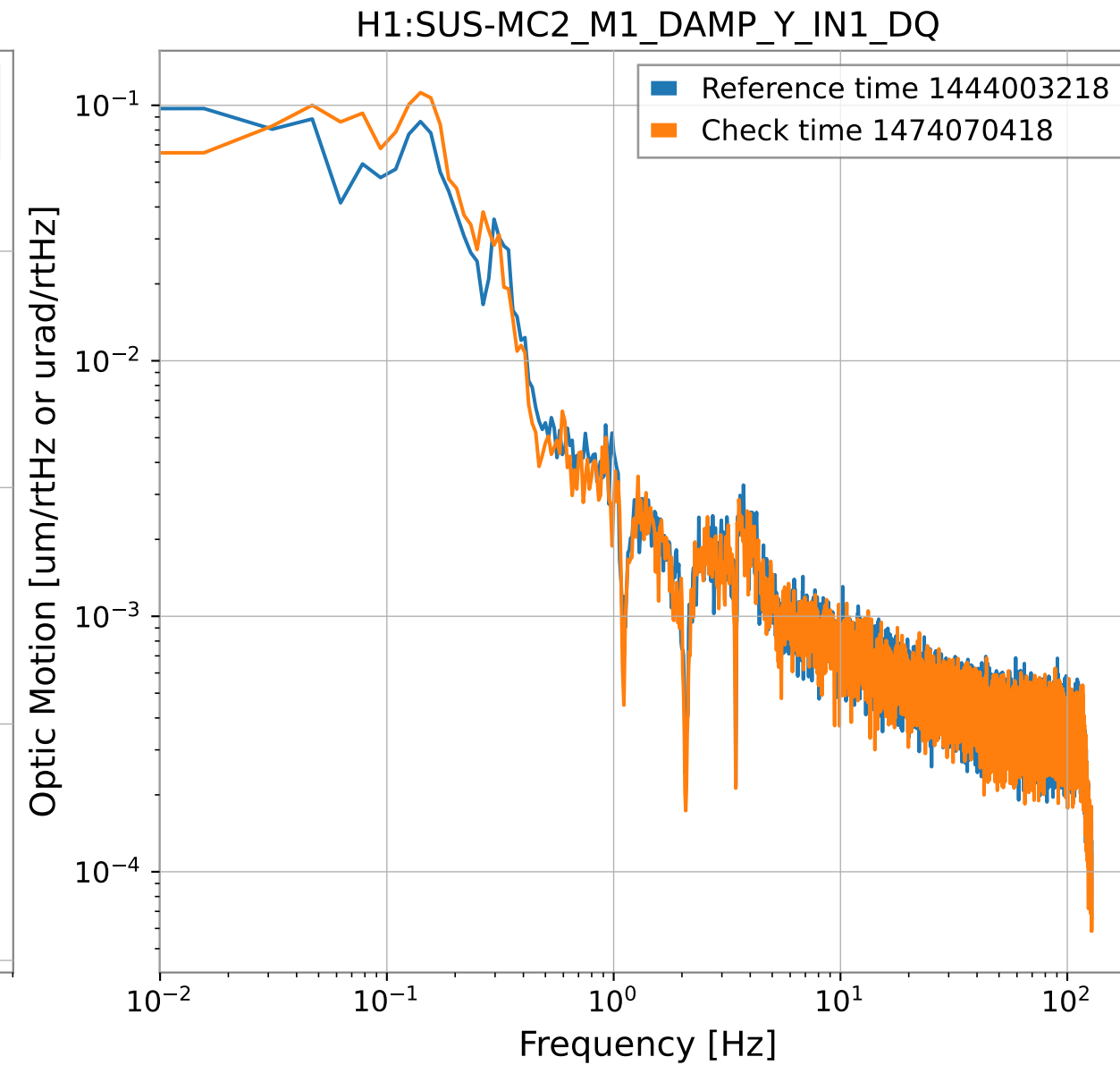
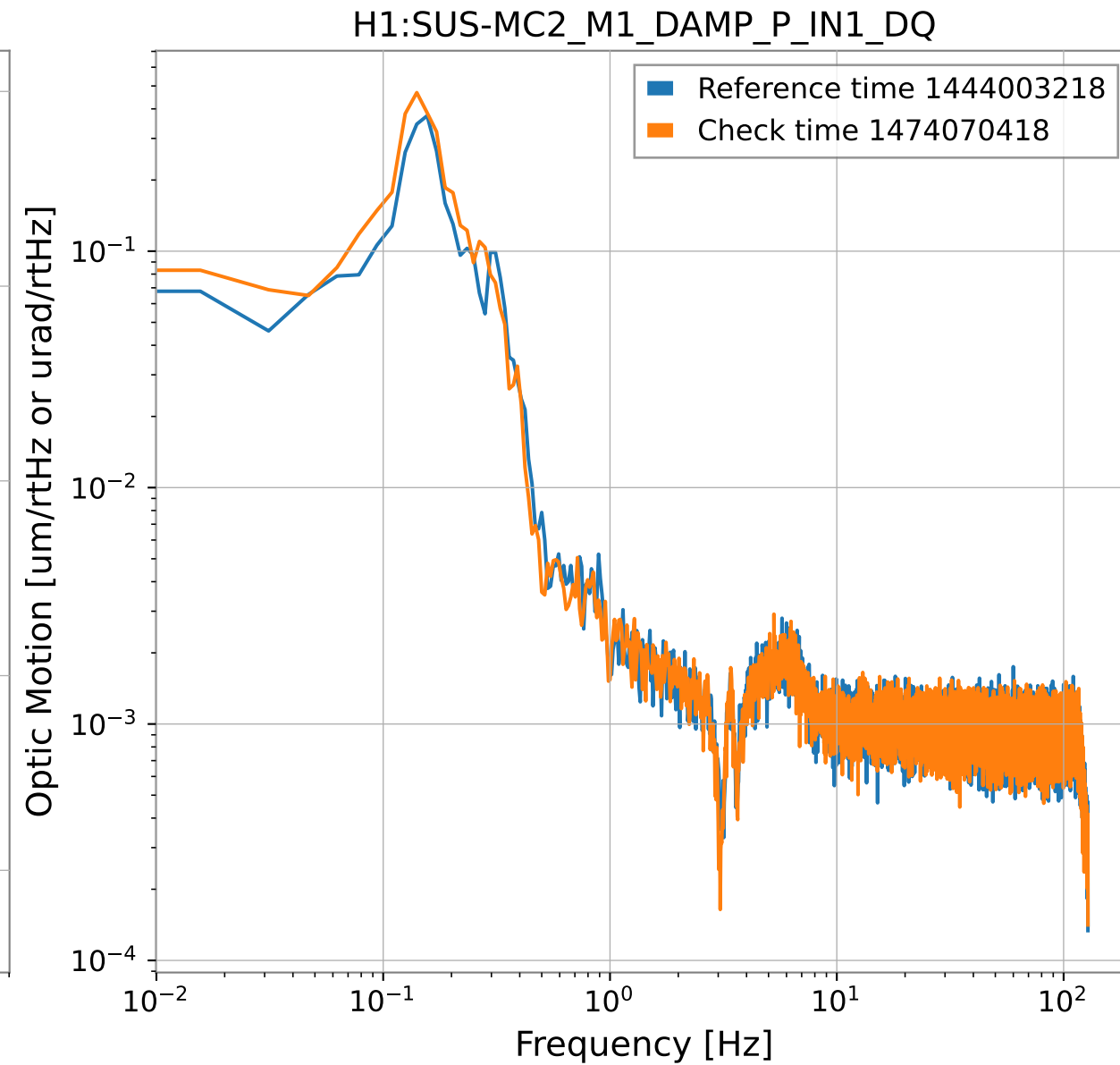
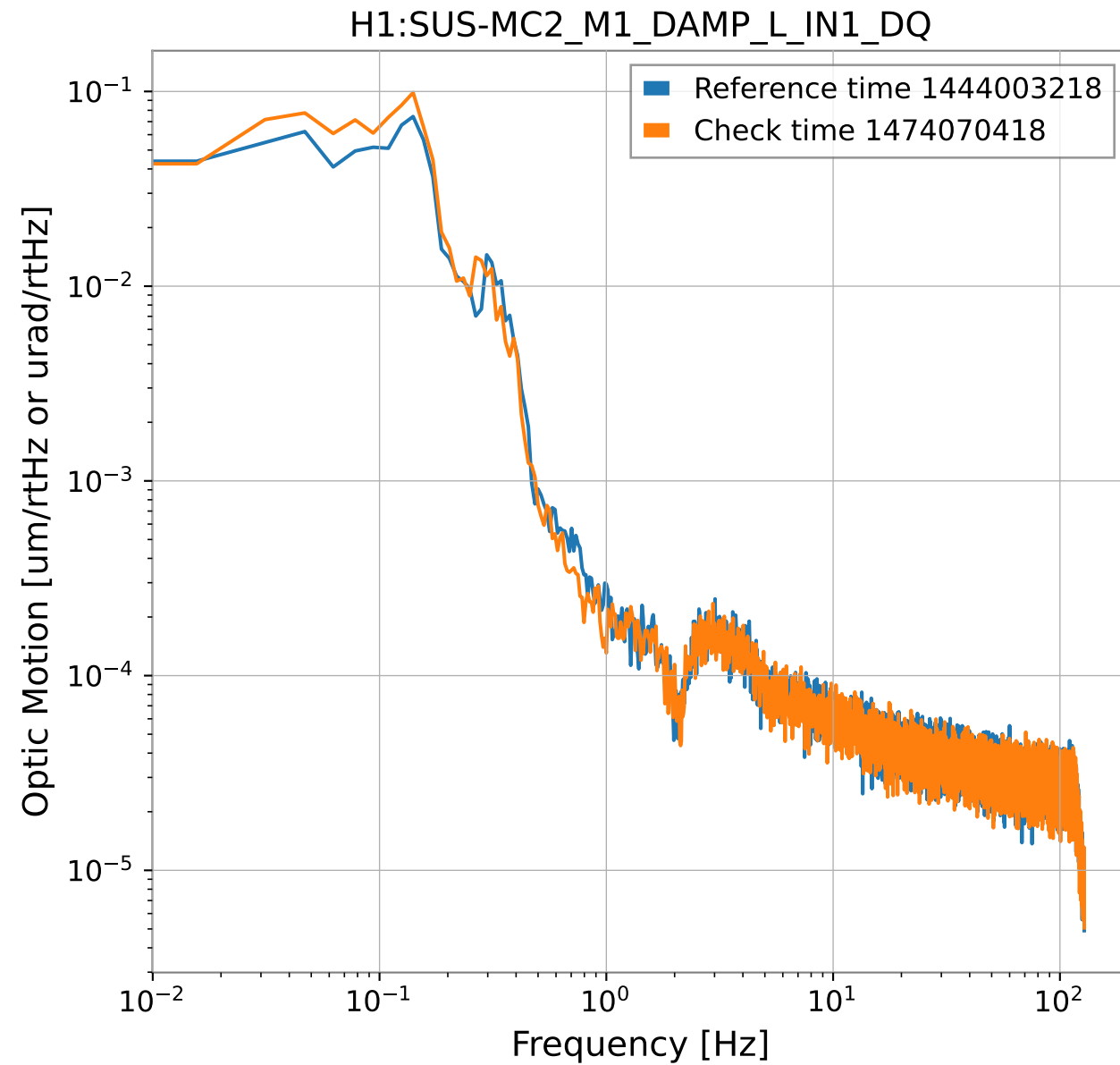
SR3



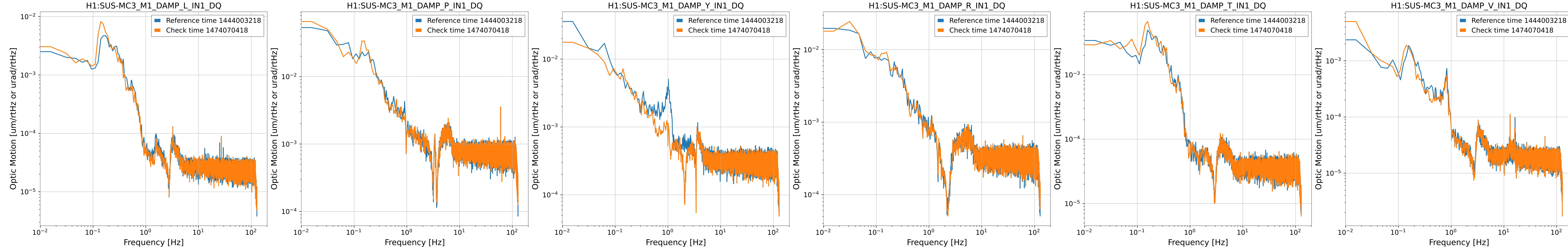
MC1



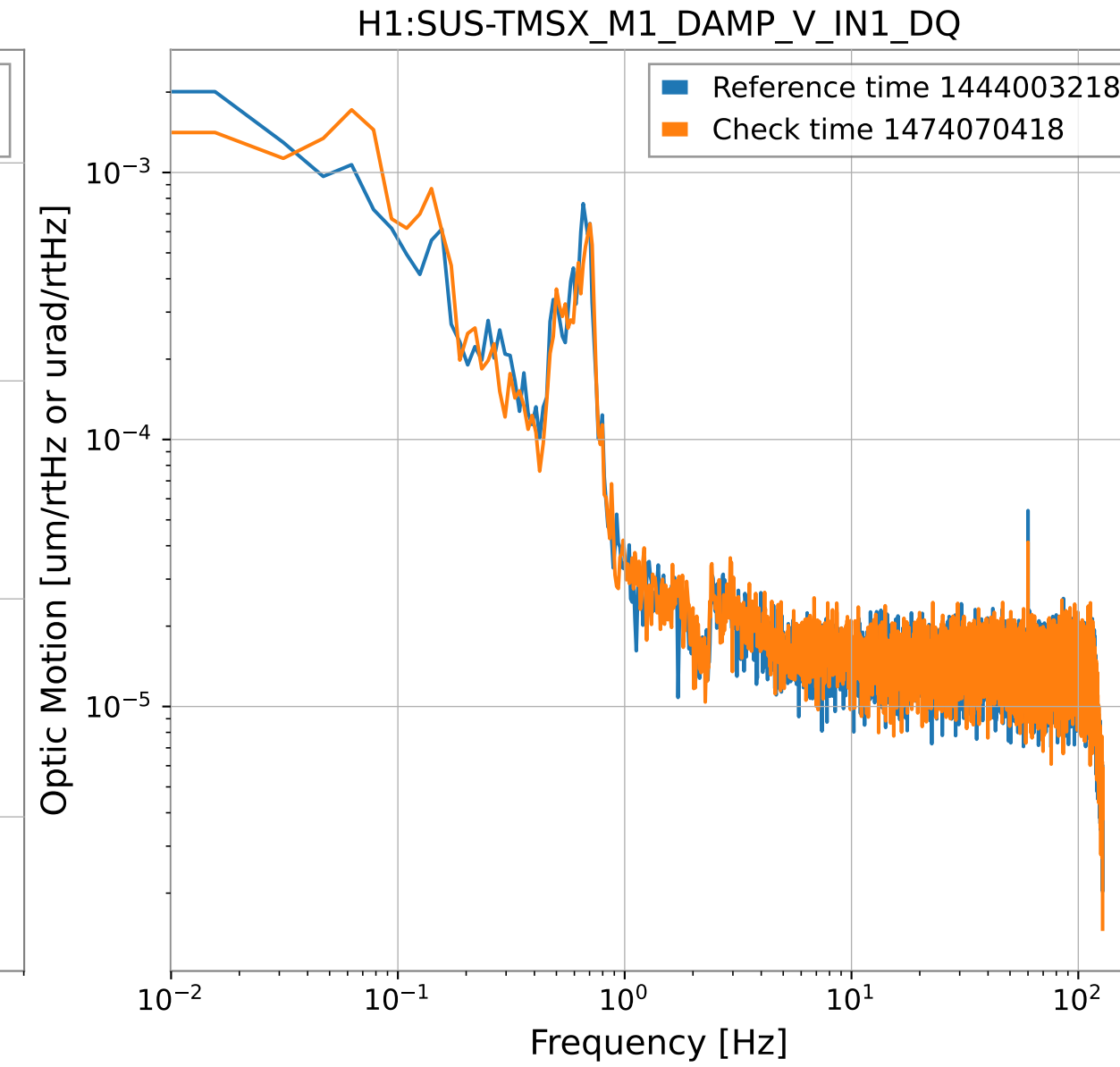
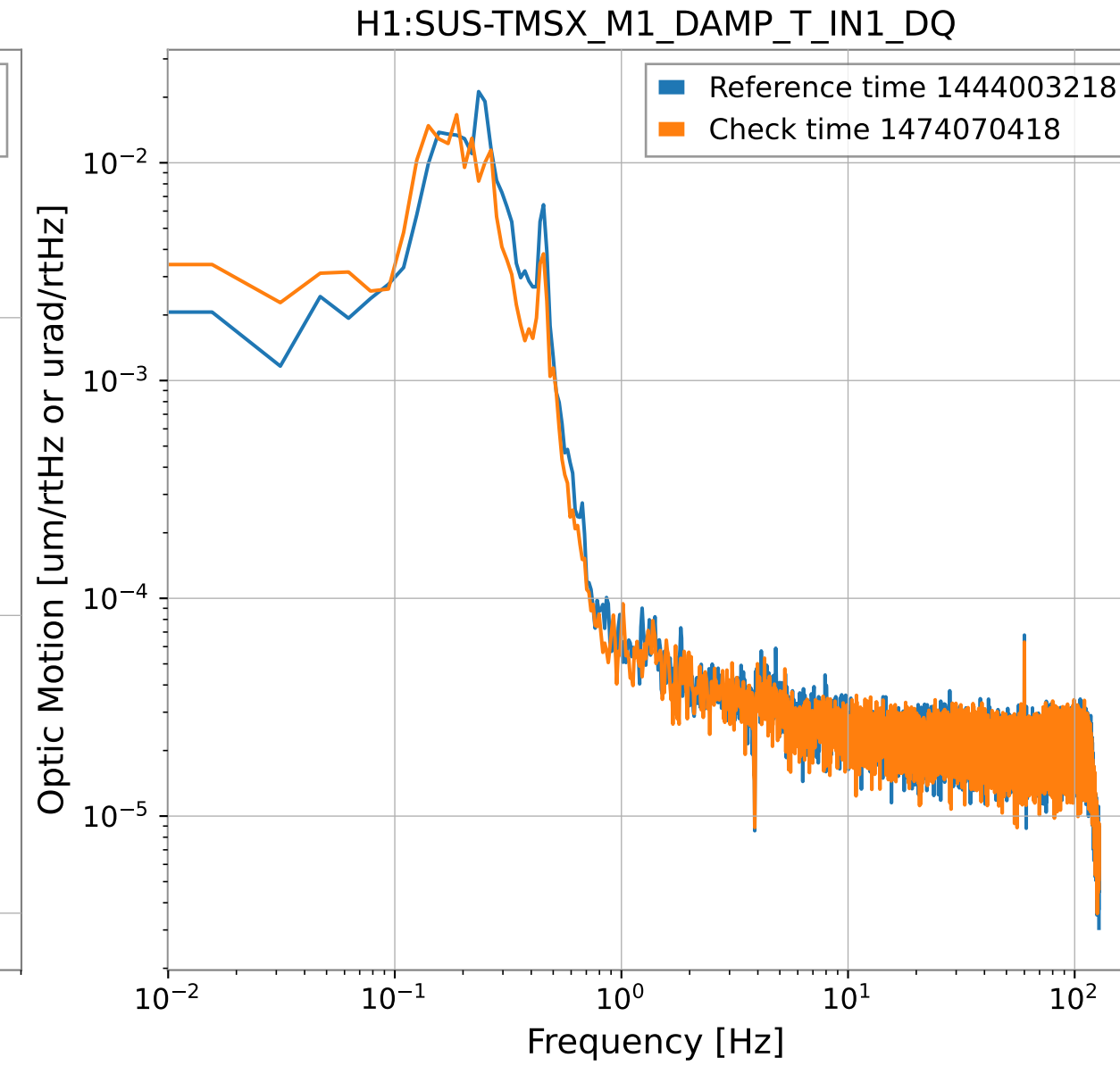
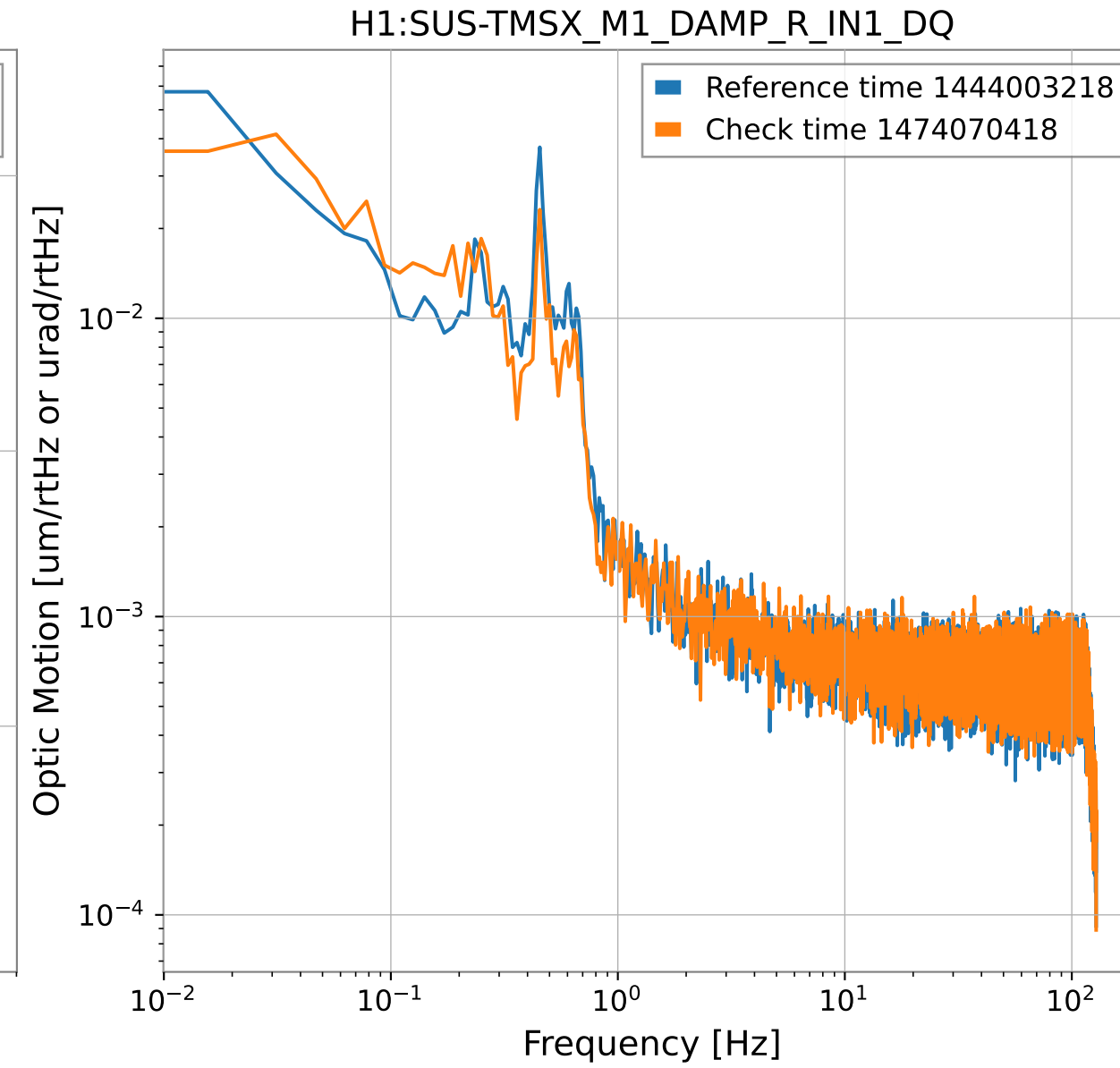
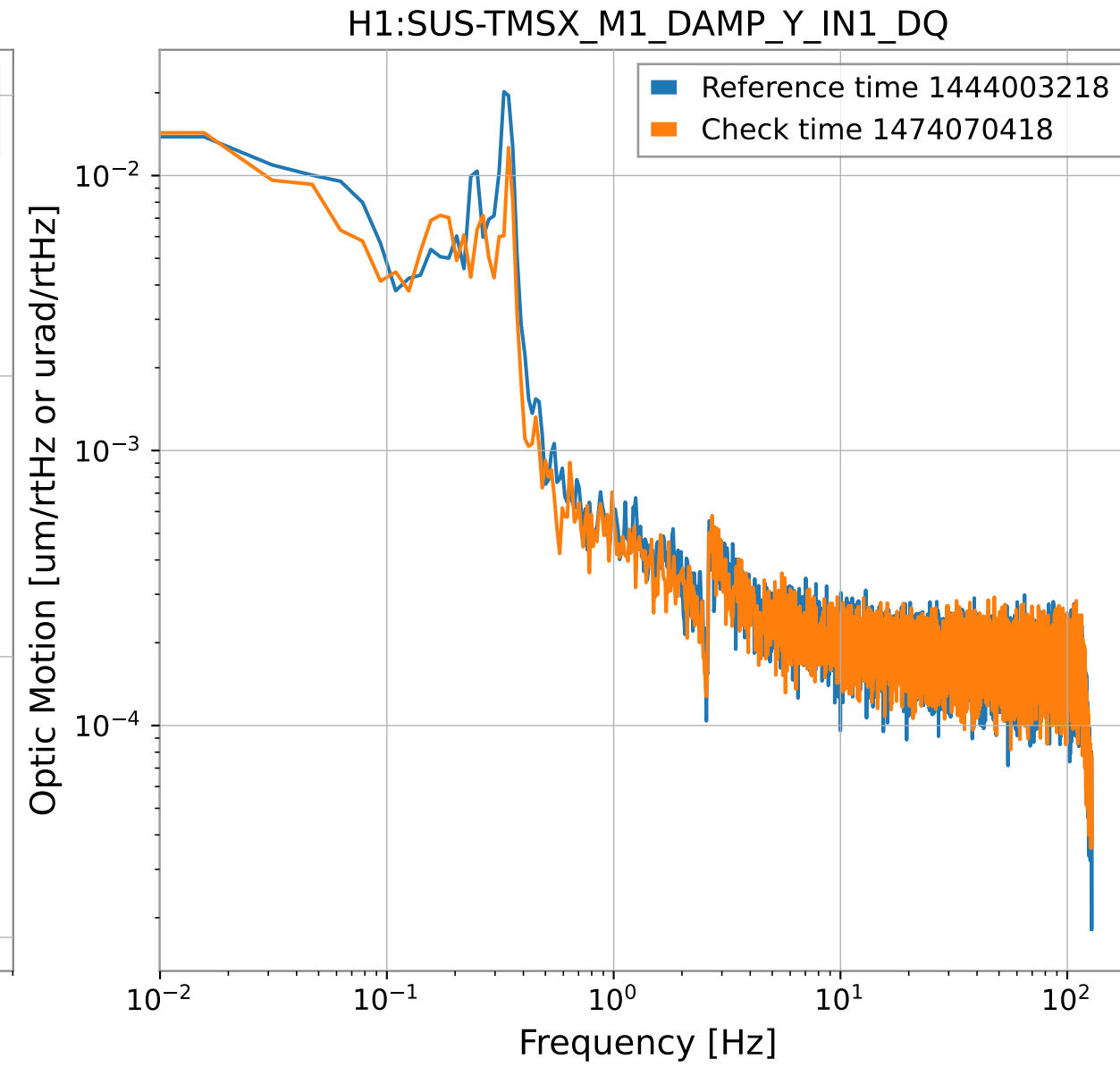
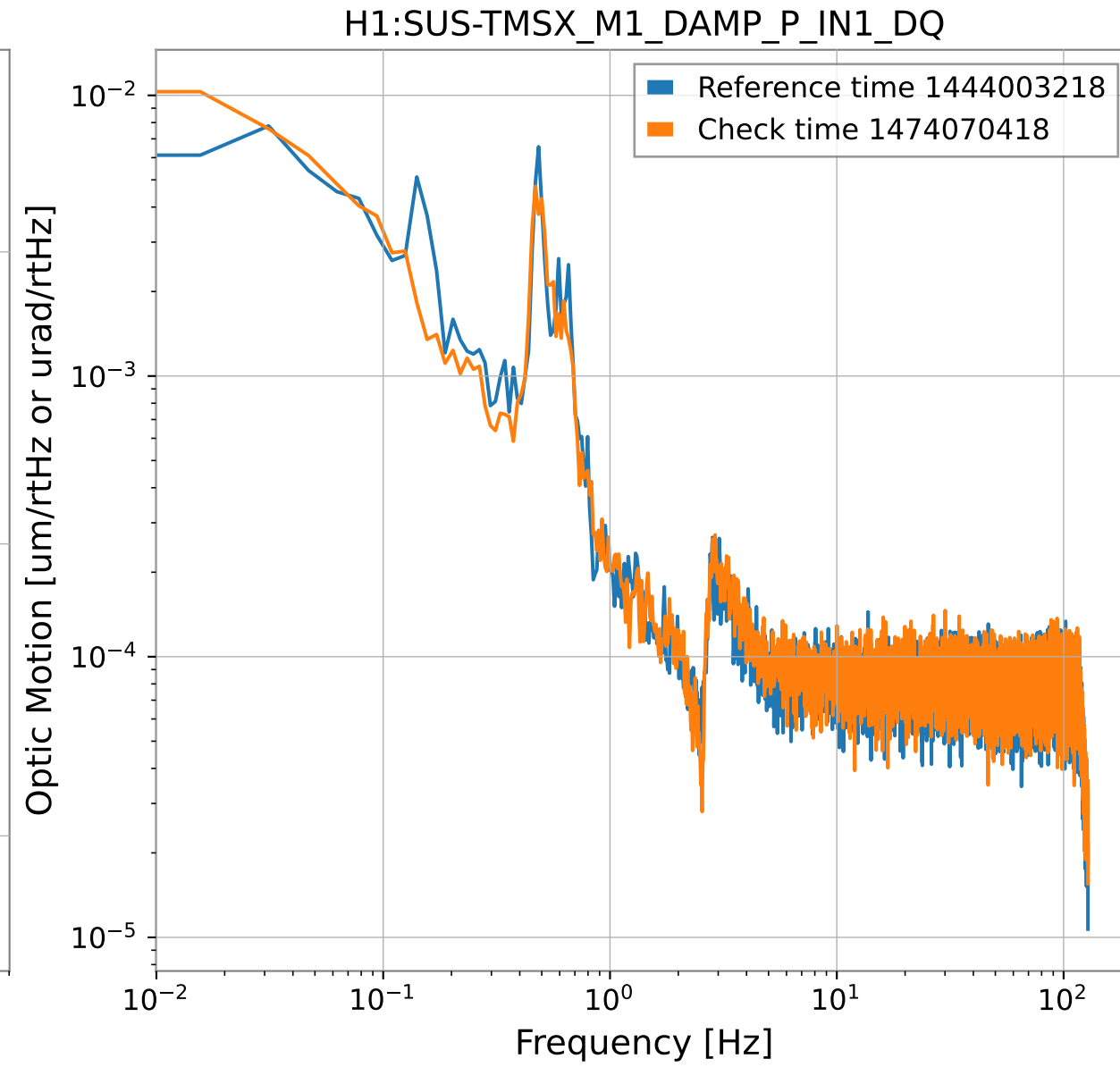
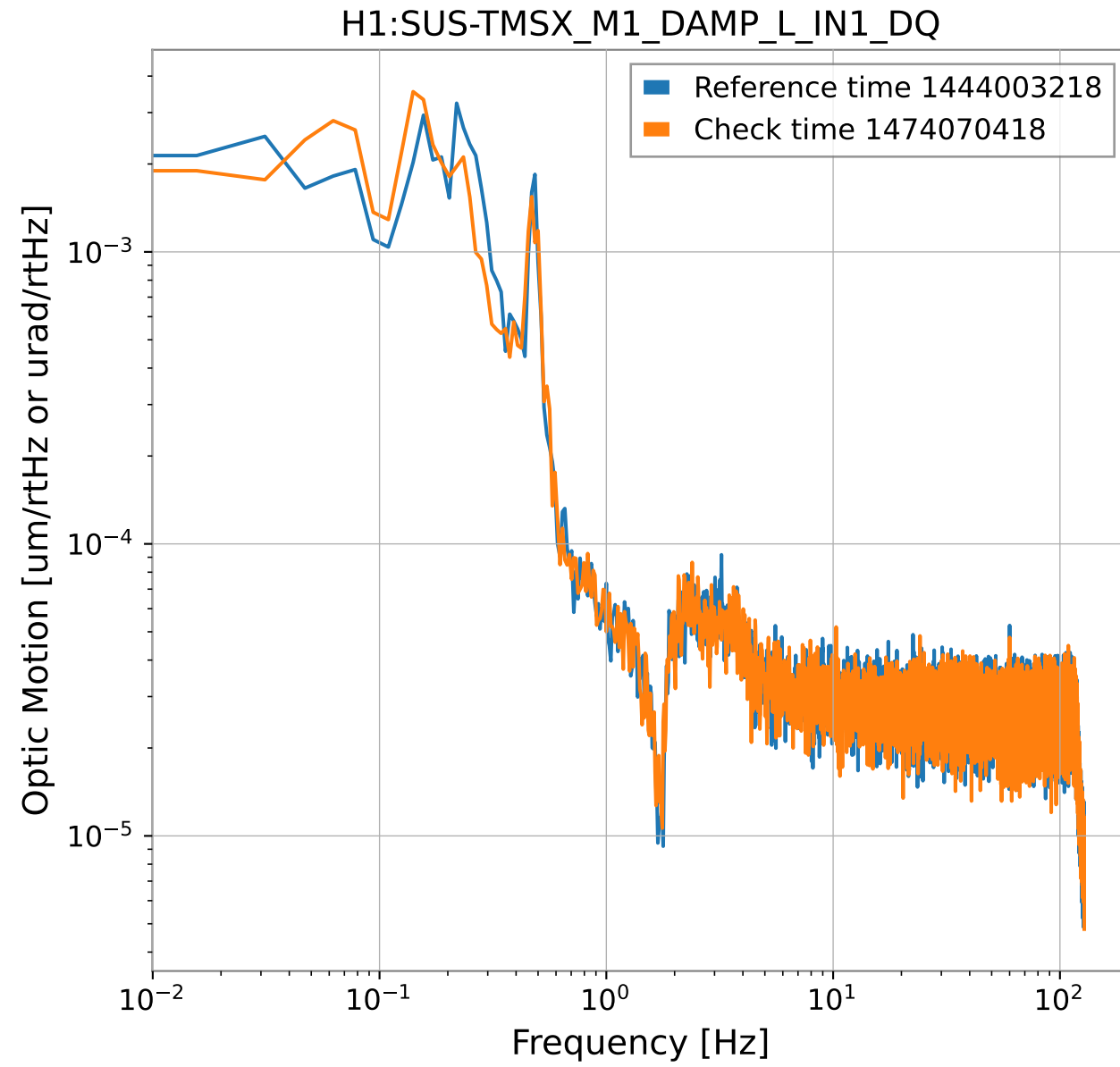
MC2



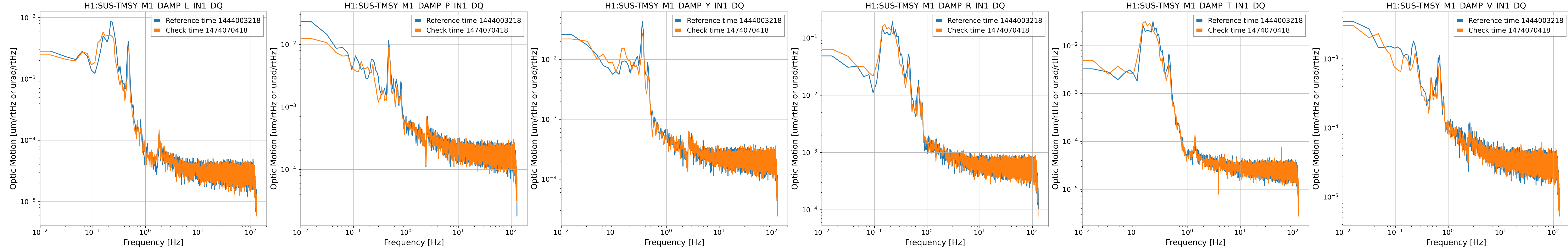
MC3



TMSX

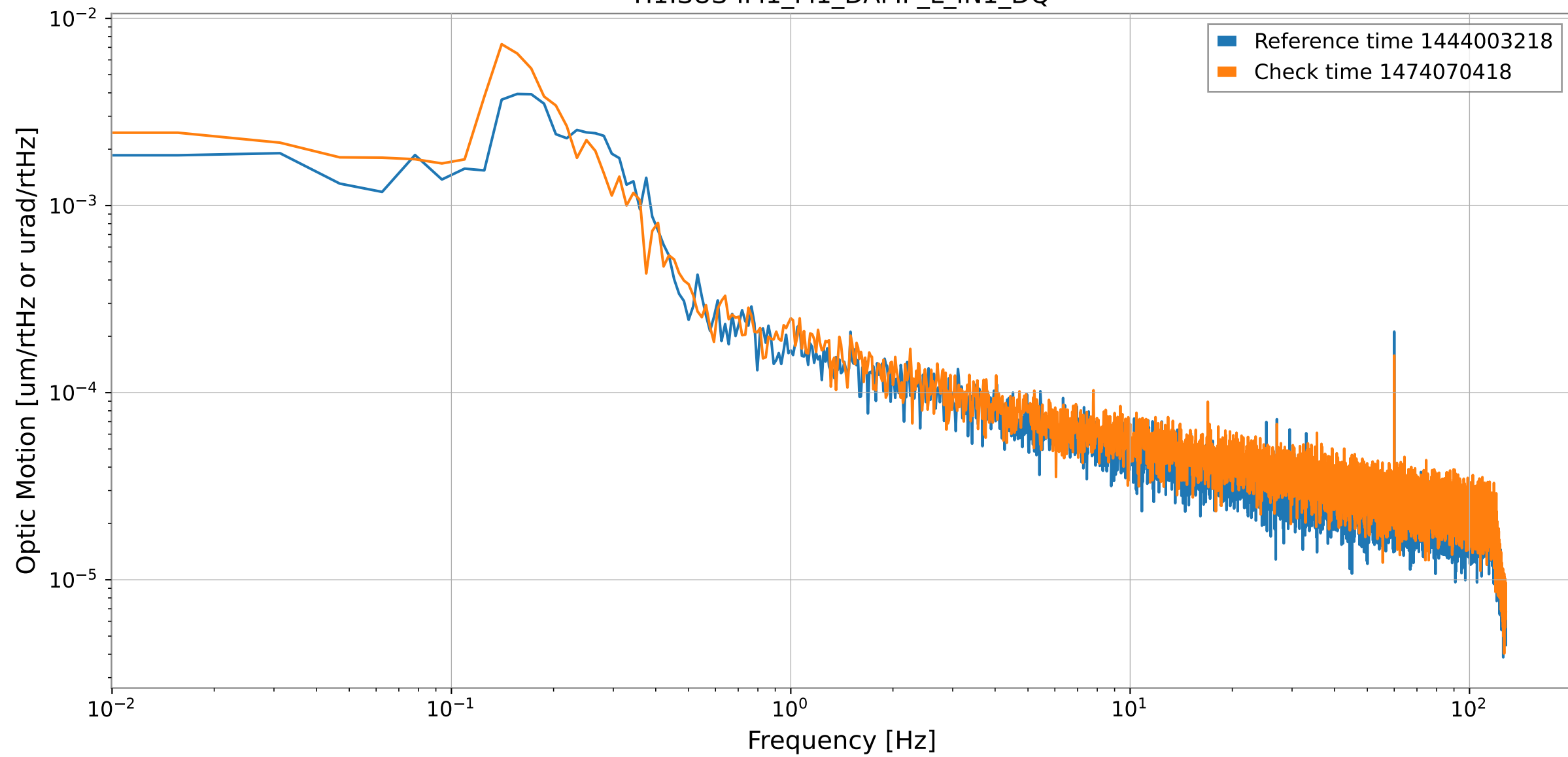


TMSY

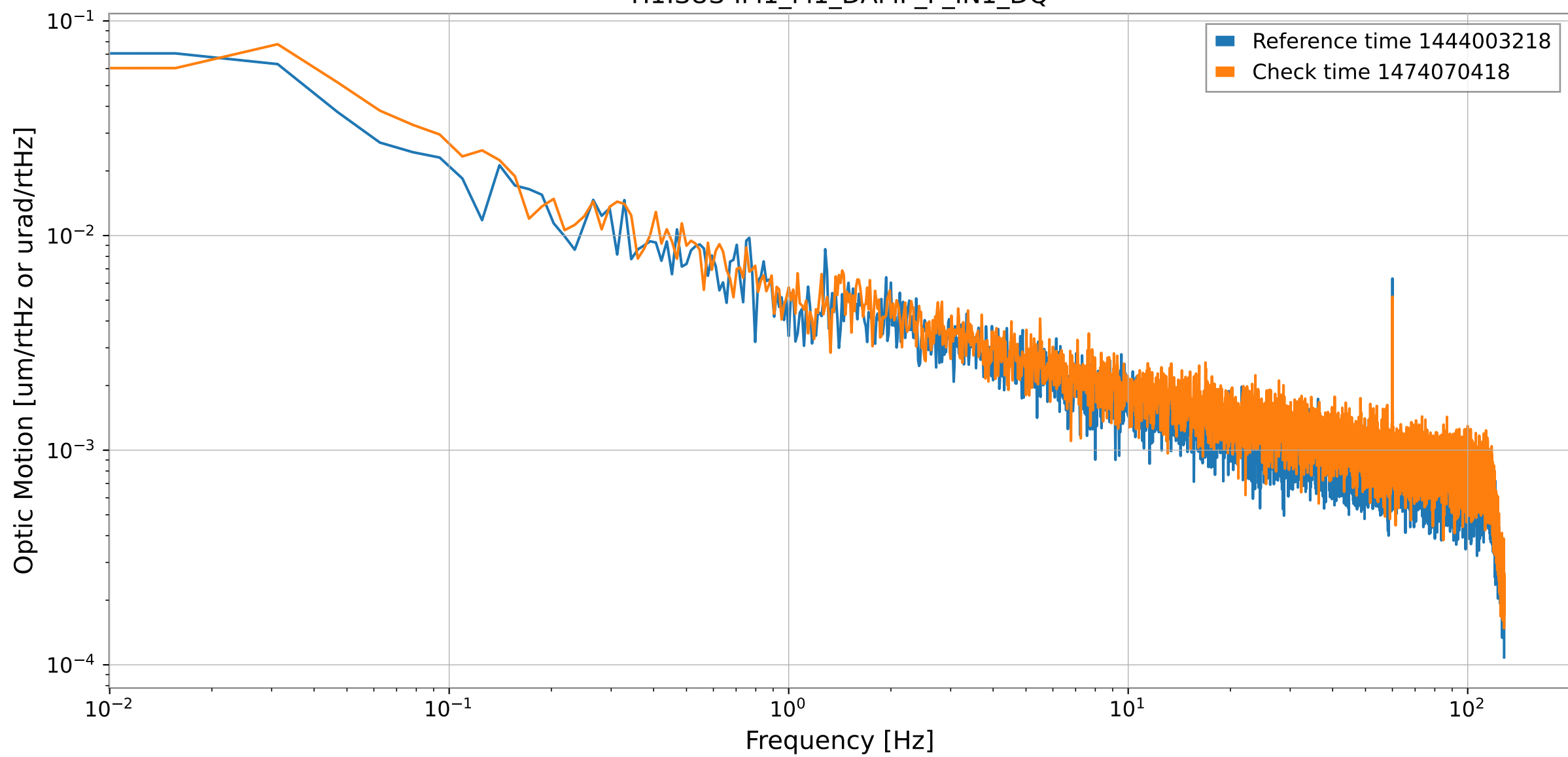


IM1

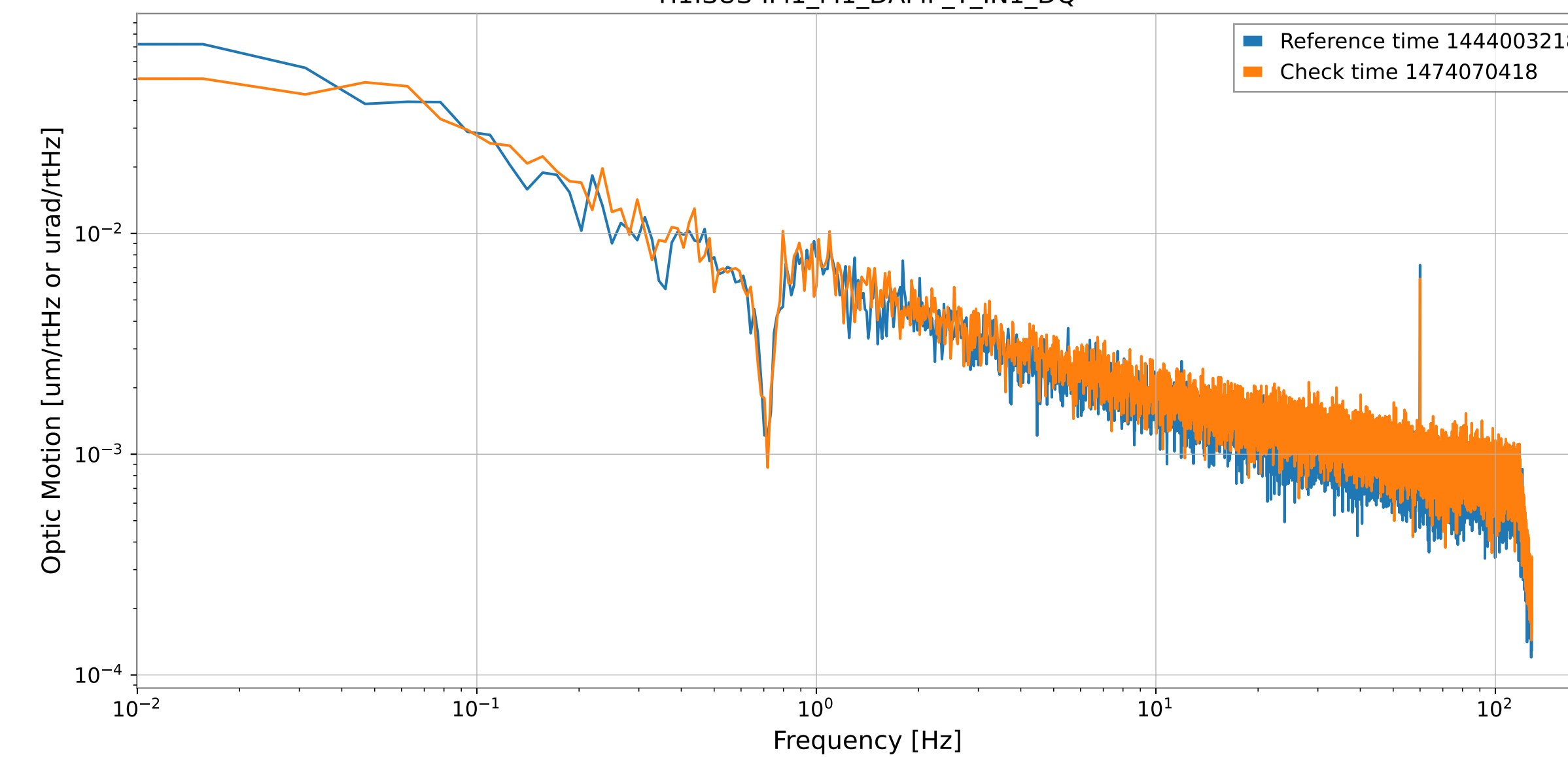
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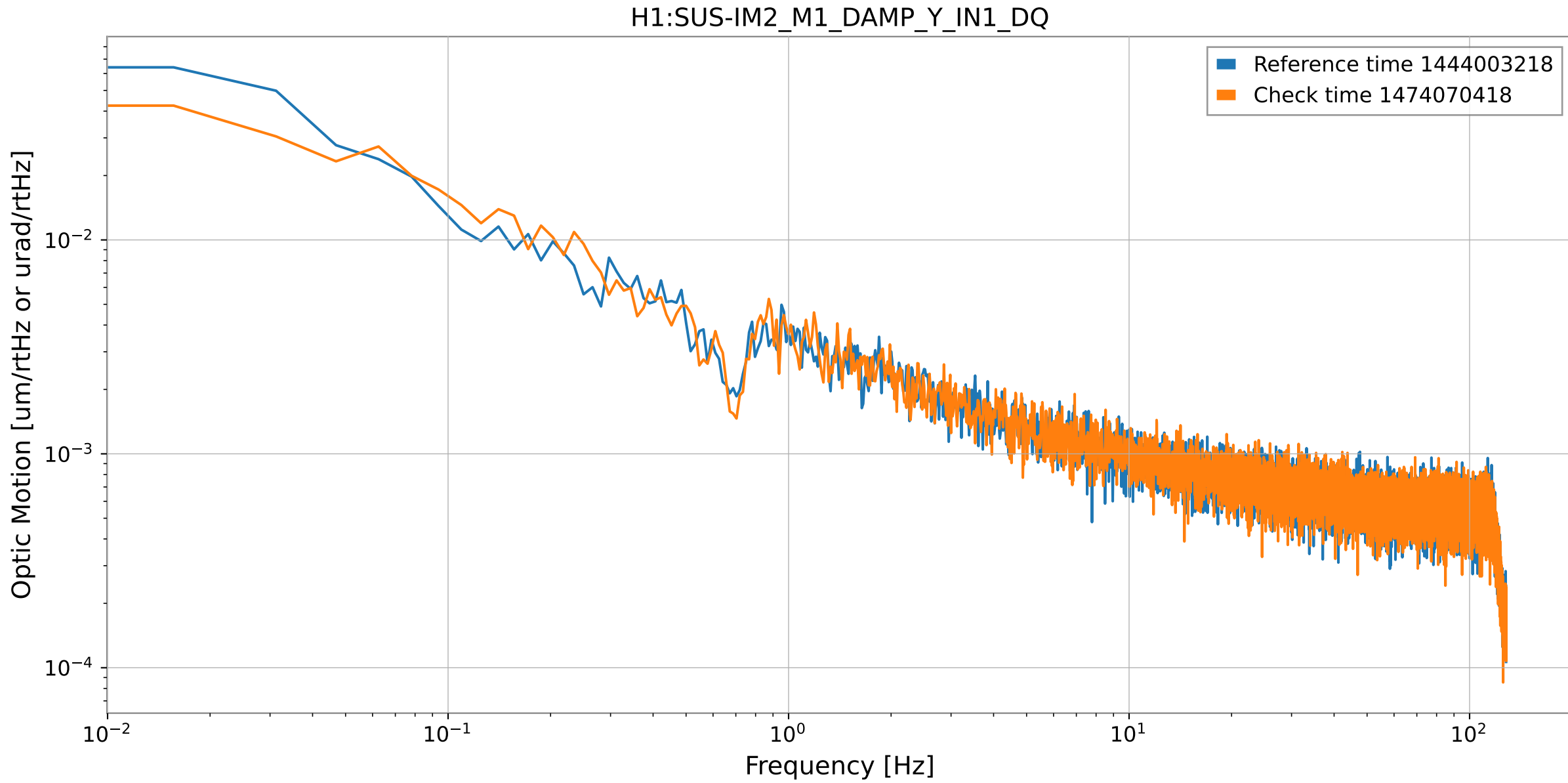
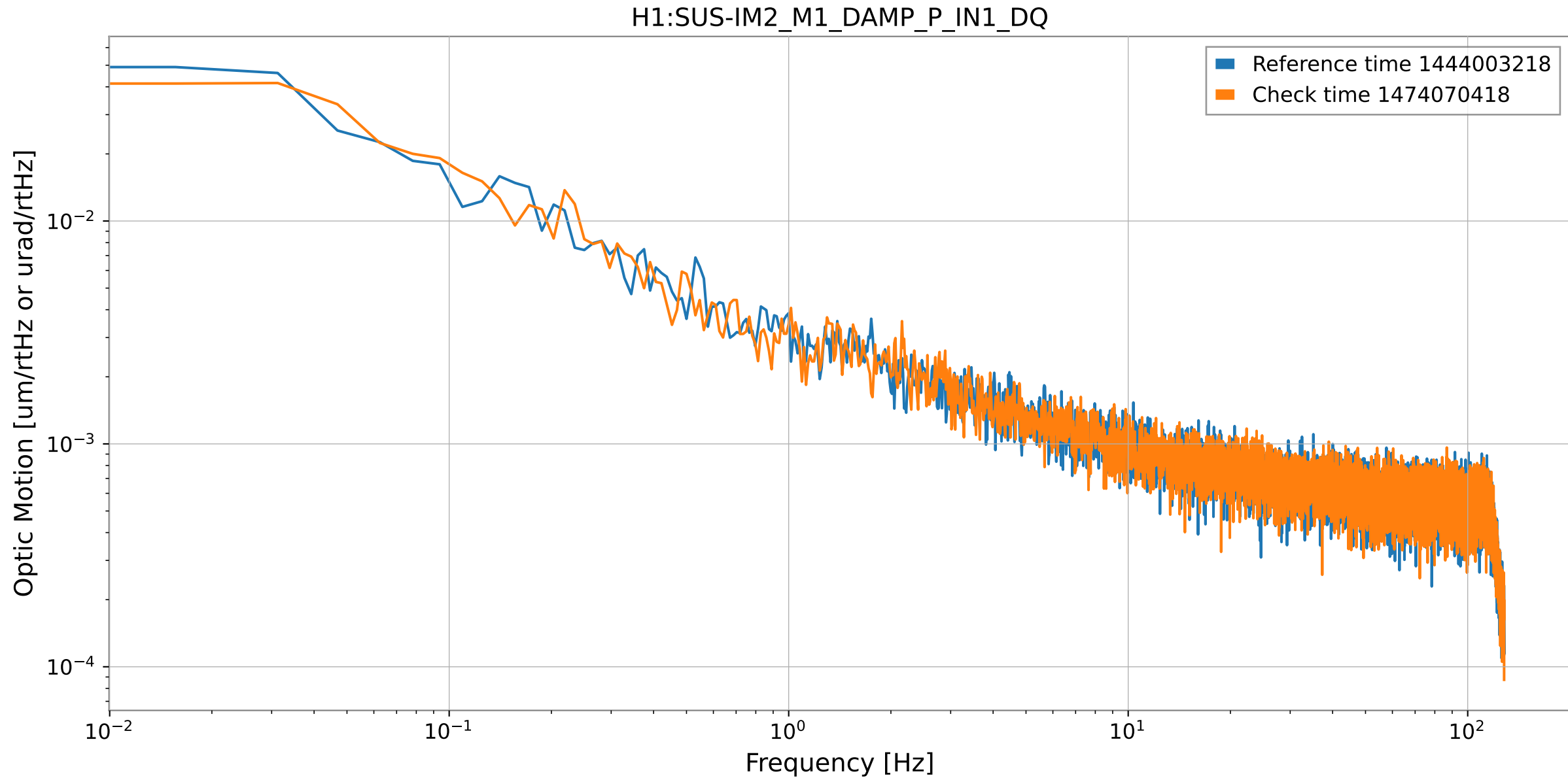
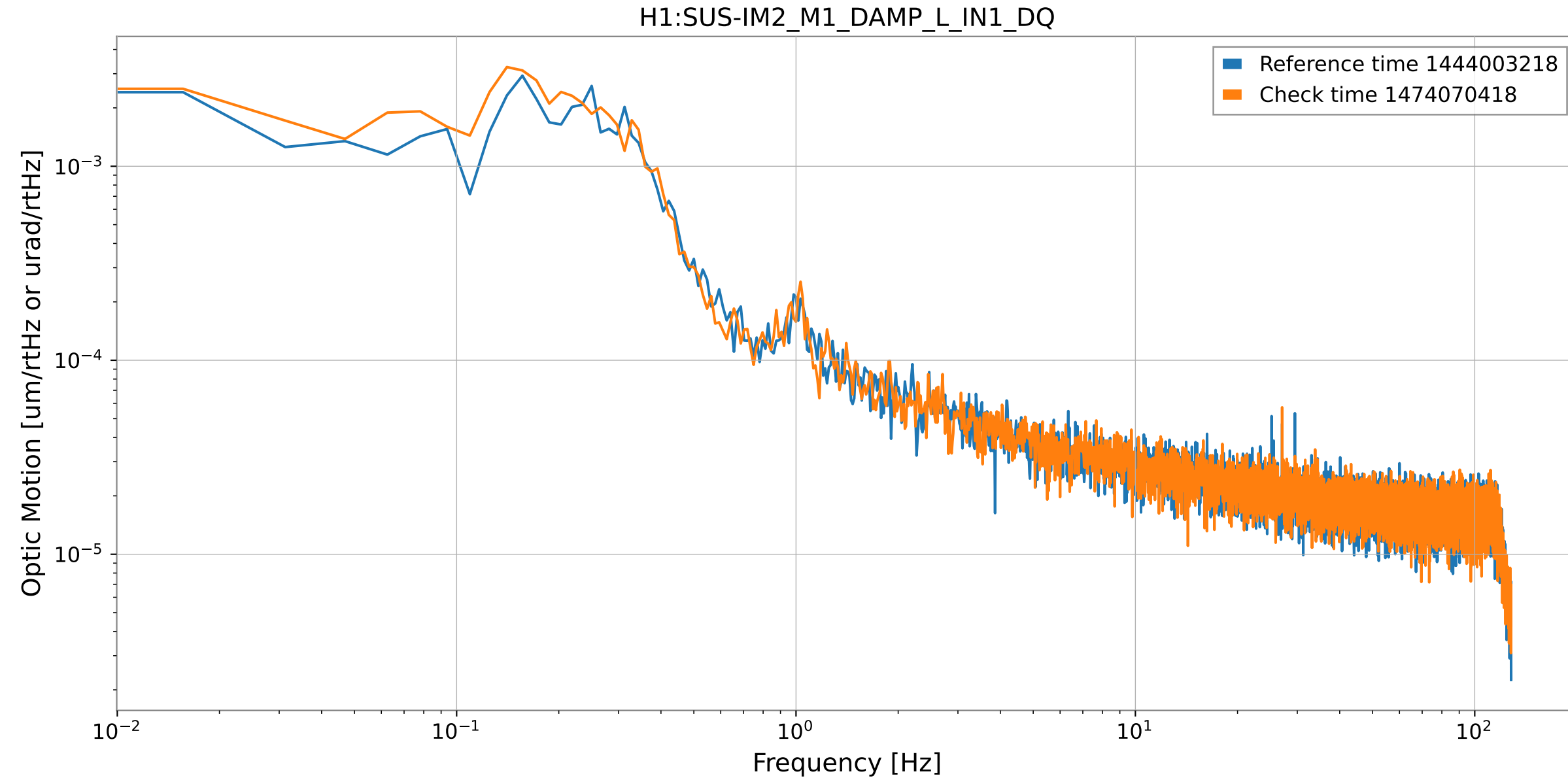
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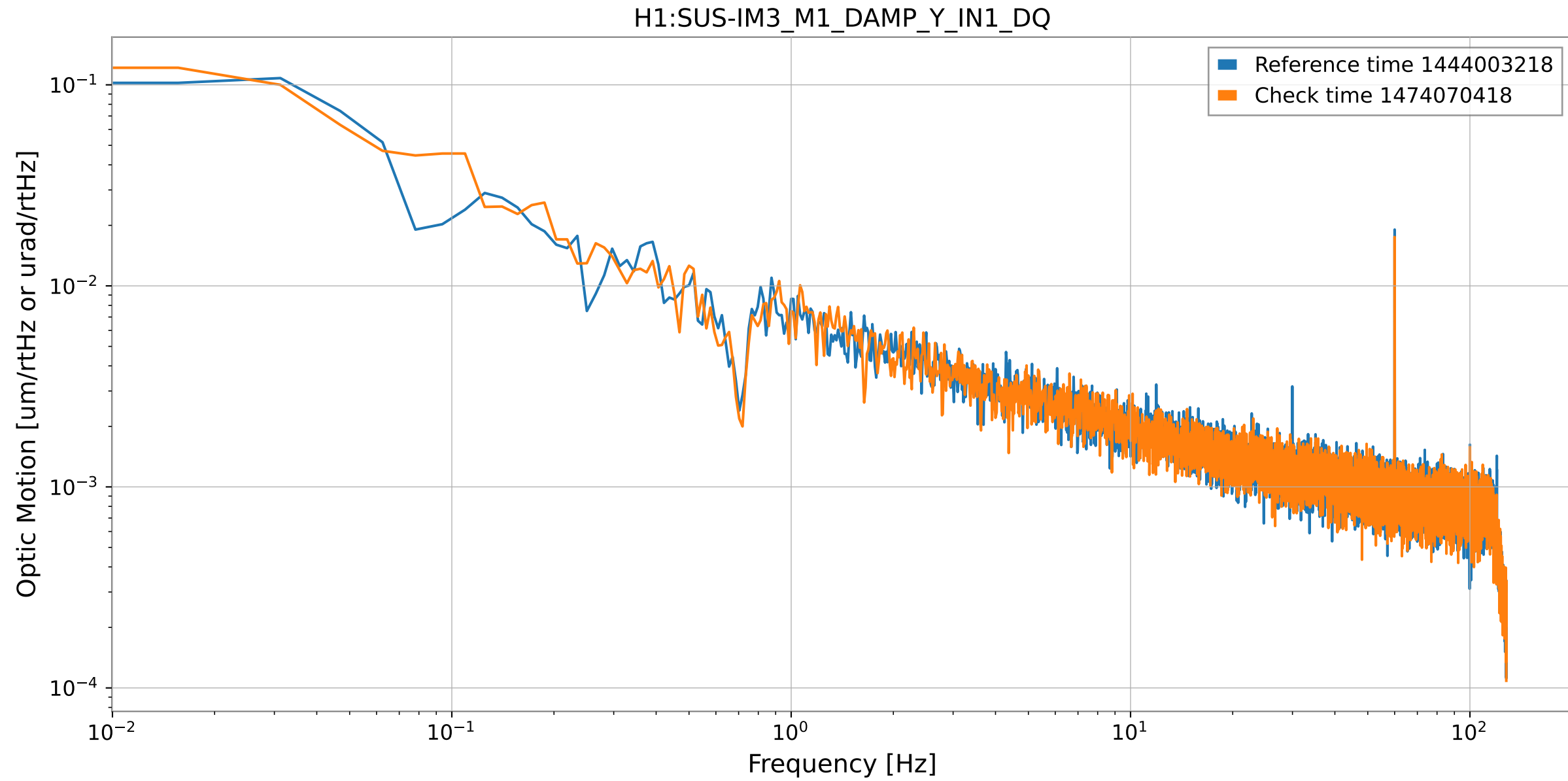
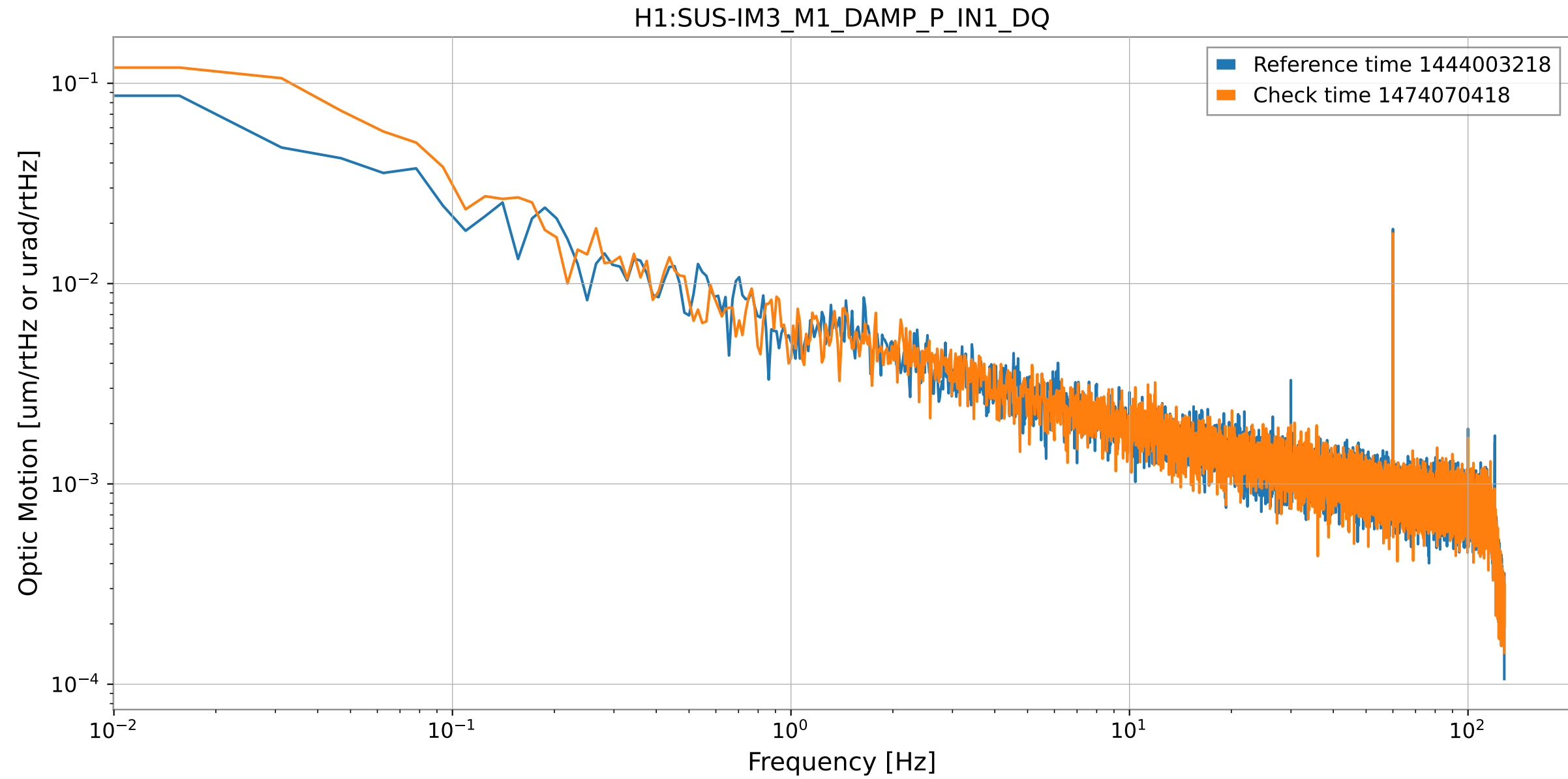
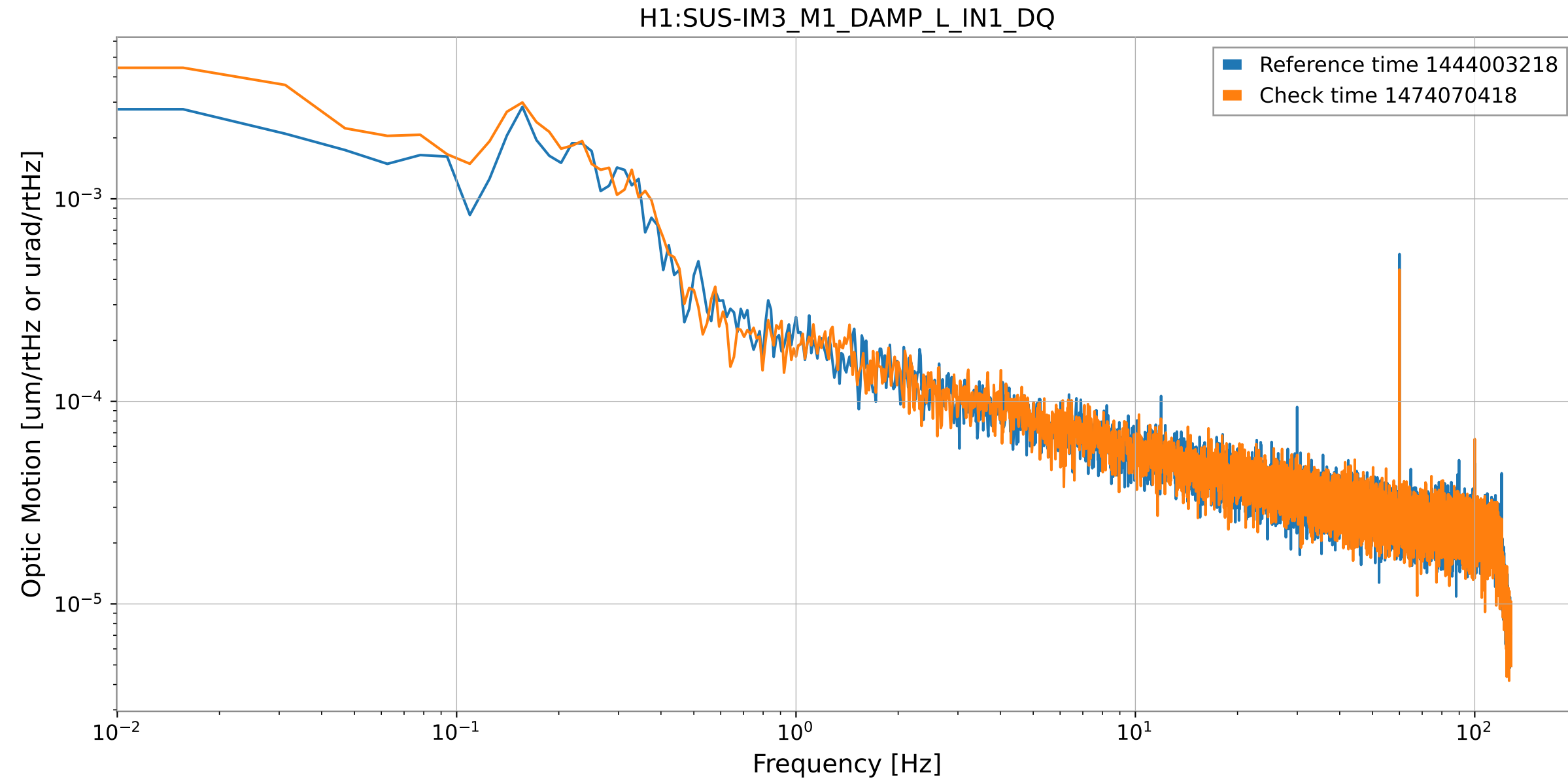
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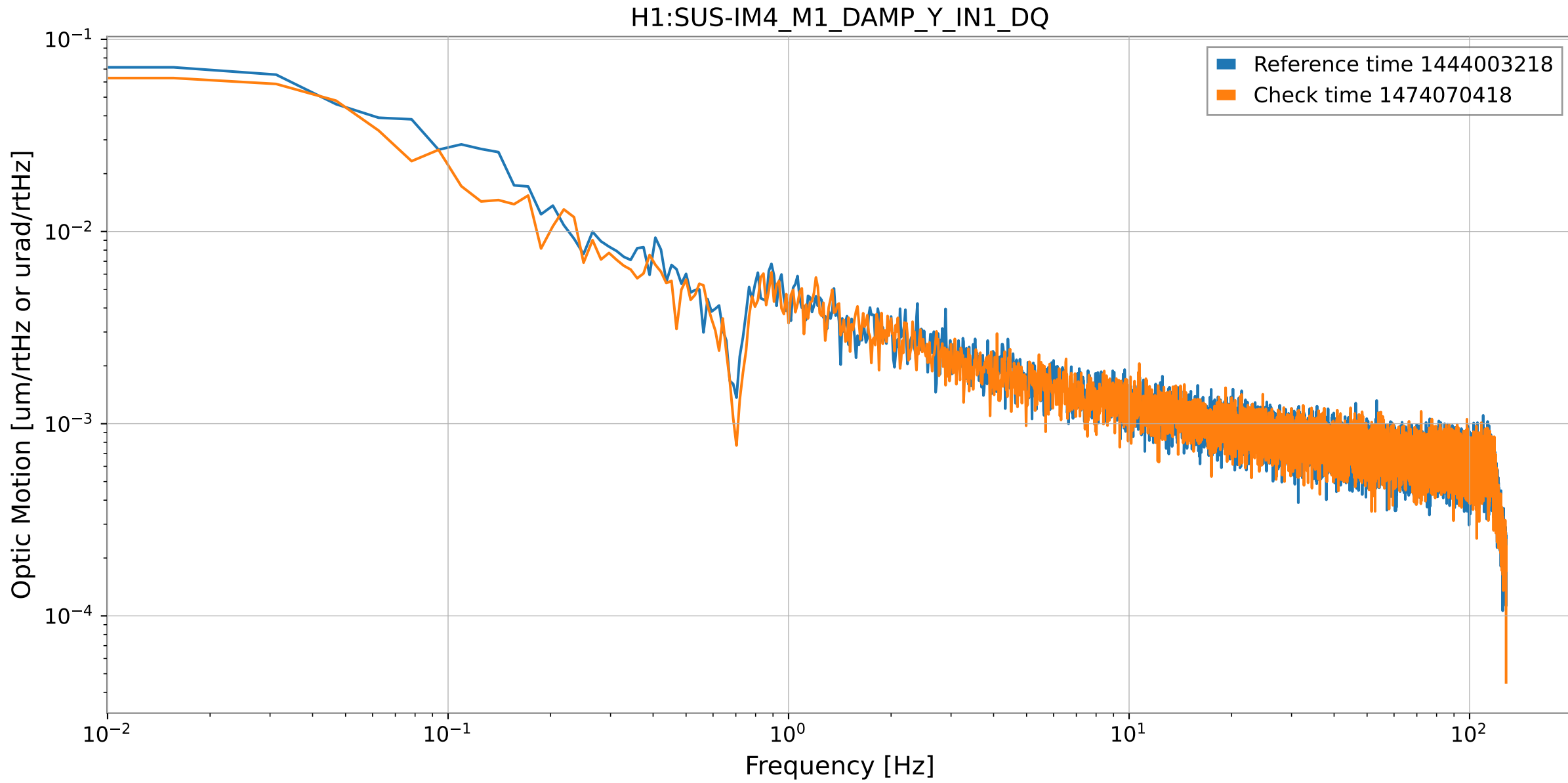
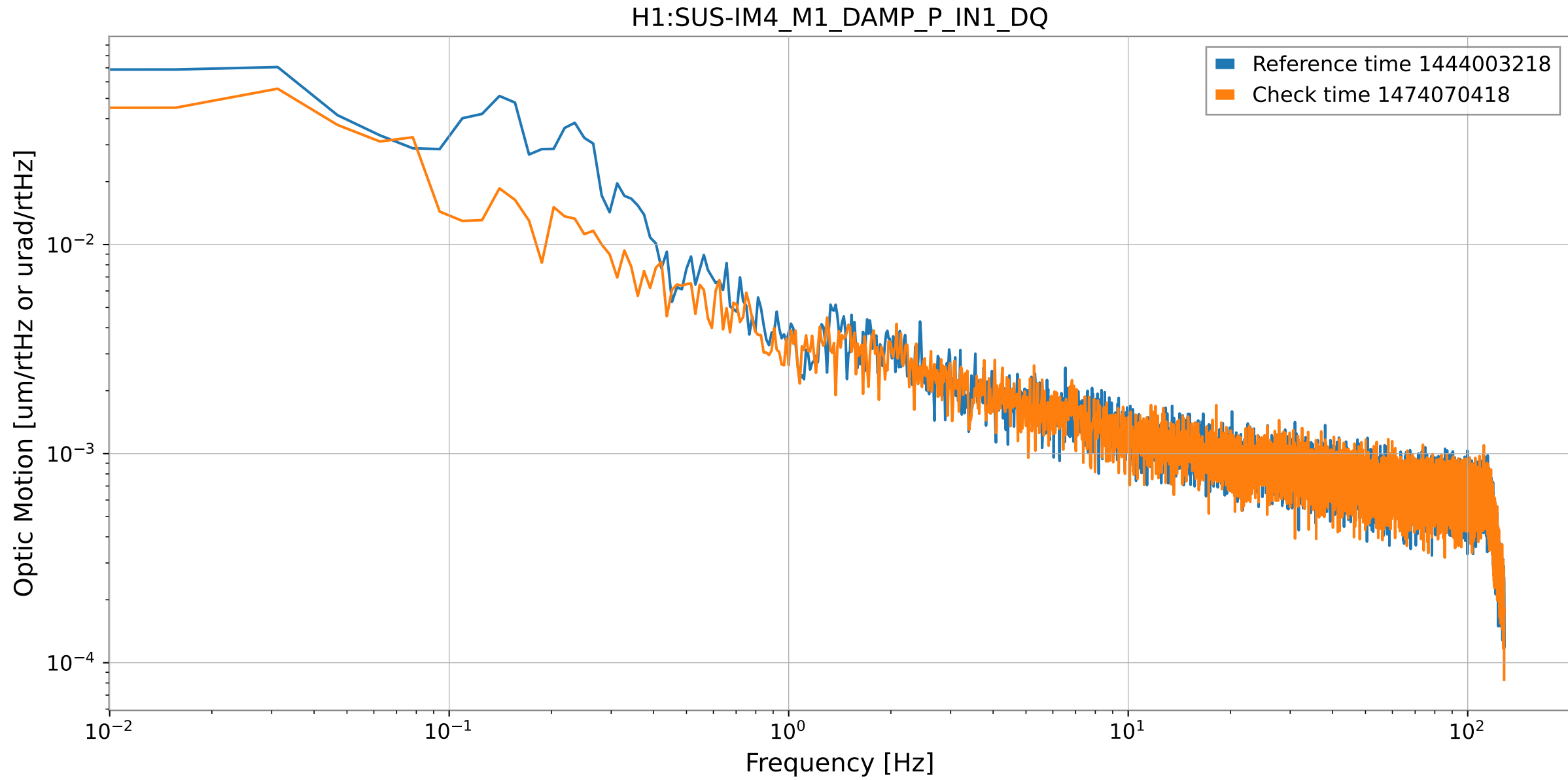
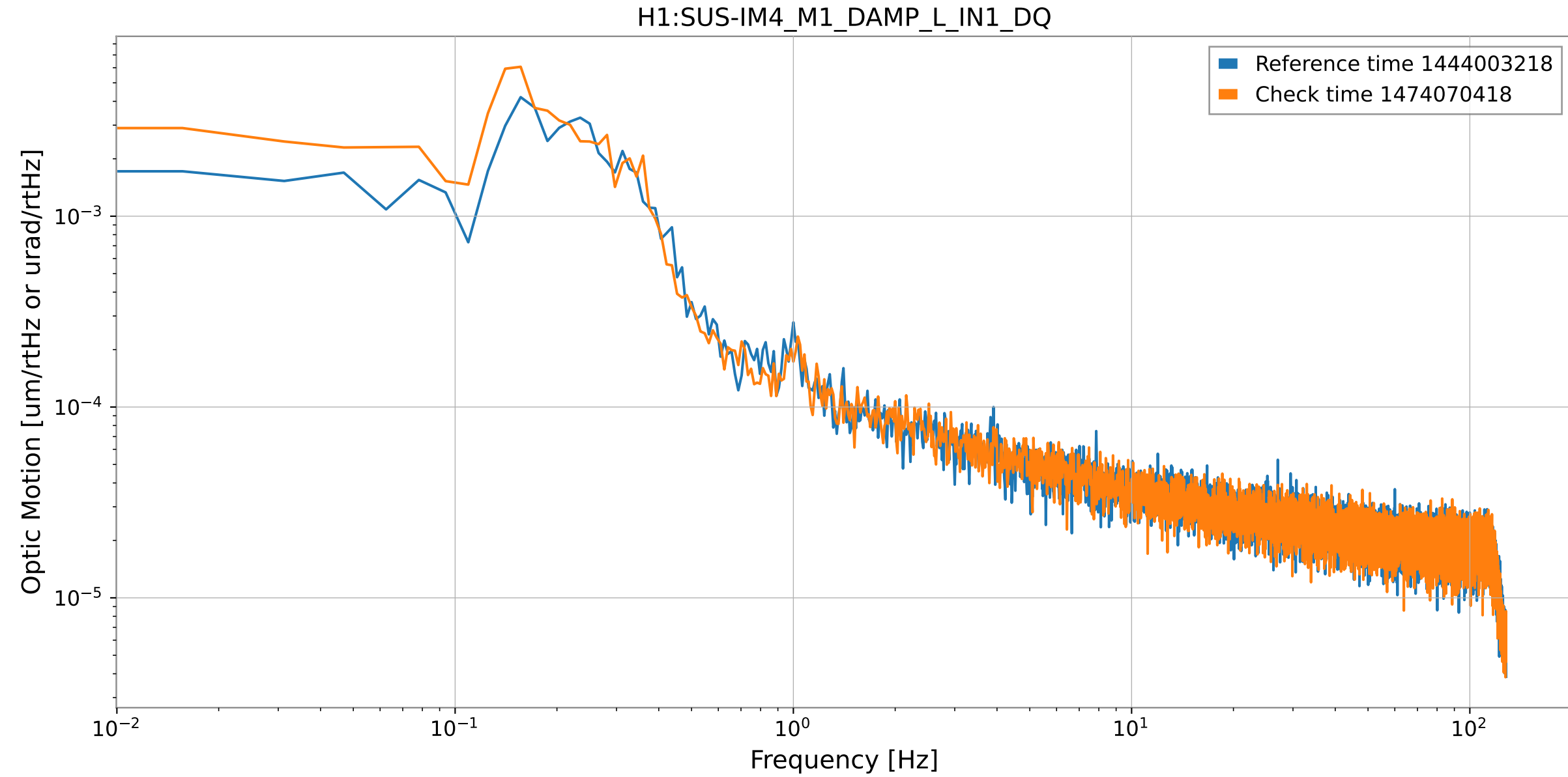
IM2



IM3

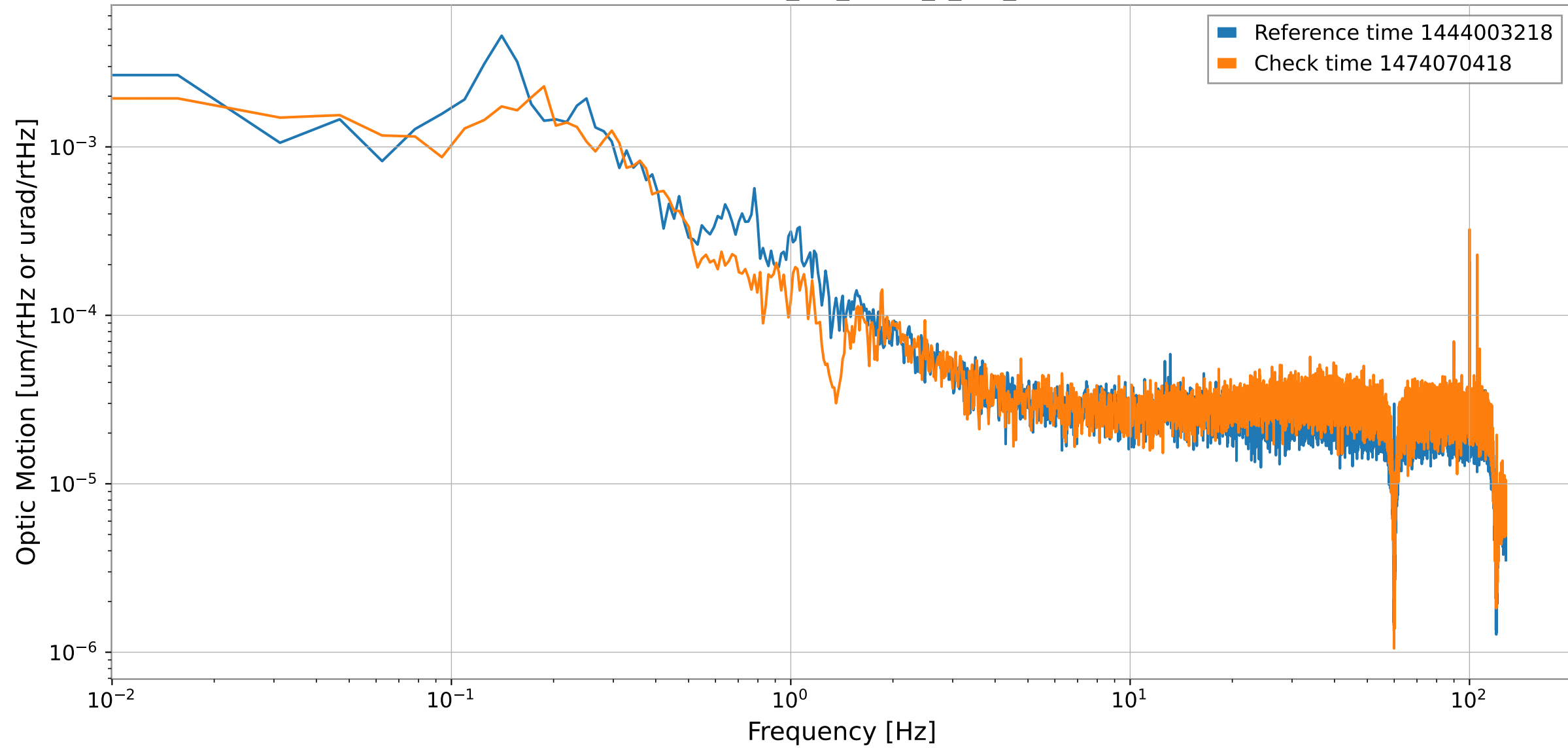


IM4

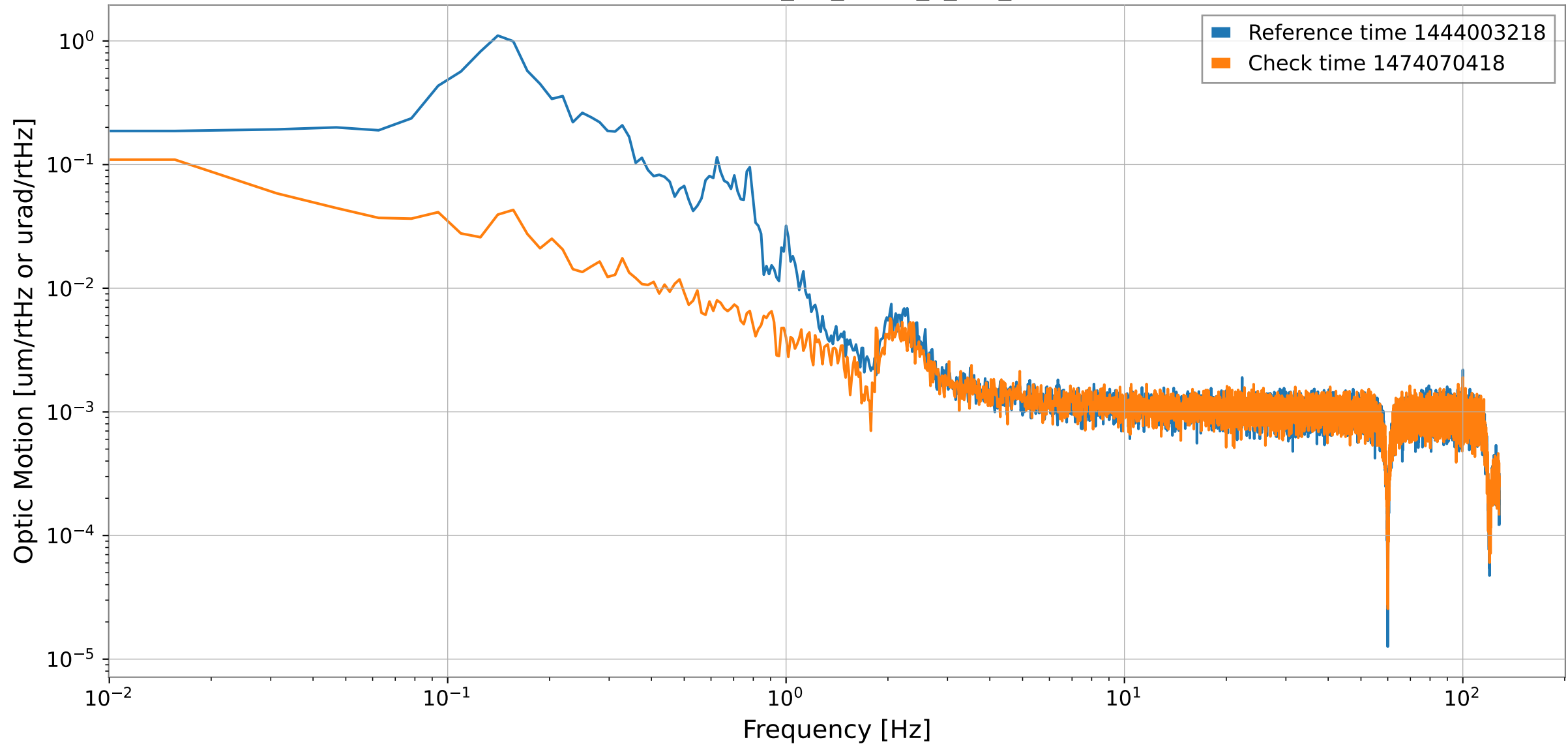


RM1

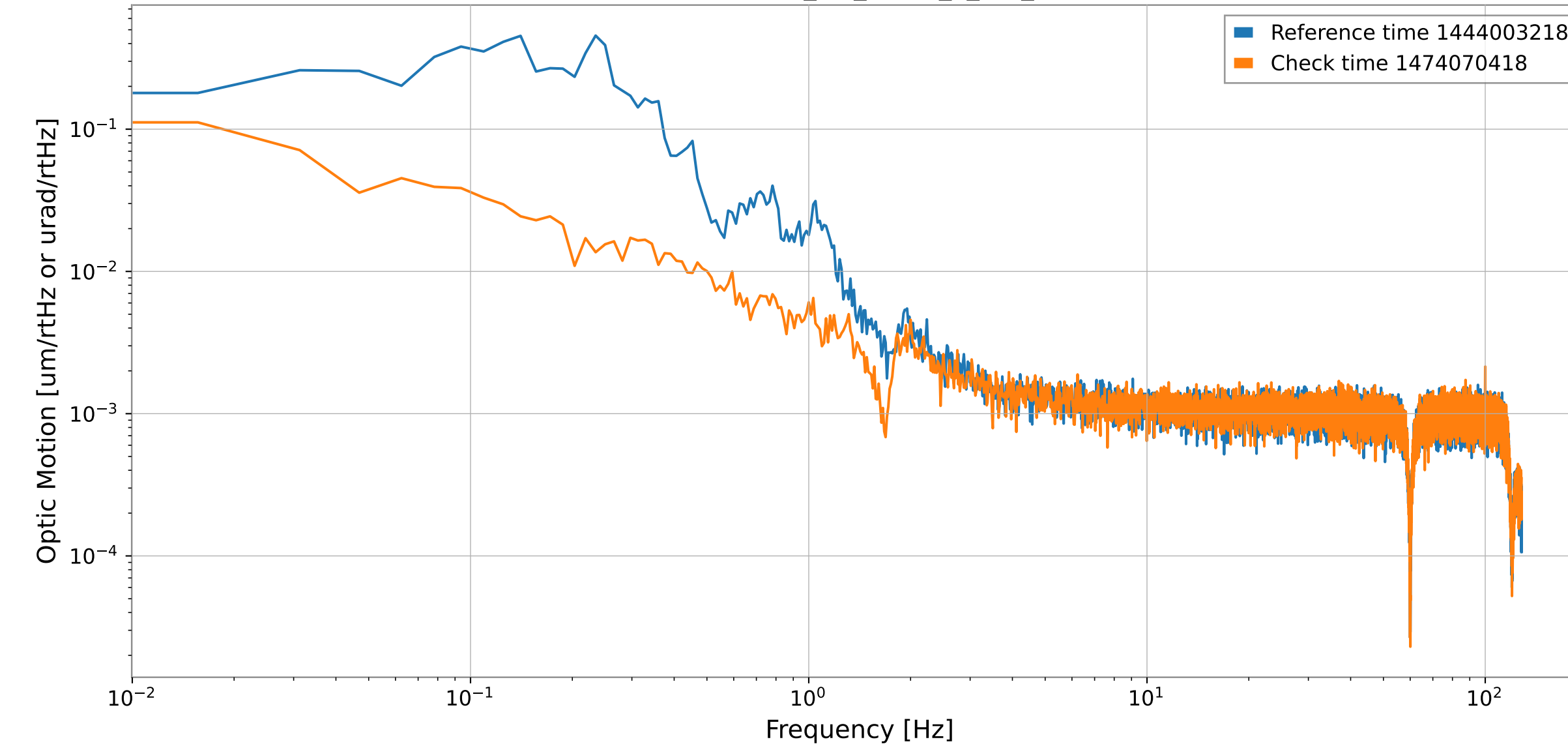
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H1:SUS-RM1_M1_DAMP_P_IN1_DQ

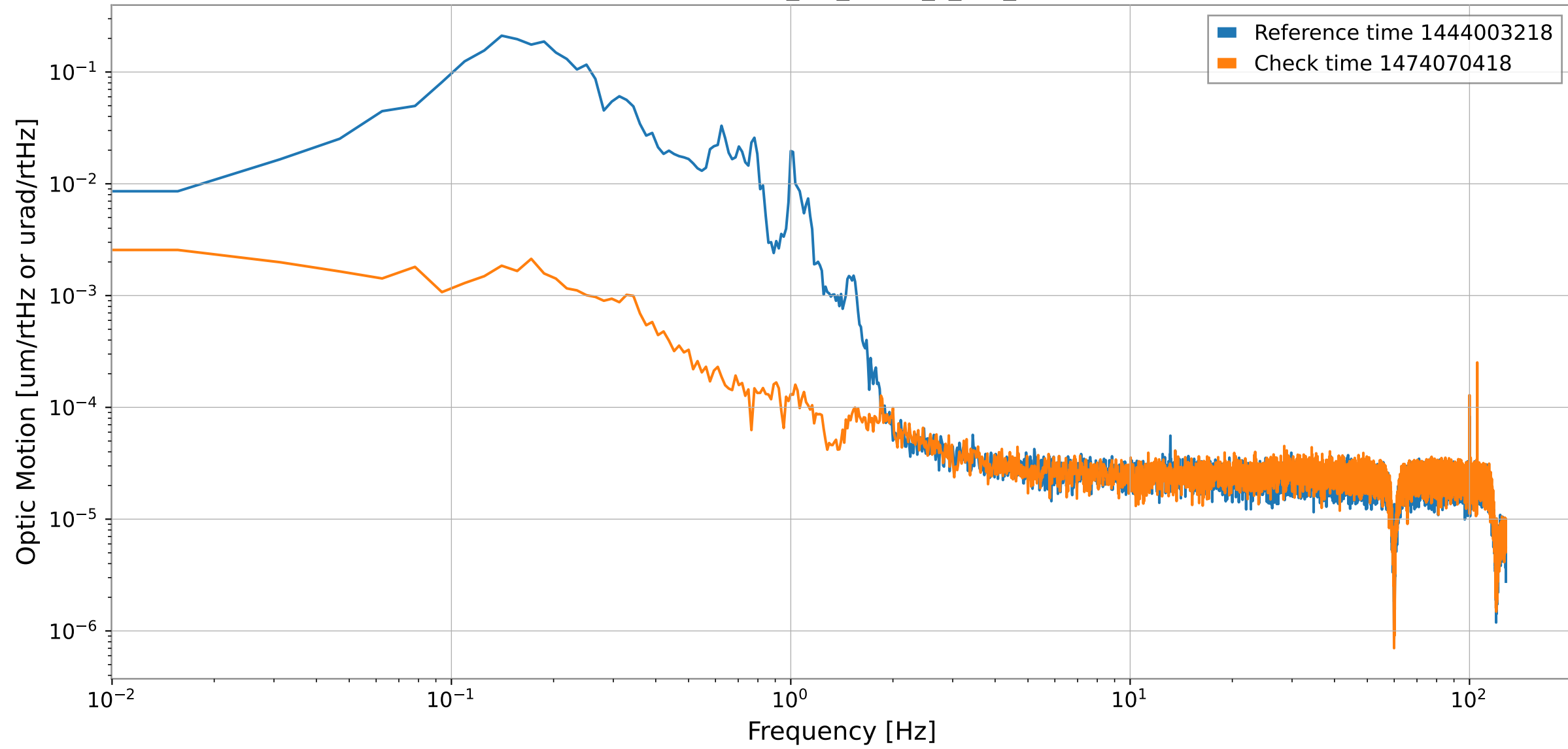


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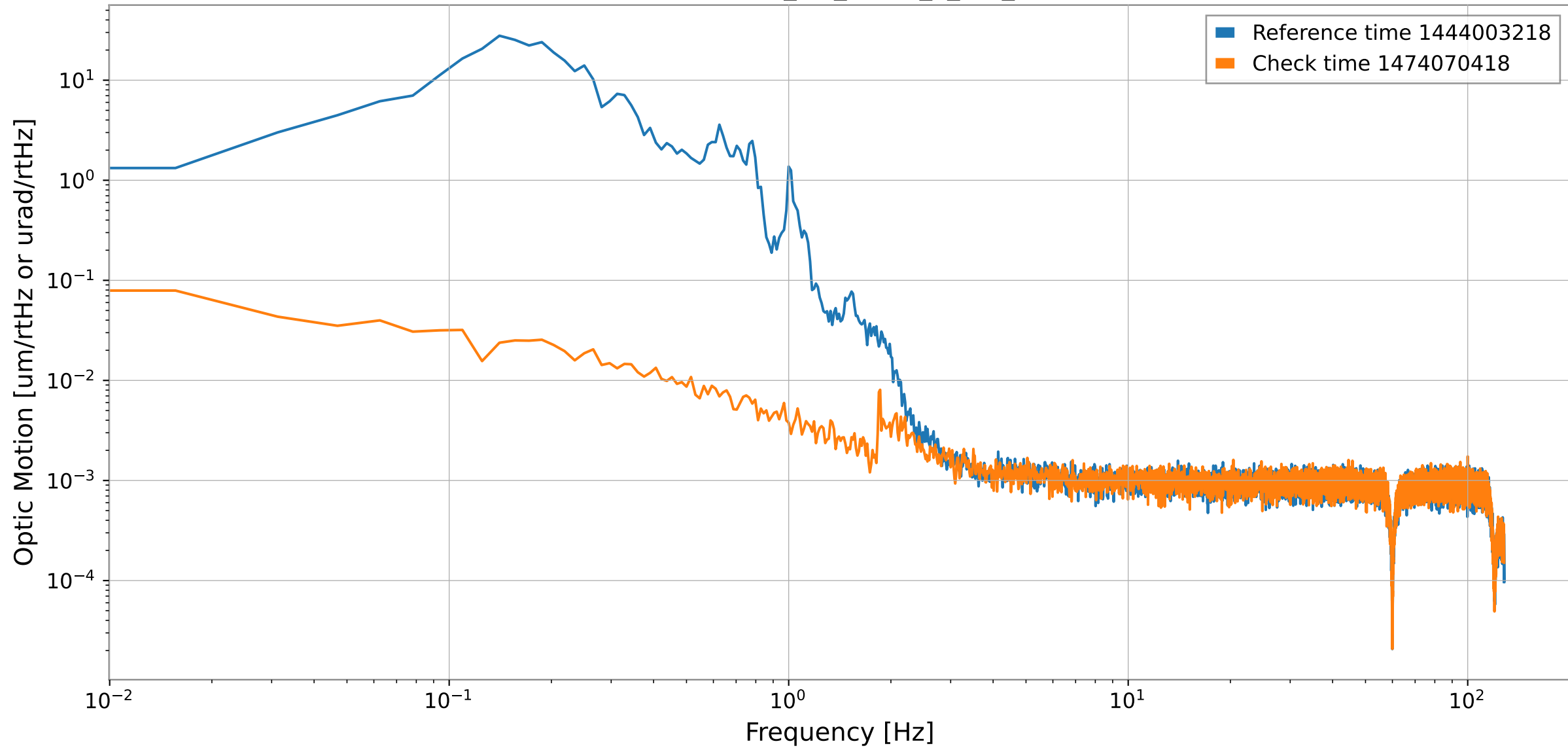


RM2

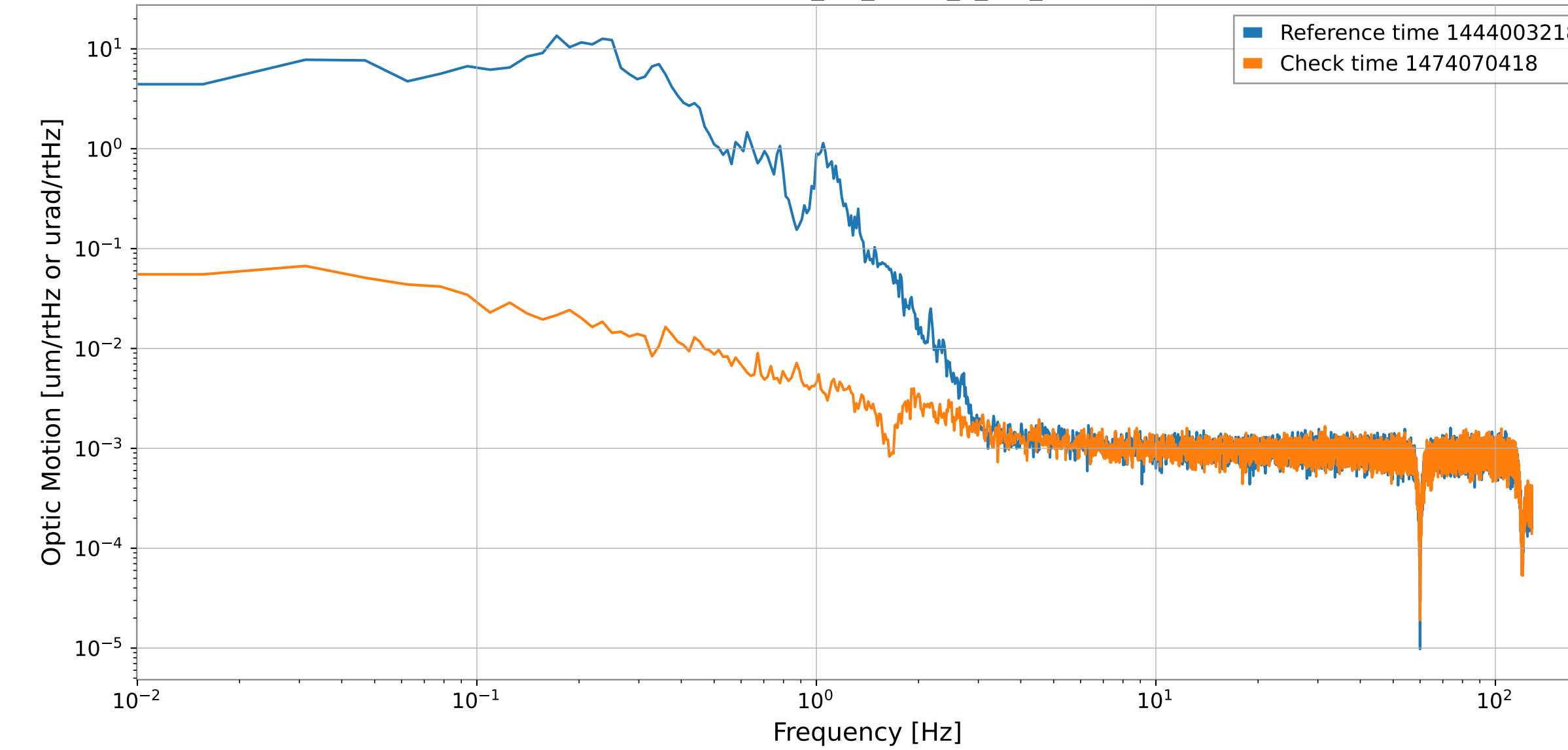
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H1:SUS-RM2_M1_DAMP_P_IN1_DQ

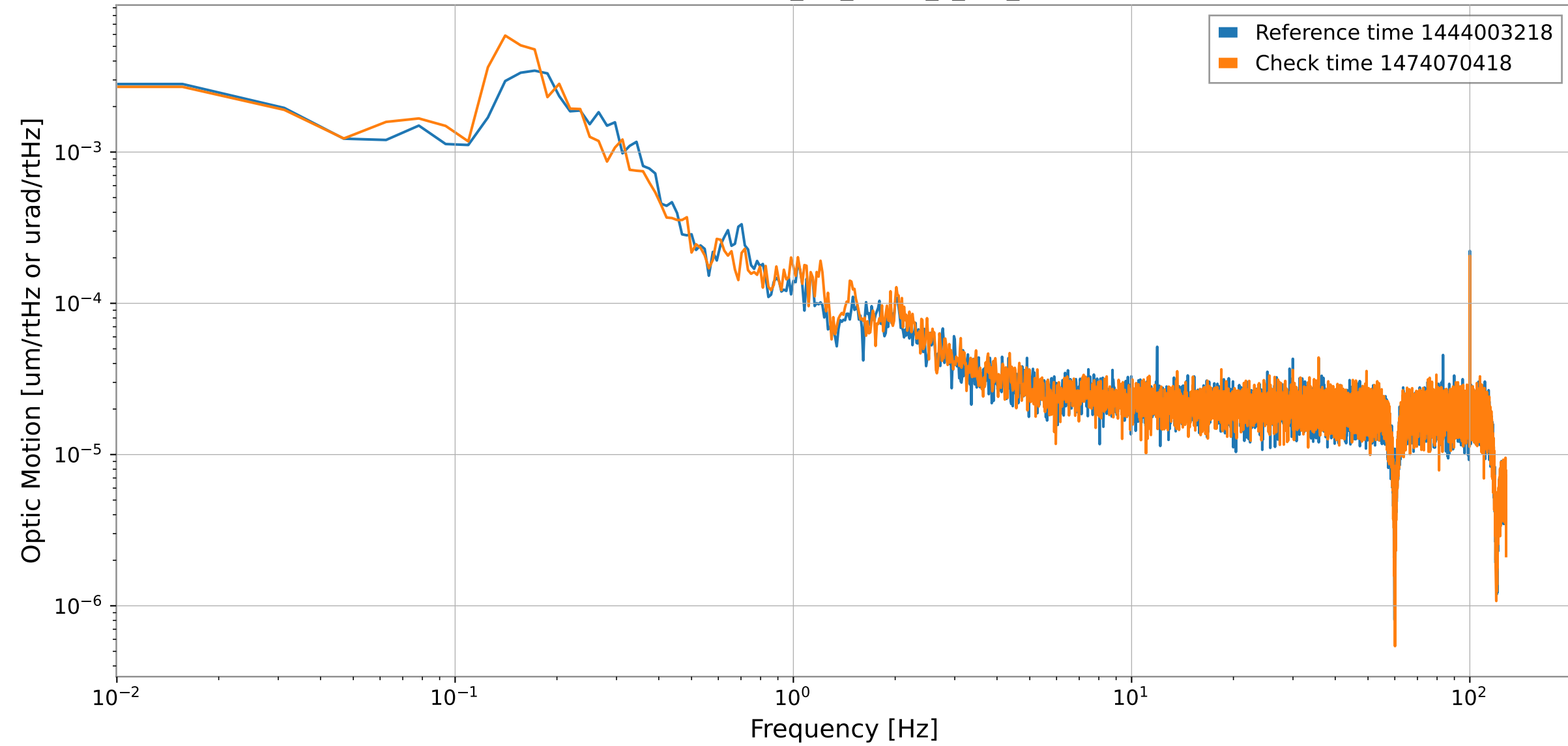


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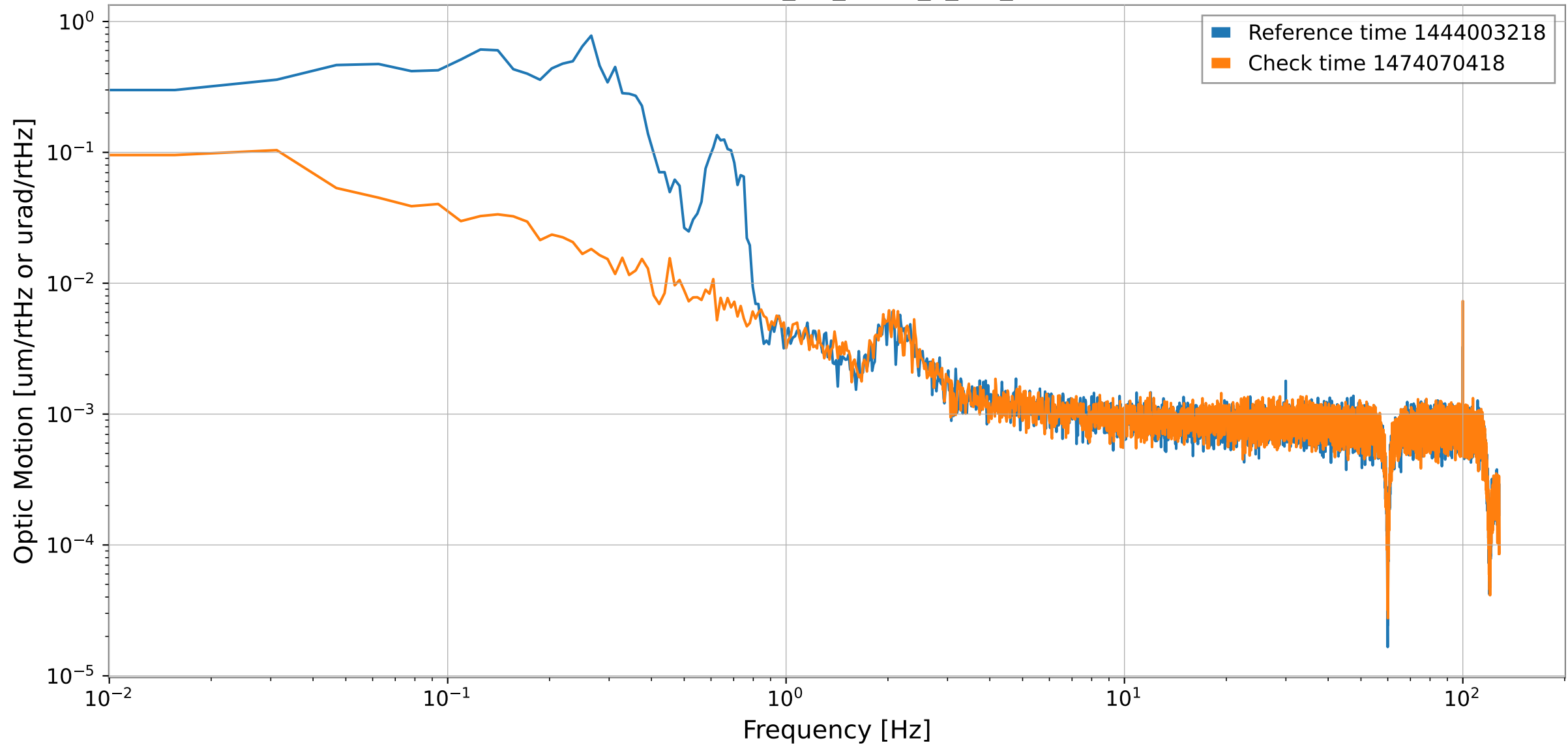


OM1

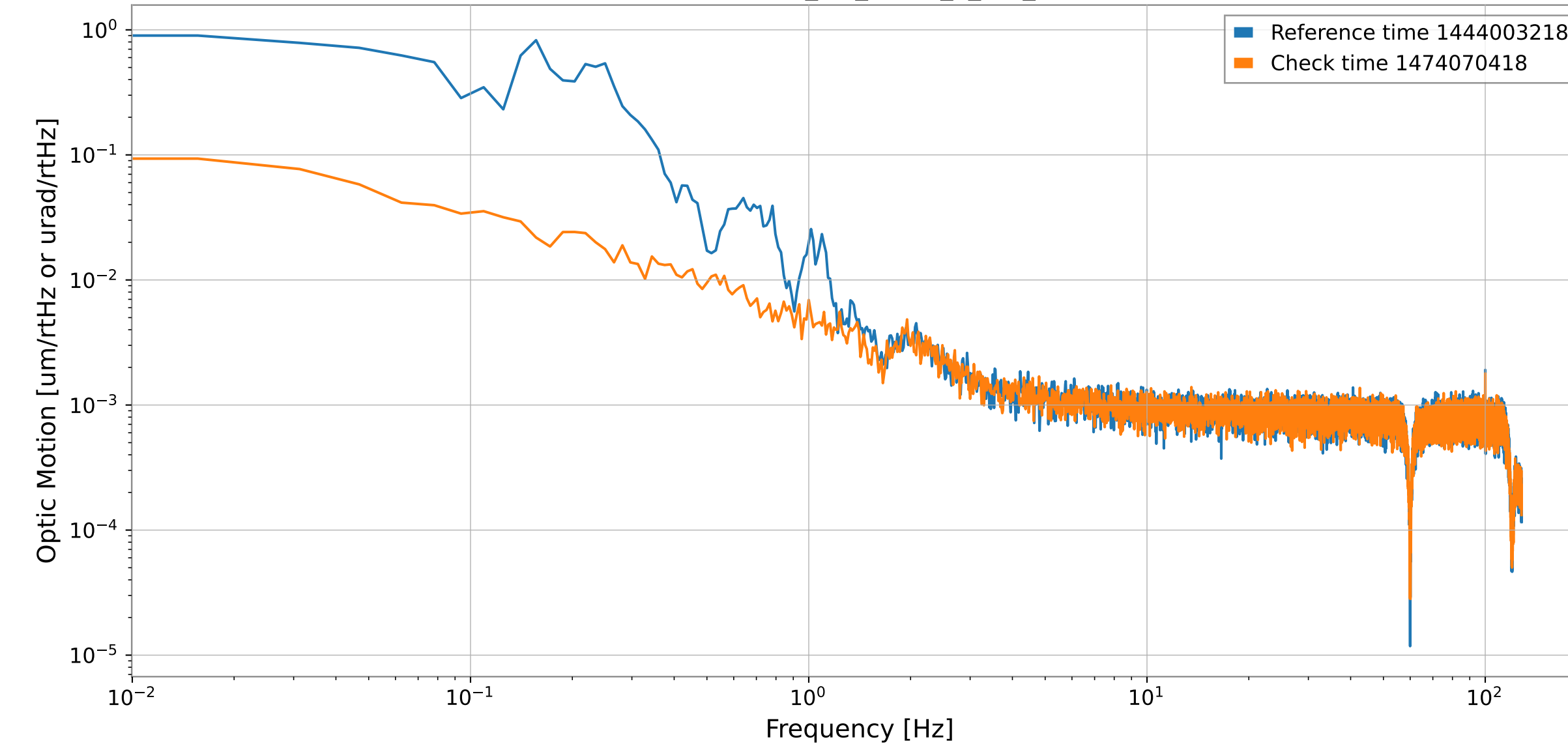
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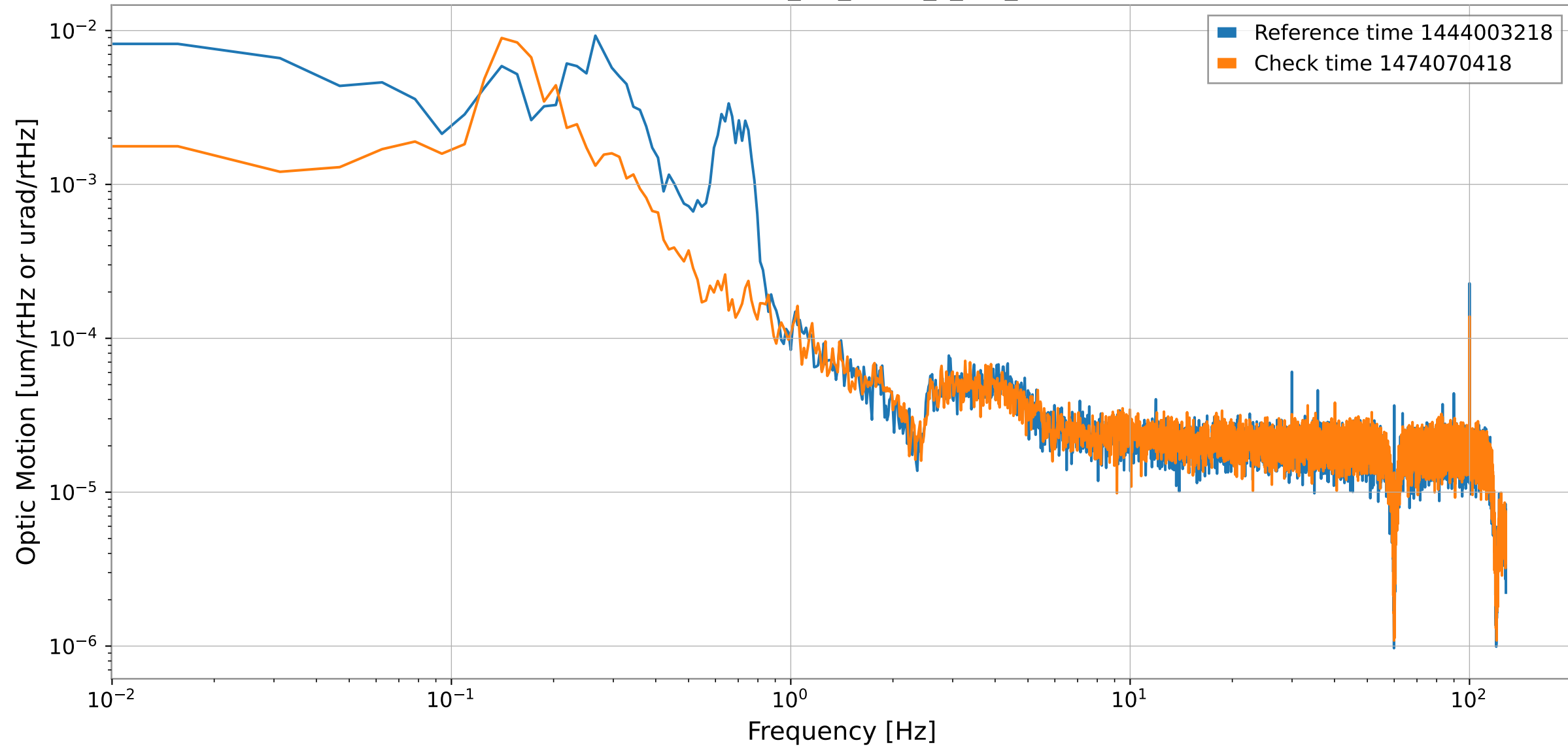


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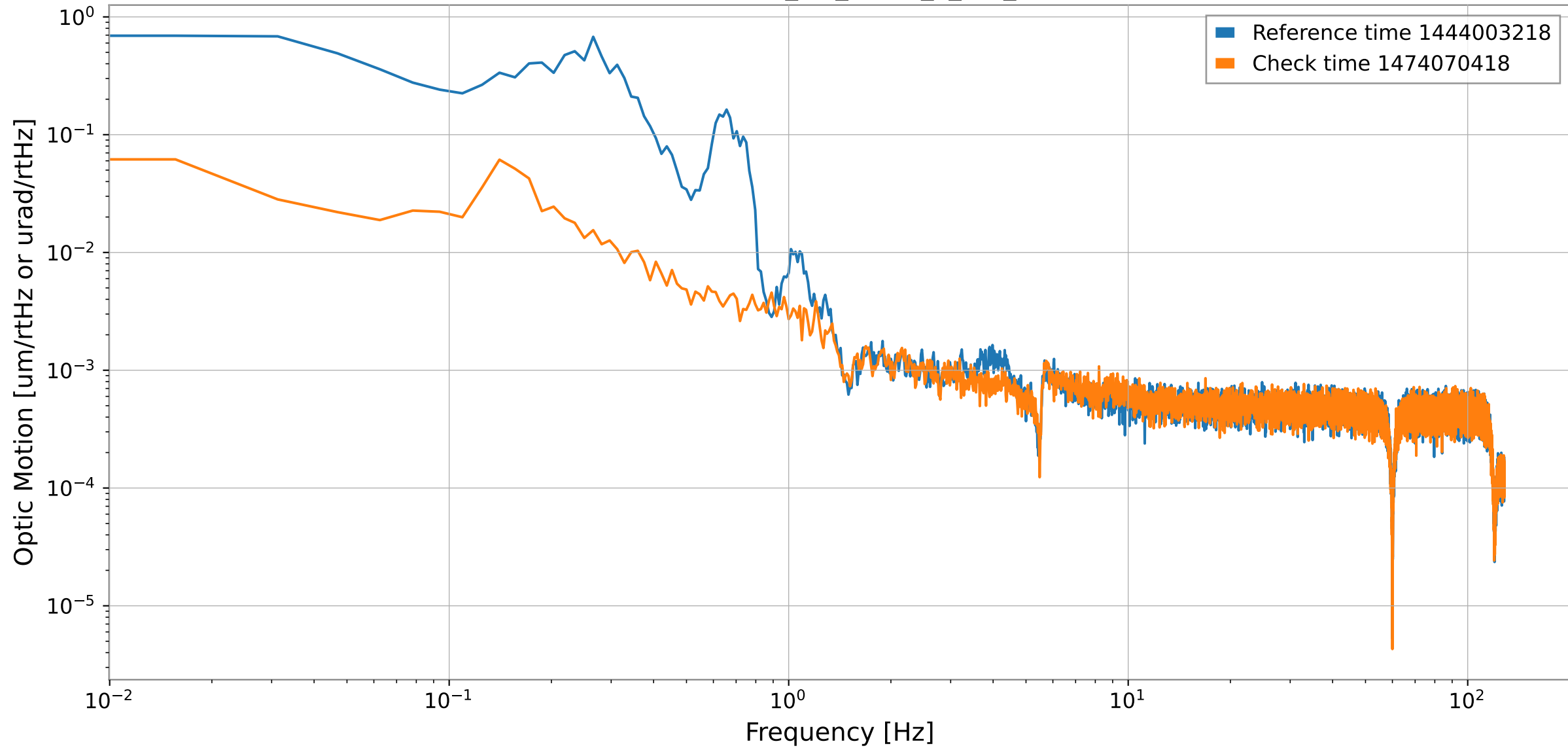


OM2

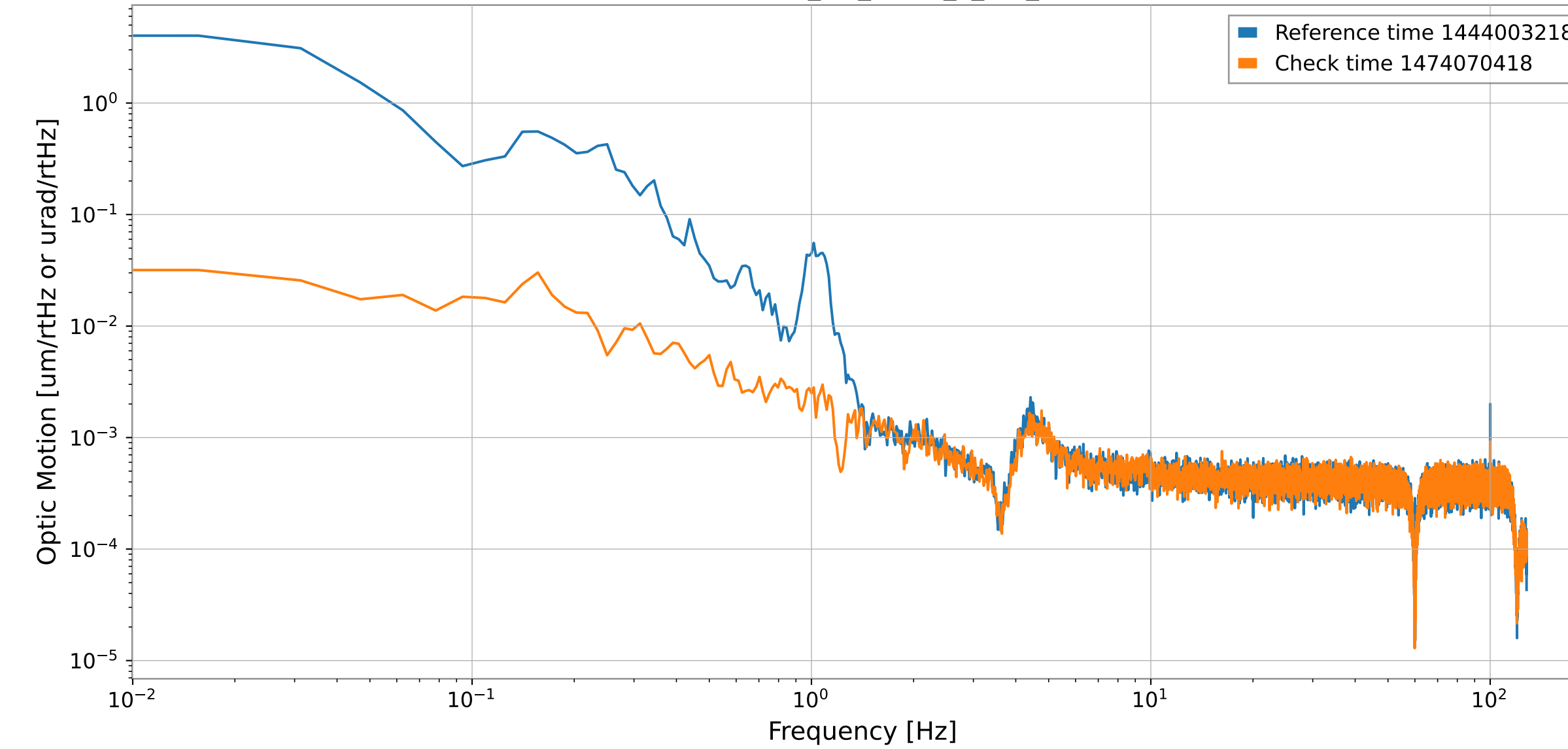
H1:SUS-OM2_M1_DAMP_L_IN1_DQ



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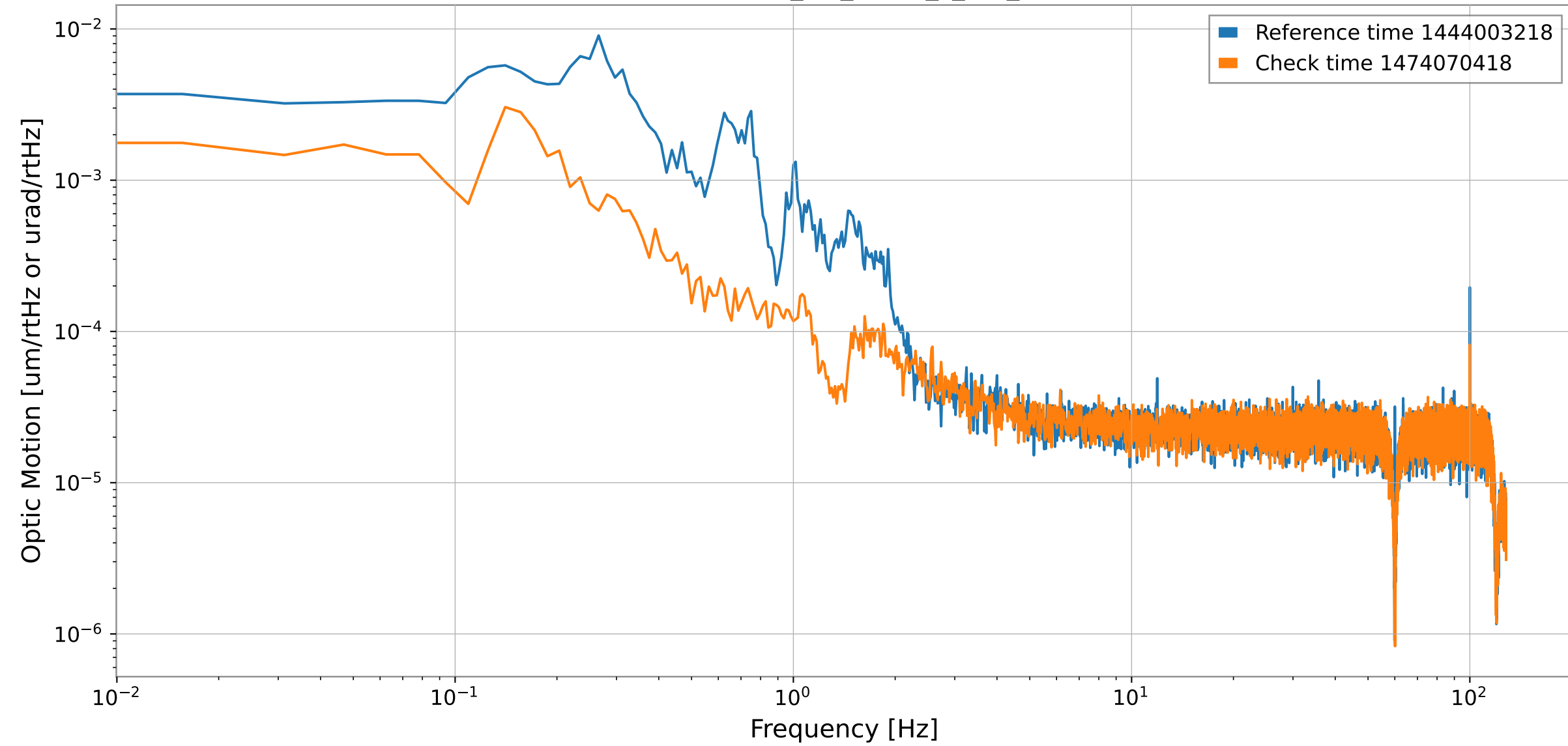


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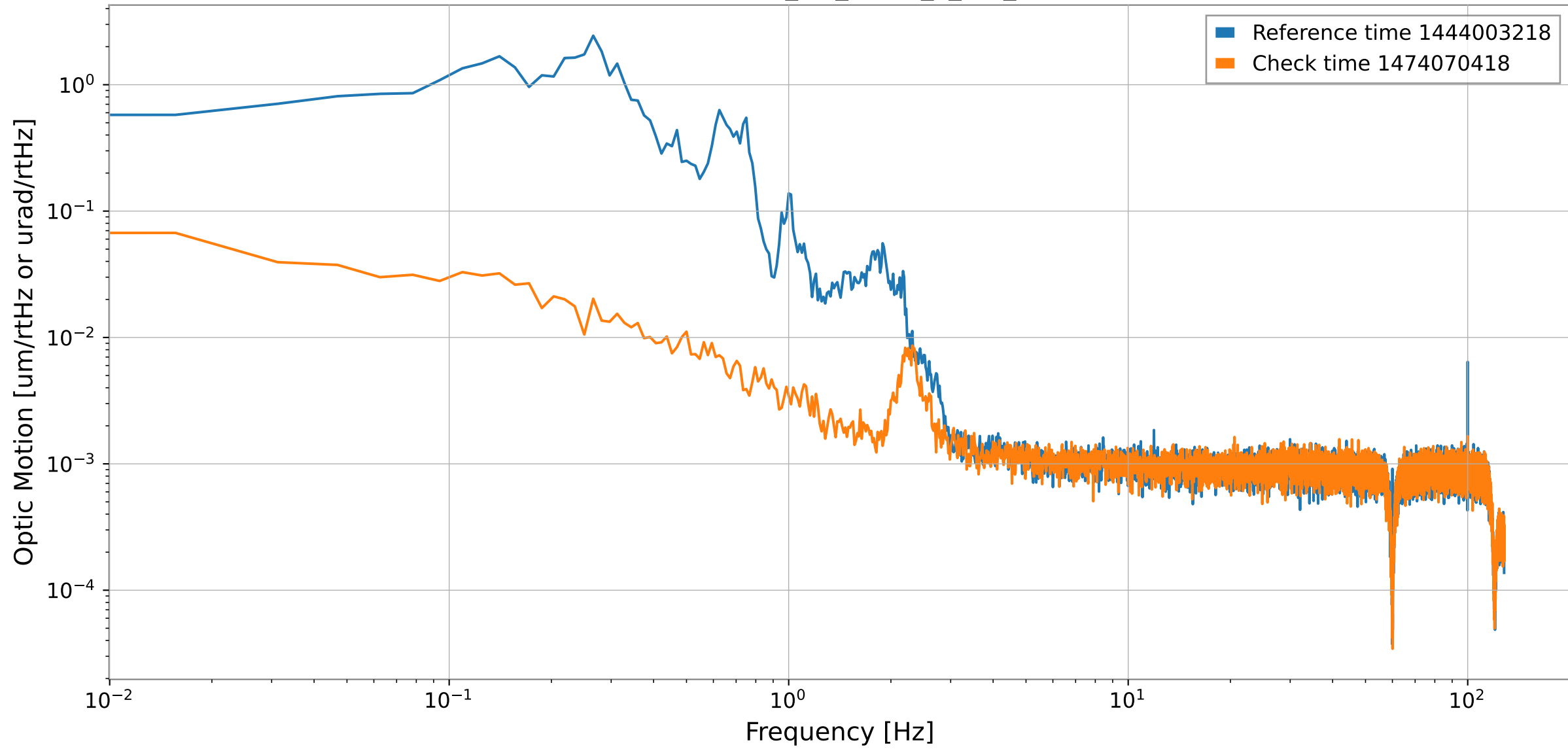


OM3

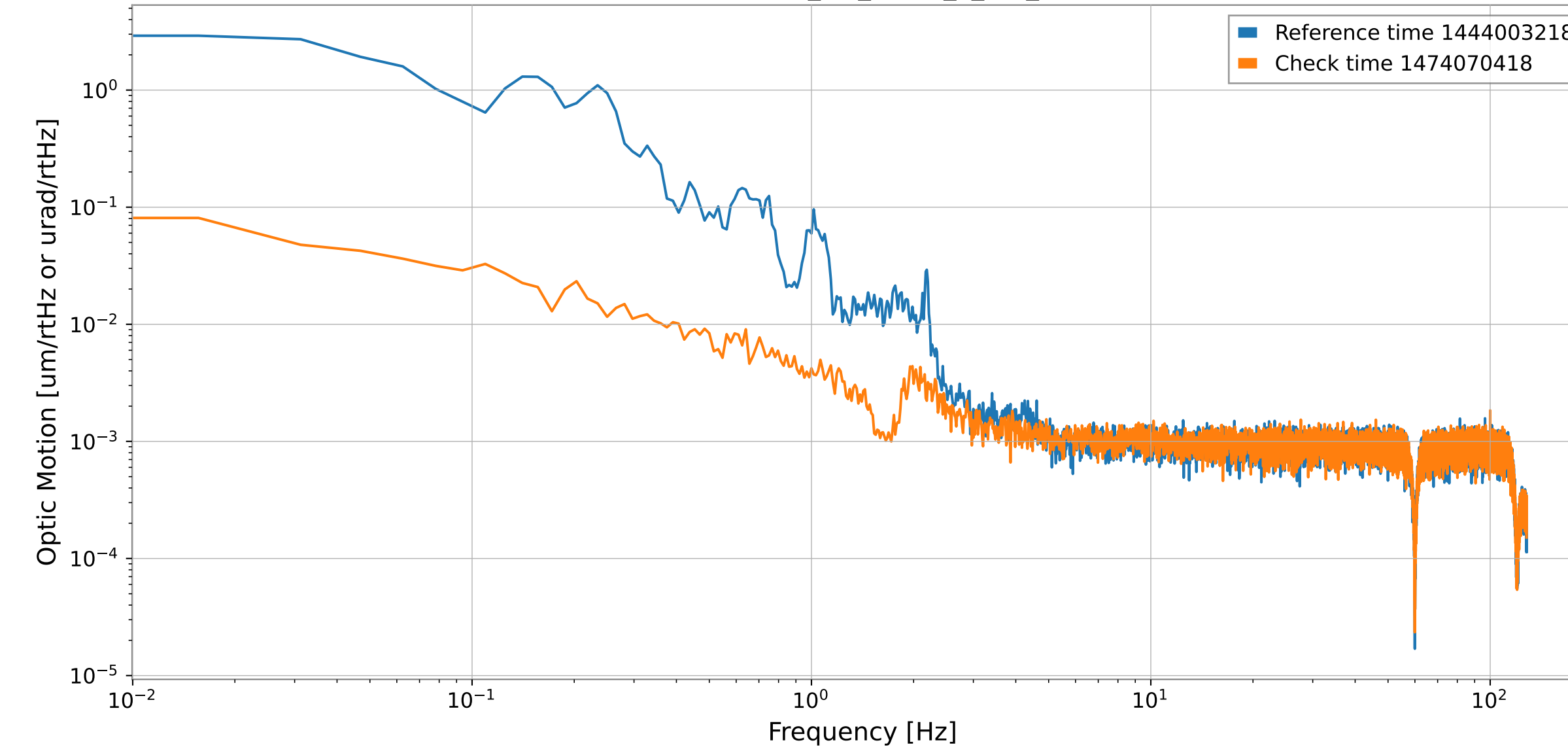
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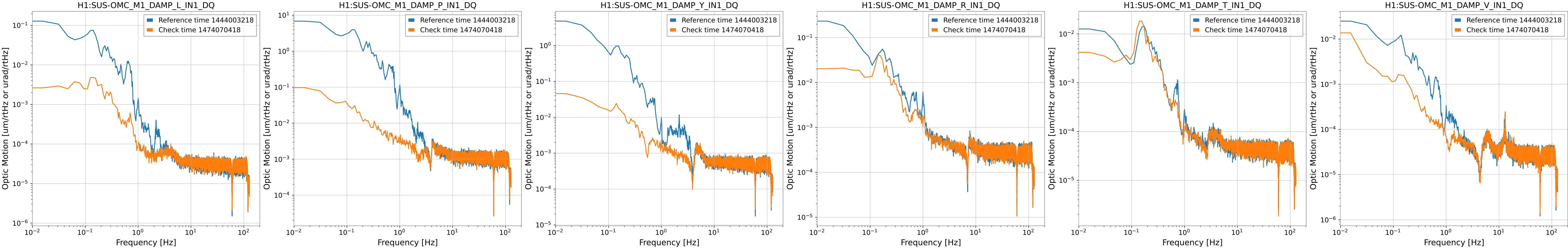
H1:SUS-OM3_M1_DAMP_P_IN1_DQ



H1:SUS-OM3_M1_DAMP_Y_IN1_DQ

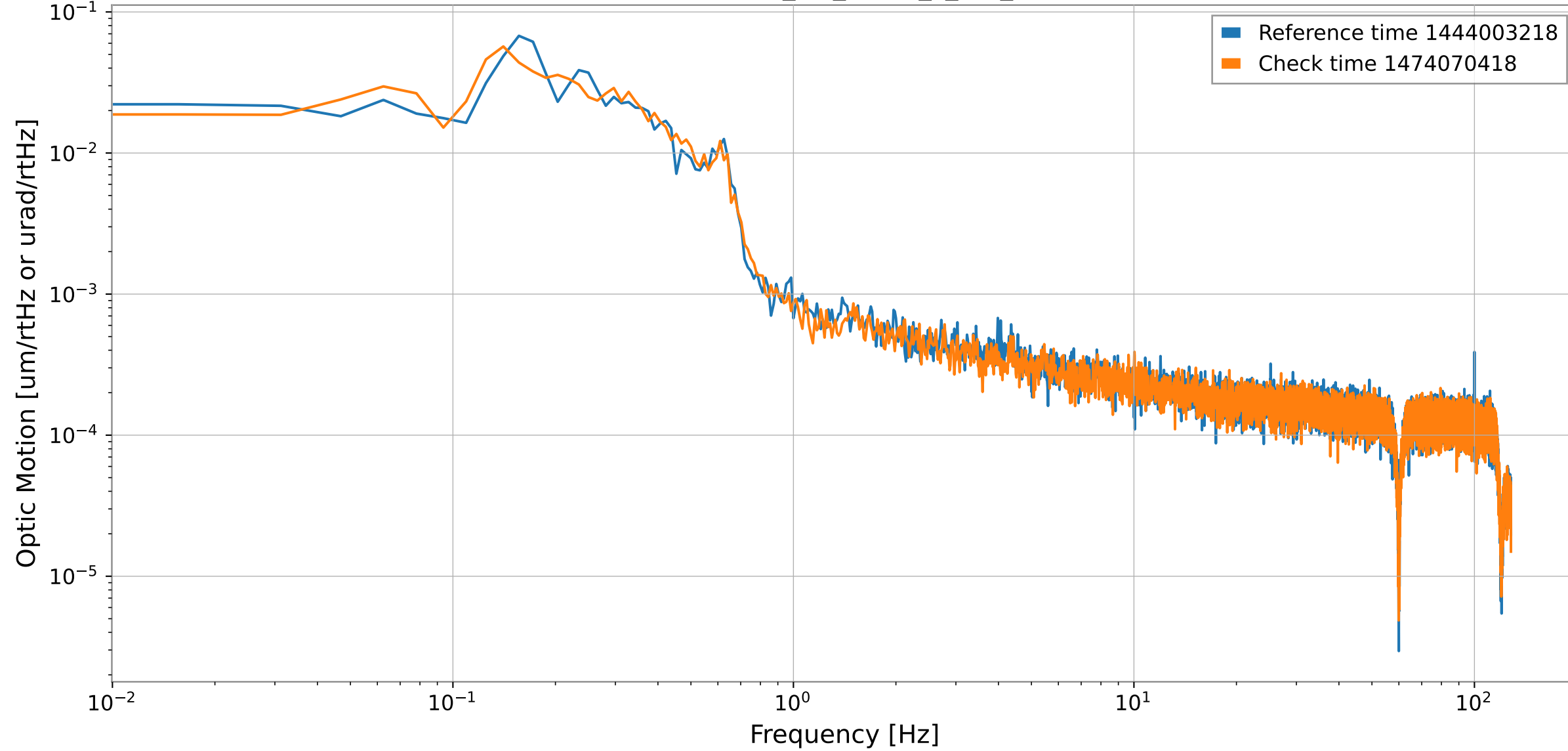


OMC

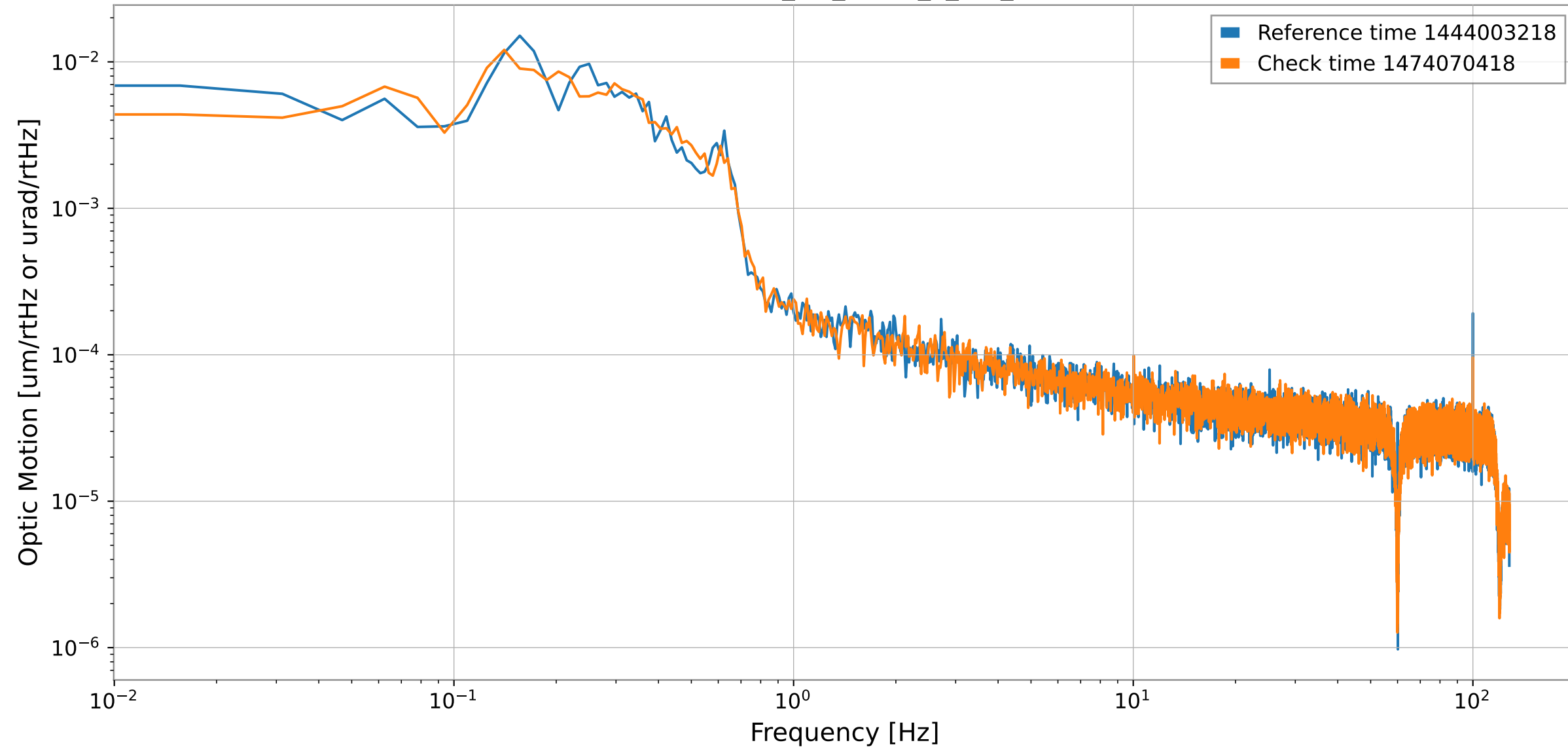


OFI

H1:SUS-OFI_M1_DAMP_L_IN1_DQ



H1:SUS-OFI_M1_DAMP_T_IN1_DQ



H1:SUS-OFI_M1_DAMP_Y_IN1_DQ

