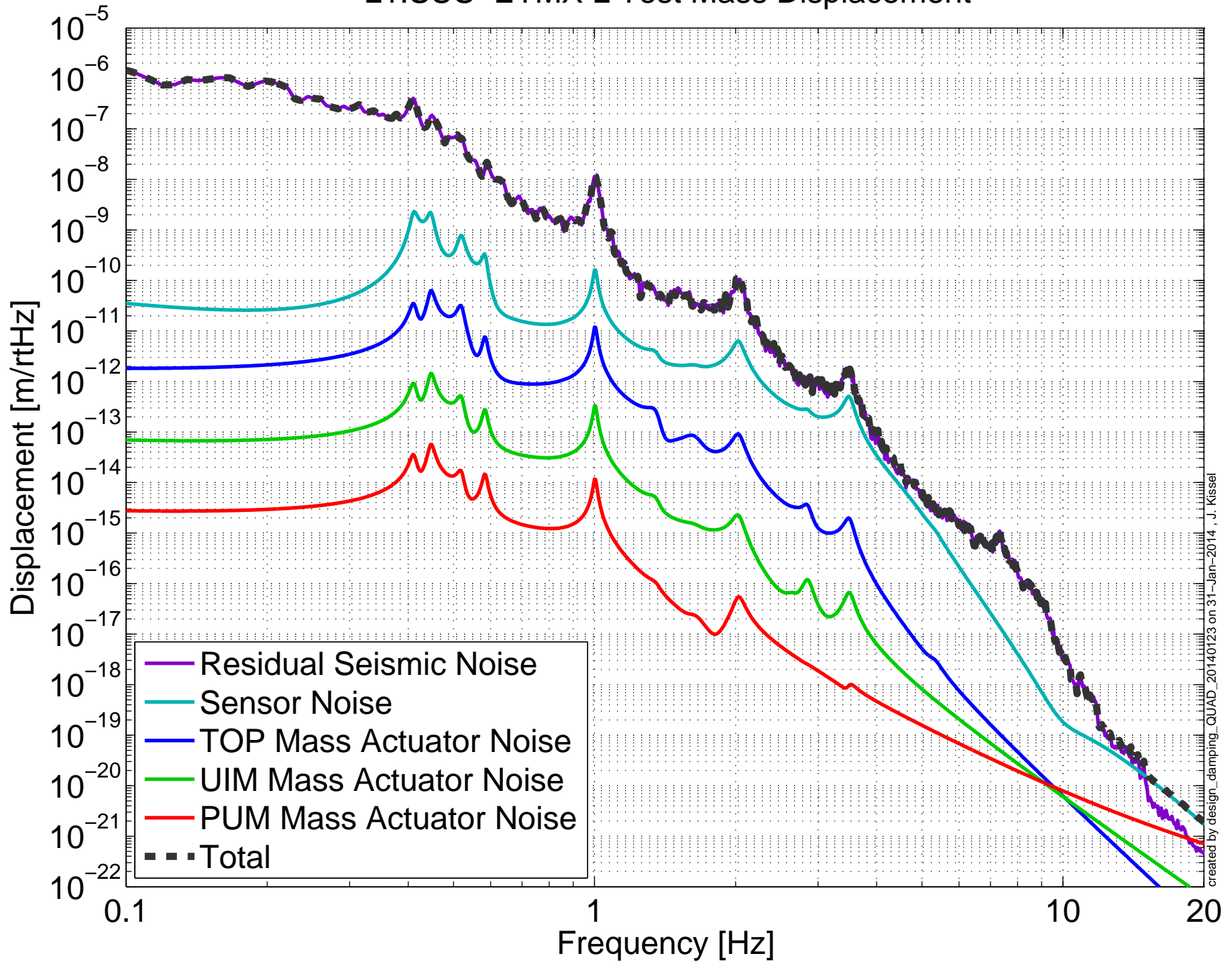


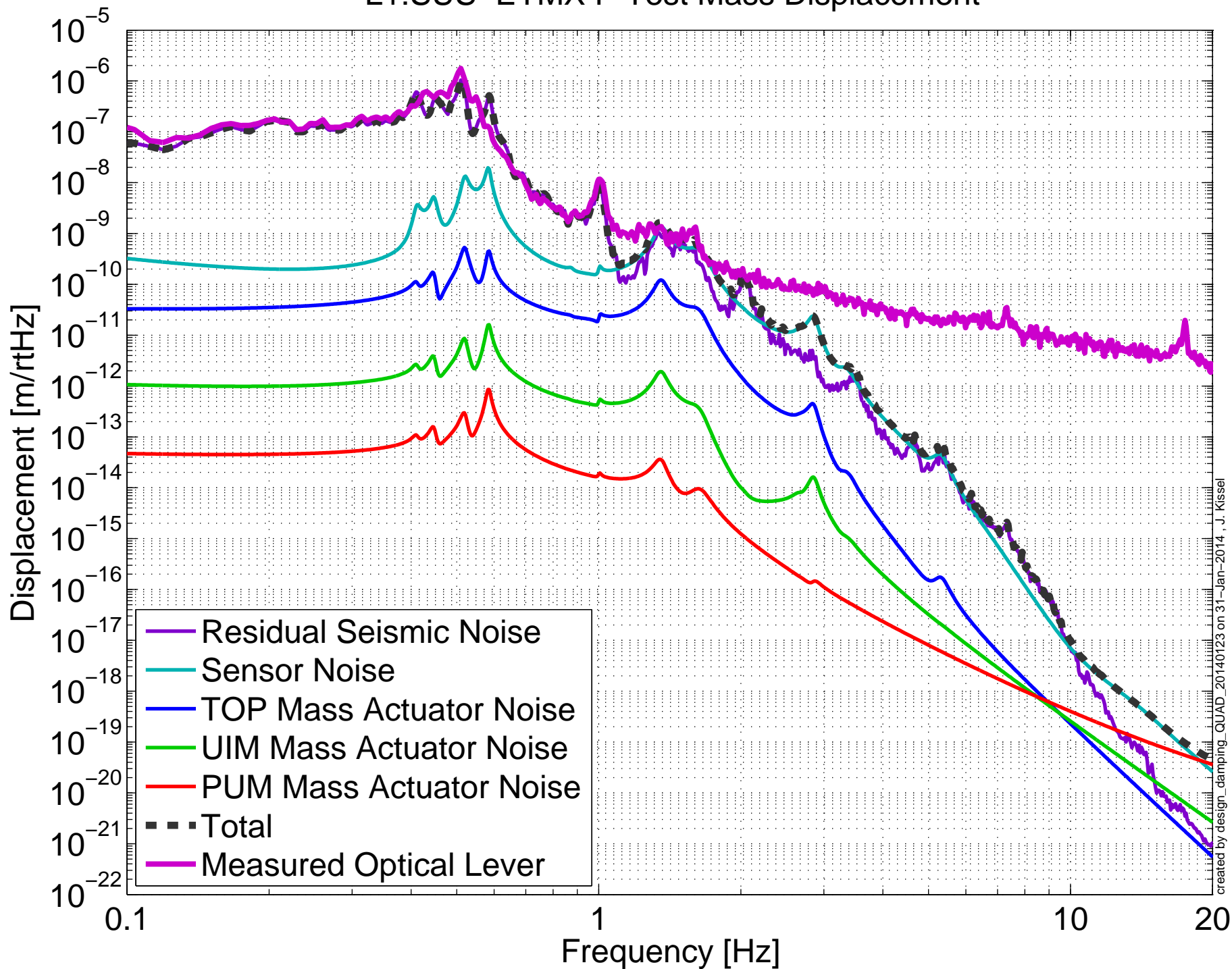
Damping Loop Performance

L1:SUS-ETMX L Test Mass Displacement



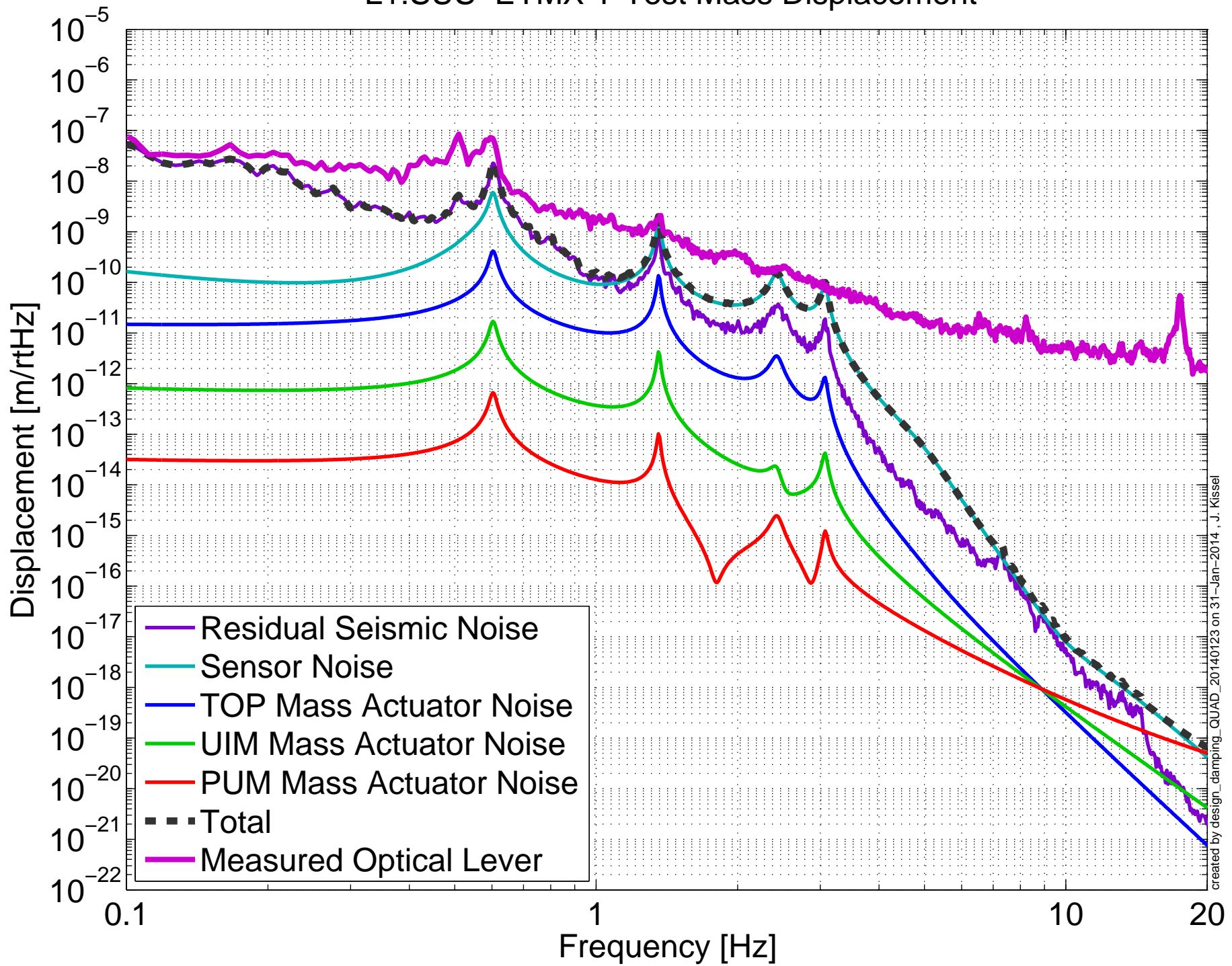
Damping Loop Performance

L1:SUS-ETMX P Test Mass Displacement



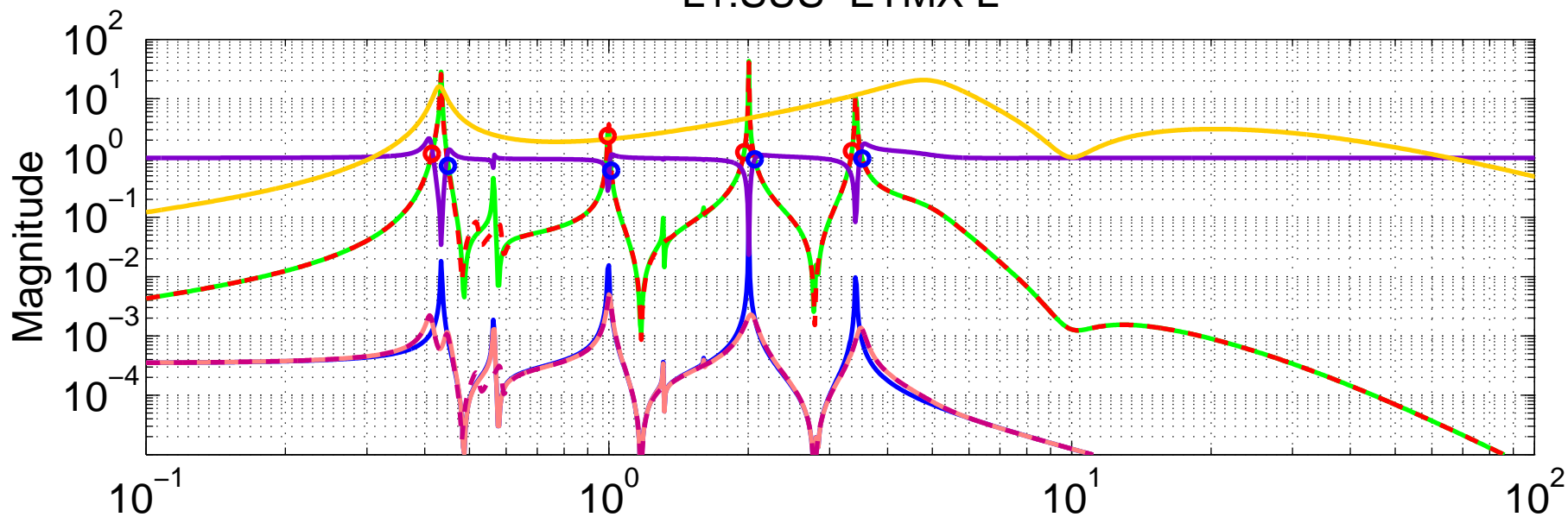
Damping Loop Performance

L1:SUS-ETMX Y Test Mass Displacement

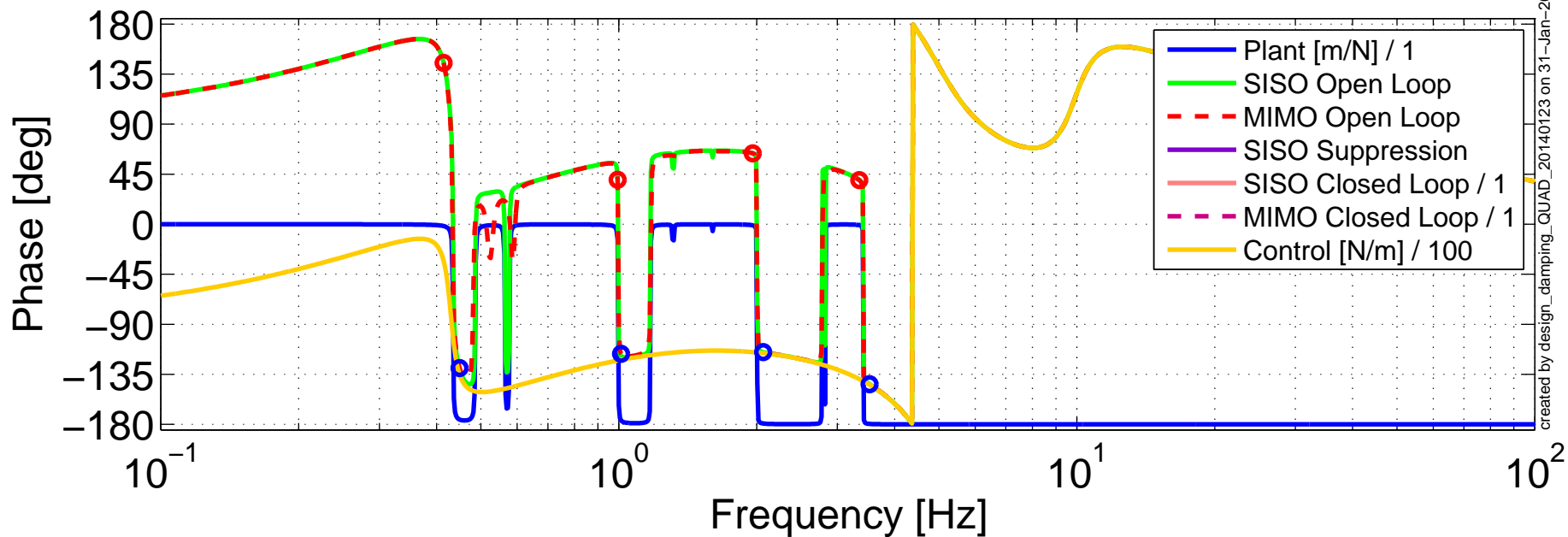


Damping Loop Design

L1:SUS-ETMX L

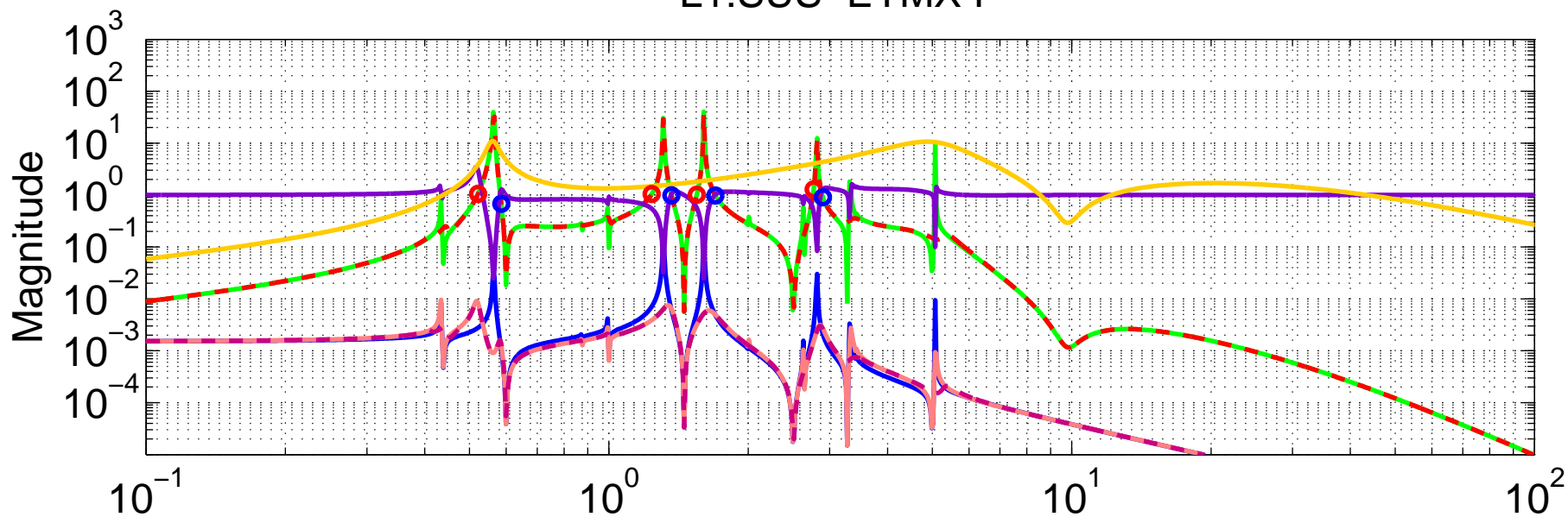


MIMO LUGF Phase Margins (red): [35 140 116 140] [deg]
MIMO UUGF Phase Margins (blue): [50.7 63.4 65 36.1] [deg]

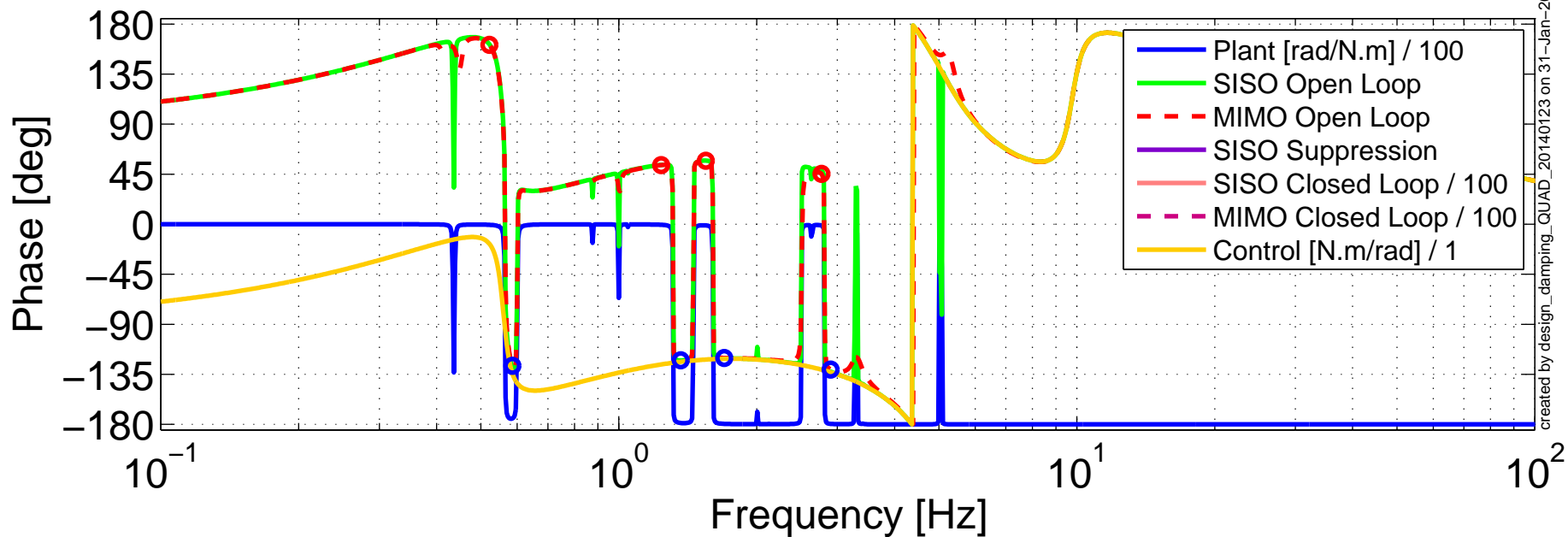


Damping Loop Design

L1:SUS-ETMX P



MIMO LUGF Phase Margins (red): [18.7 127 123 135] [deg]
MIMO UUGF Phase Margins (blue): [52.7 57.6 59.7 49.3] [deg]



created by design_damping_QUAD_20140123 on 31-Jan-2014, J. Kissel